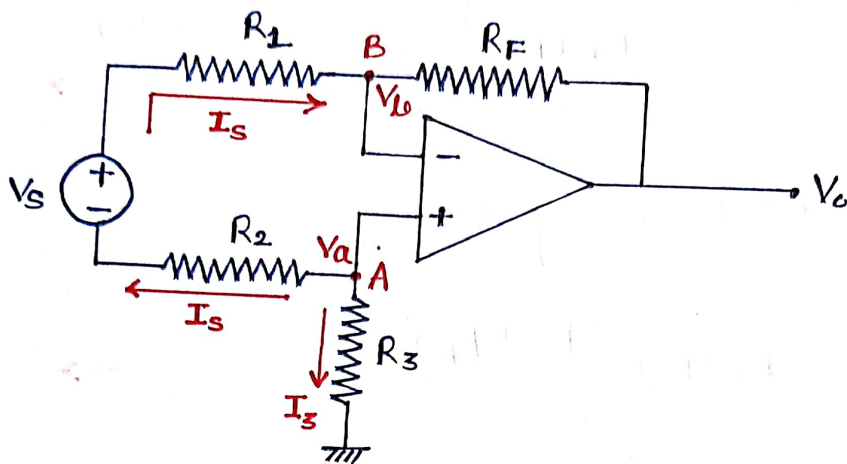


# BE QUIZ-2

## SOLUTION

SOL(1)÷



$$V_a = V_b$$

(Virtual short)

— (1)  
→ (0.25 Mark)

At node A,

$$I_S + I_3 = 0$$

(KCL at node A)

$$I_3 = -I_S$$

— (2)

$$V_a = I_3 R_3 = -I_S R_3$$

— (3)

→ (0.25 Mark)

At node B,

$$I_S = \frac{V_b - V_o}{R_F}$$

(KCL at node B)

$$I_S R_F = V_a - V_o$$

$$I_S R_F = -I_S R_3 - V_o$$

$$V_o = -I_S (R_3 + R_F)$$

— (4)

→ (1 Mark)

SOL (2)

Apply KVL between node A & node B, we get -

$$-V_a + I_s R_2 - V_s + I_s R_1 + V_b = 0$$

$$I_s = \left( \frac{V_s}{R_1 + R_2} \right) \quad \text{--- (5)}$$

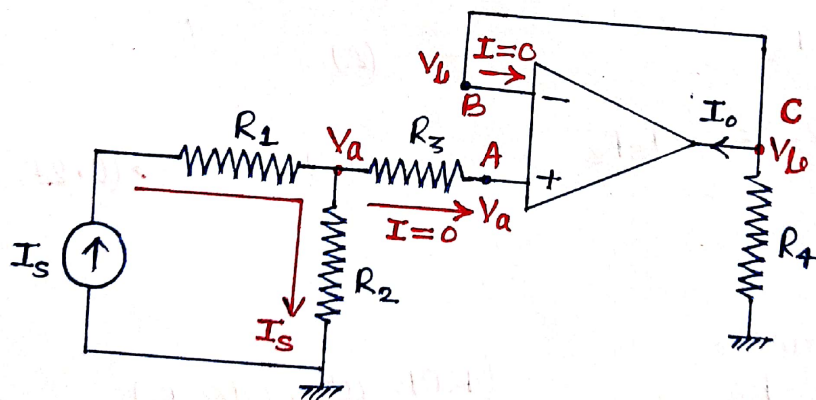
→ (1 Mark)

By eq<sup>n</sup> (4) & eq<sup>n</sup> (5), we get -

$$V_o = \frac{-V_s}{(R_1 + R_2)} (R_3 + R_4)$$

→ (0.50 Mark)

SOL (2) ÷



$$V_a = I_s R_2 \quad \text{--- (1)}$$

$$V_b = V_a = I_s R_2 \quad \text{--- (2)}$$

→ (0.5 Mark)

(Virtual Short) → (0.5 Mark)

At node-c,

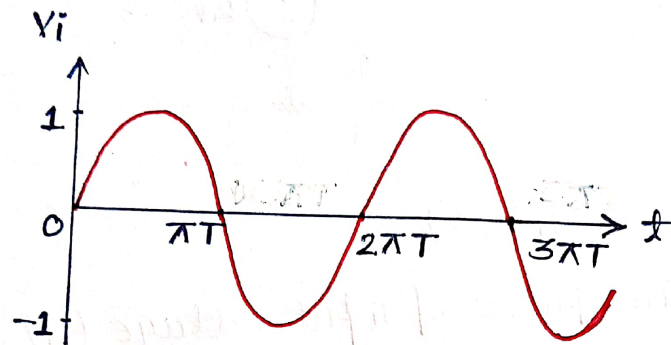
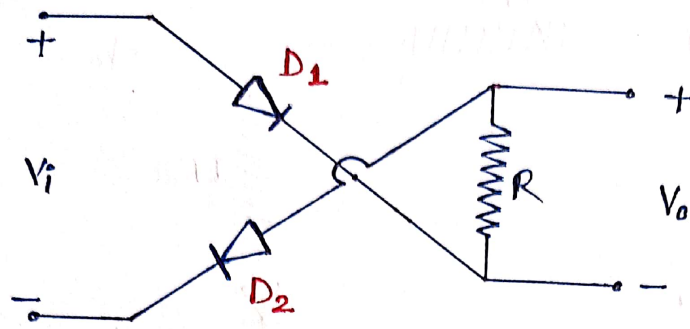
$$I_o = \frac{-V_b}{R_4} \quad (\text{KCL at node-c})$$

$$\therefore I_o = -\left( \frac{R_2}{R_4} \right) I_s$$

→ (1 Mark)



SOL(3):



→ (0.25 MARK)

Case(I):

During positive pulse of input voltage ( $V_i$ ), both diodes are ON (Forward biased). Hence —

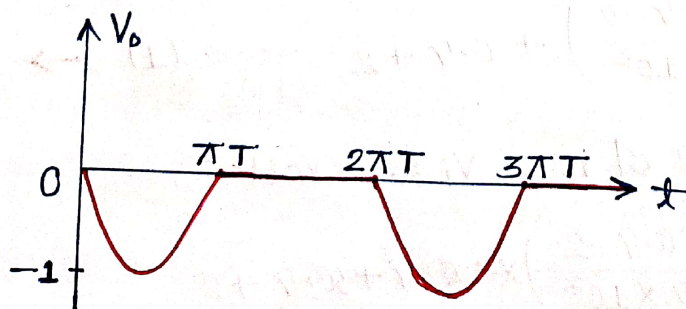
$$V_o = -V_i \quad \text{--- (1)}$$

→ (0.5 MARK)

Case (II): During negative pulse of input voltage ( $V_i$ ), both diodes are OFF (Reverse biased). Hence —

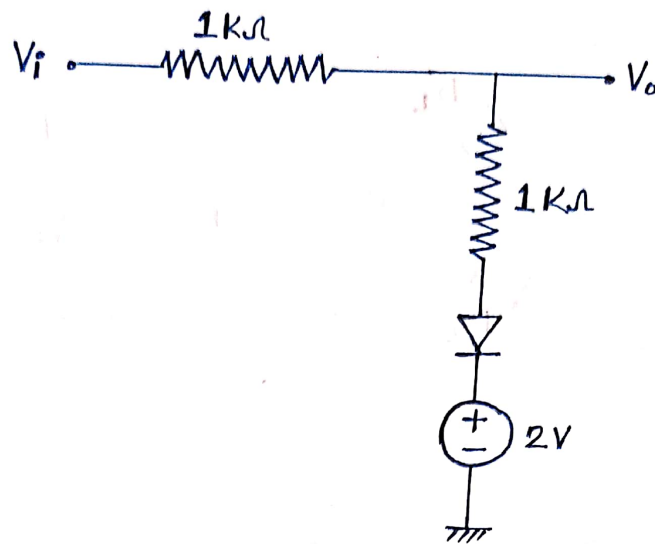
$$V_o = 0 \quad \text{--- (2)}$$

→ (0.5 MARK)



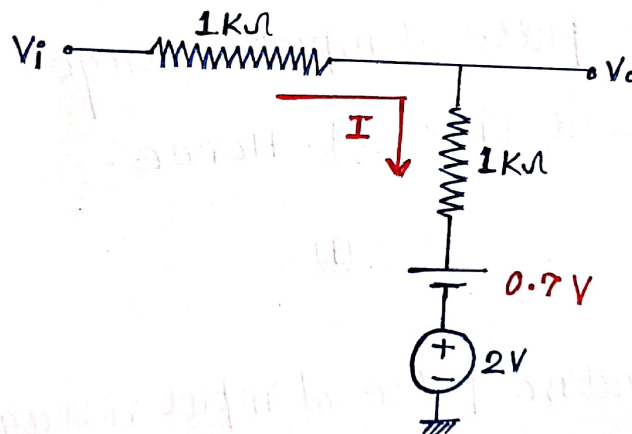
→ (0.75 MARK)

SOL(4)÷



Given that —  $V_i = 5 \sin(\omega t)$

Case(I): During positive pulse of input voltage ( $V_i$ ), diode is ON (forward bias)



Here,  $V_o = I \times 1 \times 10^3 + 0.7 + 2$

$$V_o = 1 \times 10^3 \times \left( \frac{V_i - 0.7 - 2}{2 \times 10^3} \right) + 0.7 + 2 \quad \text{--- (1) } \rightarrow (0.5 \text{ MARK})$$

For maximum value of  $V_o$ ,  $V_i = 5 \text{ Volt}$ .

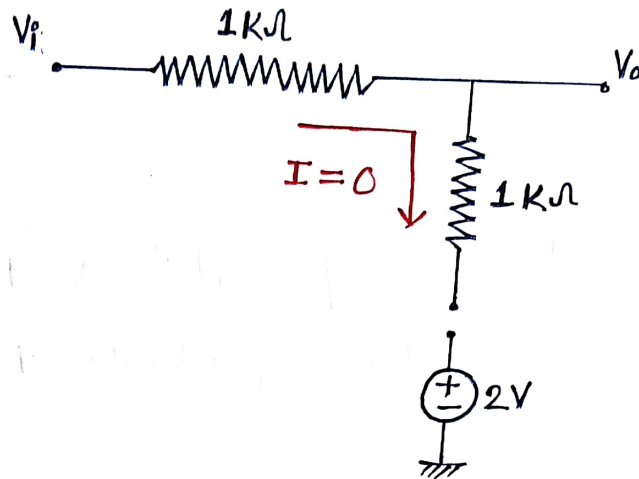
$$\therefore V_o = \left( \frac{5 - 0.7 - 2}{2 \times 10^3} \right) \times 1 \times 10^3 + 0.7 + 2$$

$$\therefore (V_o)_{\text{maximum}} = 3.85 \text{ Volt}$$

$\rightarrow (1 \text{ MARK})$



Case (II): During negative pulse of input voltage ( $V_i$ ), diode is OFF (reverse bias).



Hence,  $V_o = V_i$

→ (0.5 Mark)

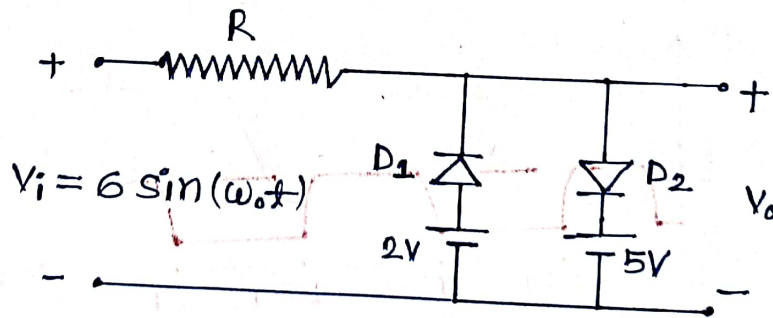
For minimum value of  $V_o$ ,  $V_i = -5$  Volt

∴  $(V_o)_{\text{minimum}} = -5$  Volt

→ (1 Mark)

SOL(5)÷

(a)



Case (I): If input voltage ( $V_i$ ) < 2V then diode  $D_1$  is ON (forward bias) & diode  $D_2$  is OFF (reverse bias).

Hence,  $V_o = 2$  Volt

— (1)

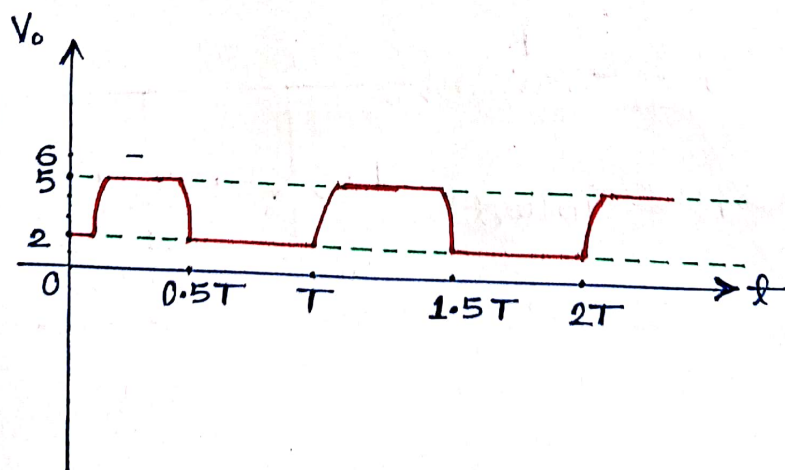
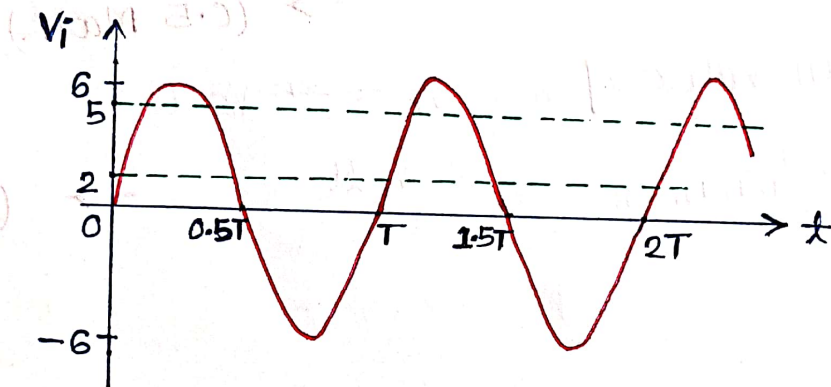
→ (0.75 Mark)

Case (II): If  $2V < \text{input voltage}(V_i) < 5V$  then <sup>both</sup> diode  $D_1$  &  $D_2$  is OFF (reverse bias)

Here,  $V_o = V_i$  — (2)  $\rightarrow$  (0.75 Mark)

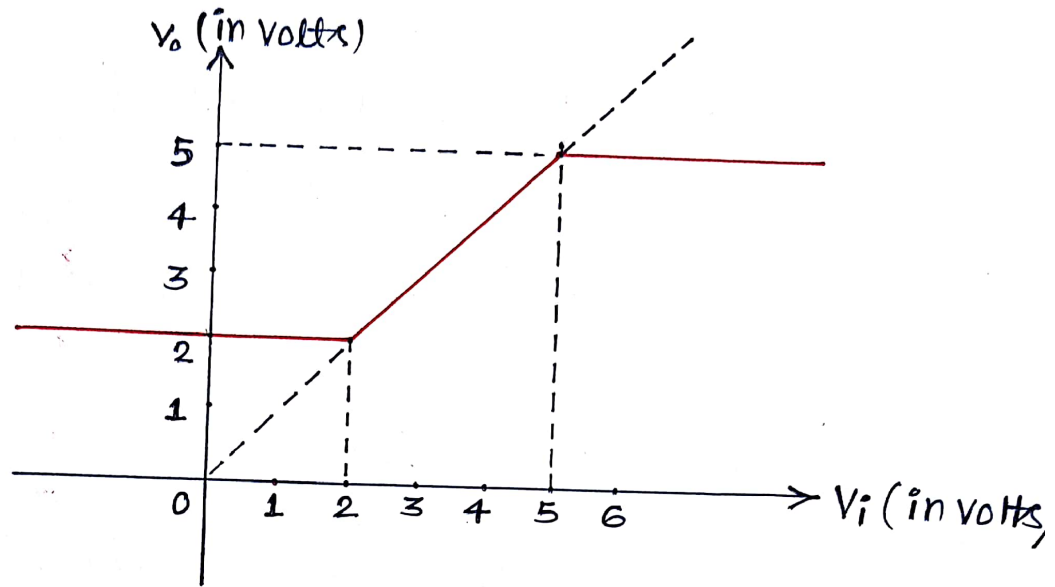
Case (III): If input voltage  $(V_i) > 5V$  then diode  $D_1$  is OFF (reverse bias) & diode  $D_2$  is ON (forward bias).

Here,  $V_o = 5V$  — (3)  $\rightarrow$  (0.75 Mark)



$\rightarrow$  (0.75 Mark)

SOL(5)(b) ÷ By case(I), case(II) & case(III), we get —



→ (1 Mark)