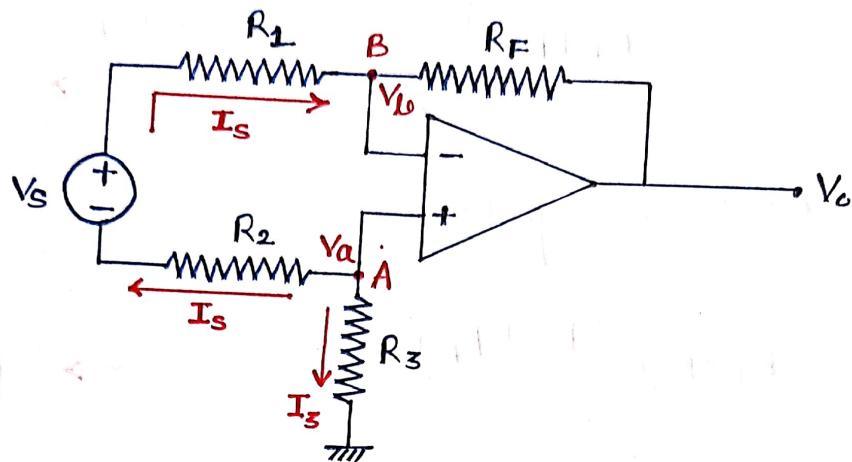


BE QUIZ-2
SOLUTION

SOL(1) :-



$$V_A = V_B$$

(Virtual short)

— (1)
→ (0.25 MARK)

At node A,

$$I_S + I_3 = 0 \quad (\text{KCL at node A})$$

$$I_3 = -I_S \quad — (2)$$

$$V_A = I_3 R_3 = -I_S R_3 \quad — (3)$$

→ (0.25 MARK)

At node B,

$$I_S = \frac{V_B - V_o}{R_F} \quad (\text{KCL at node B})$$

$$I_S R_F = V_A - V_o$$

$$I_S R_F = -I_S R_3 - V_o$$

$$V_o = -I_S (R_3 + R_F)$$

— (4) → (1 MARK)

Apply KVL between node A & node B, we get -

$$-V_a + I_s R_2 - V_s + I_s R_1 + V_b = 0$$

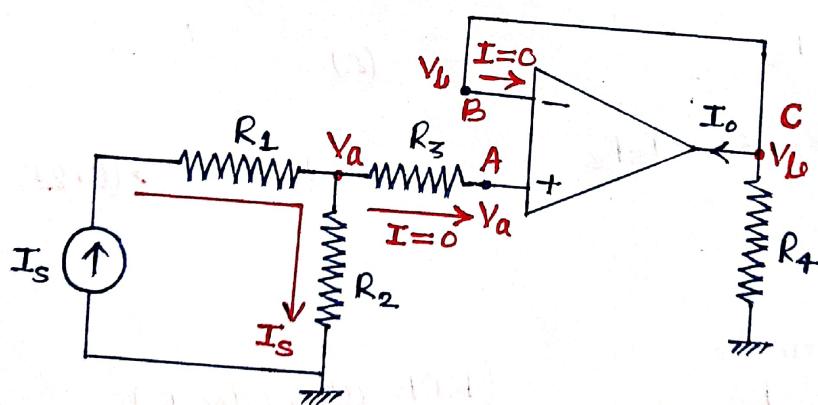
$$I_s = \left(\frac{V_s}{R_1 + R_2} \right) \quad \rightarrow (5)$$

$\rightarrow (1 \text{ MARK})$

By eqn (4) & eqn (5), we get -

$$V_o = \frac{-V_s}{(R_1 + R_2)} (R_3 + R_F) \quad \rightarrow (0.5 \text{ MARK})$$

SOL (2) :-



$$V_a = I_s R_2 \quad \rightarrow (1)$$

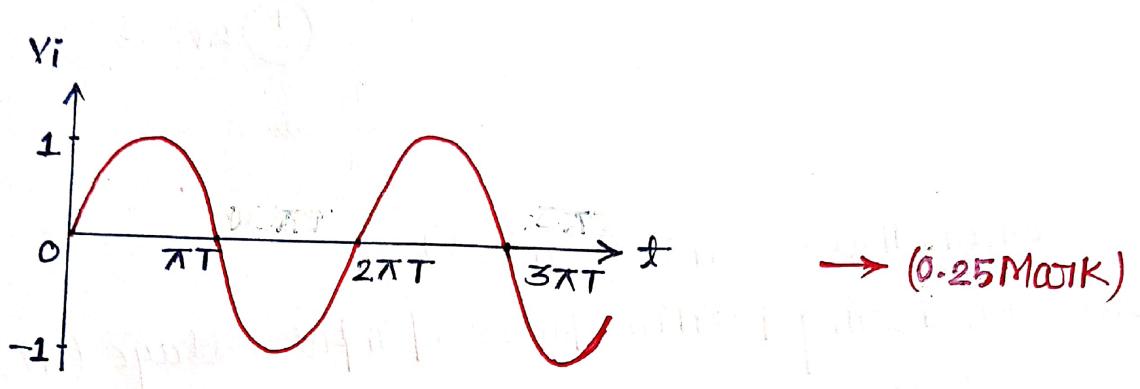
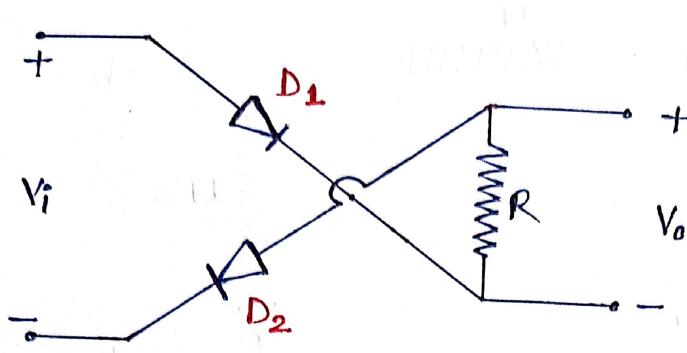
$$V_b = V_a = I_s R_2 \quad \rightarrow (2) \quad (\text{virtual short}) \rightarrow (0.5 \text{ MARK})$$

At node-C,

$$I_o = \frac{-V_b}{R_4} \quad (\text{KCL at node-C})$$

$$\therefore I_o = -\left(\frac{R_2}{R_4}\right) I_s \quad \rightarrow (1 \text{ MARK})$$

SOL(3) :-



Case(I) :-

During positive pulse of input voltage (V_i), both diodes are ON (Forward biased). Hence -

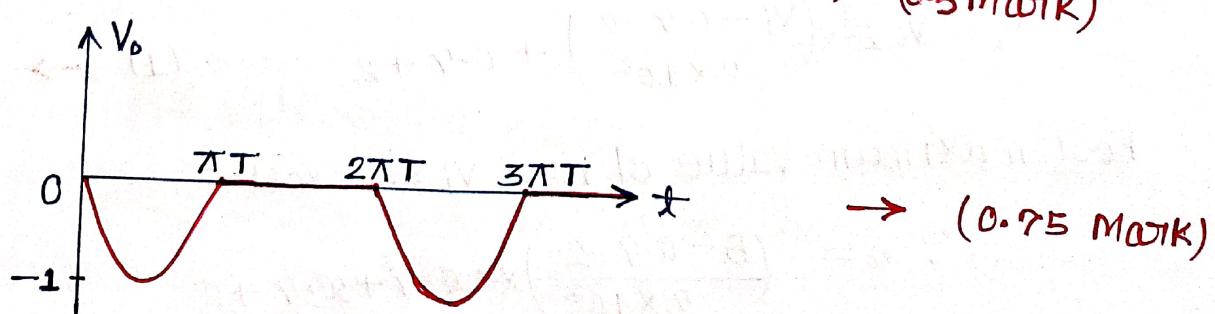
$$V_o = -V_i \quad \text{--- (1)}$$

→ (0.5 MARK)

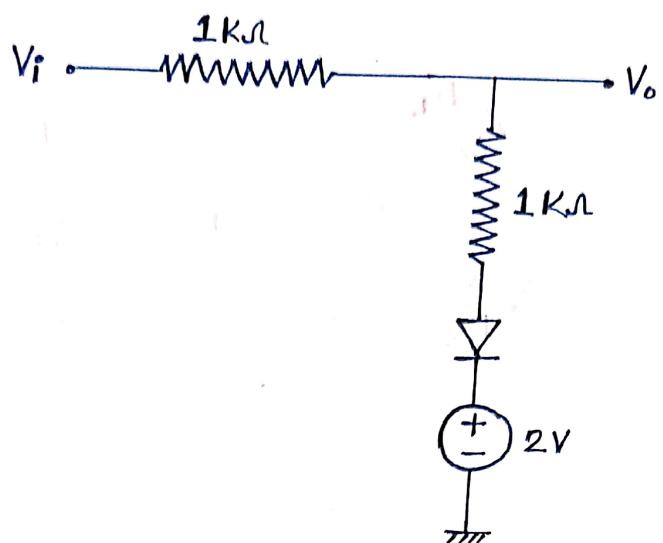
Case (II): During negative pulse of input voltage (V_i), both diodes are OFF (Reverse biased). Hence -

$$V_o = 0 \quad \text{--- (2)}$$

→ (0.5 MARK)

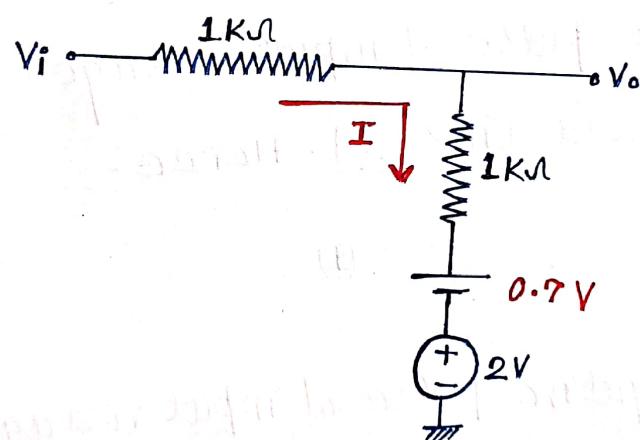


SOL(4) :-



Given that — $V_i = 5 \sin(\omega t)$

Case(I): During positive pulse of input voltage (V_i), diode is ON (forward bias)



$$\text{Here, } V_o = I \times 1 \times 10^3 + 0.7 + 2$$

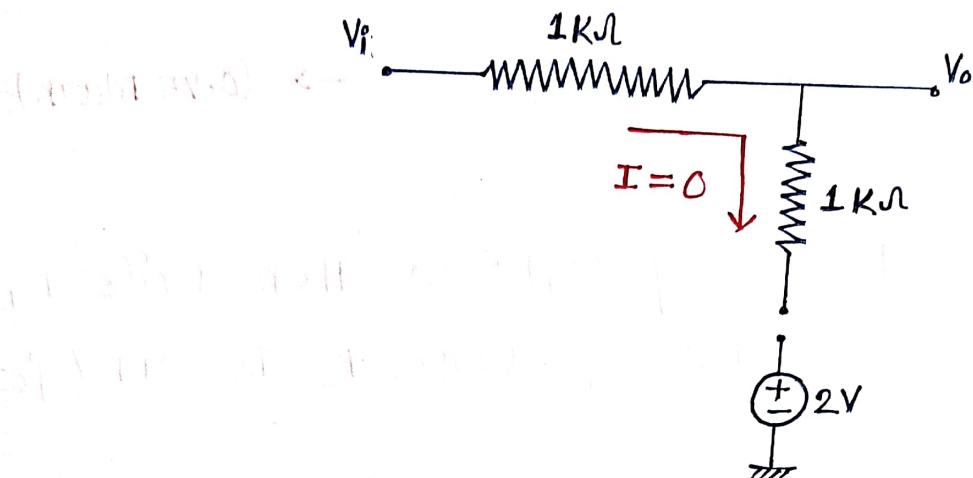
$$V_o = 1 \times 10^3 \times \left(\frac{V_i - 0.7 - 2}{2 \times 10^3} \right) + 0.7 + 2 \quad \text{--- (1)} \rightarrow (0.5 \text{ Mark})$$

For maximum value of V_o , $V_i = 5 \text{ volt}$.

$$\therefore V_o = \left(\frac{5 - 0.7 - 2}{2 \times 10^3} \right) \times 1 \times 10^3 + 0.7 + 2$$

$$\therefore (V_o)_{\text{maximum}} = 3.85 \text{ volt} \quad \rightarrow (1 \text{ Mark})$$

Case (II): During negative pulse of input voltage (V_i), diode is OFF (reverse bias).



$$\text{Hence, } V_o = V_i$$

→ (0.5 Mark)

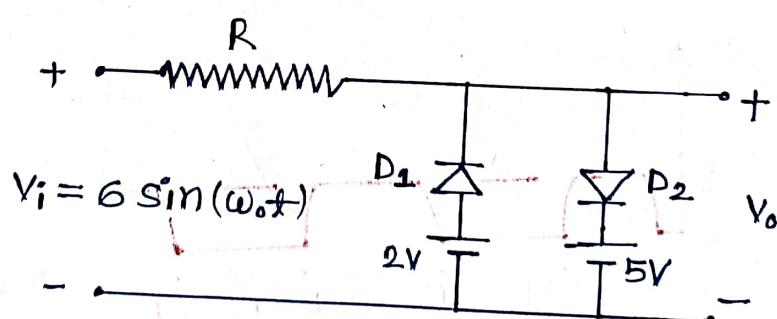
For minimum value of V_o , $V_i = -5 \text{ Volt}$

$$\therefore (V_o)_{\text{minimum}} = -5 \text{ Volt}$$

→ (1 Mark)

SOL(5)%

(a)



Case (I): If input voltage (V_i) < 2V then diode D_1 is ON (forward bias) & diode D_2 is OFF (reverse bias).

$$\text{Hence, } V_o = 2 \text{ Volt}$$

— (1)

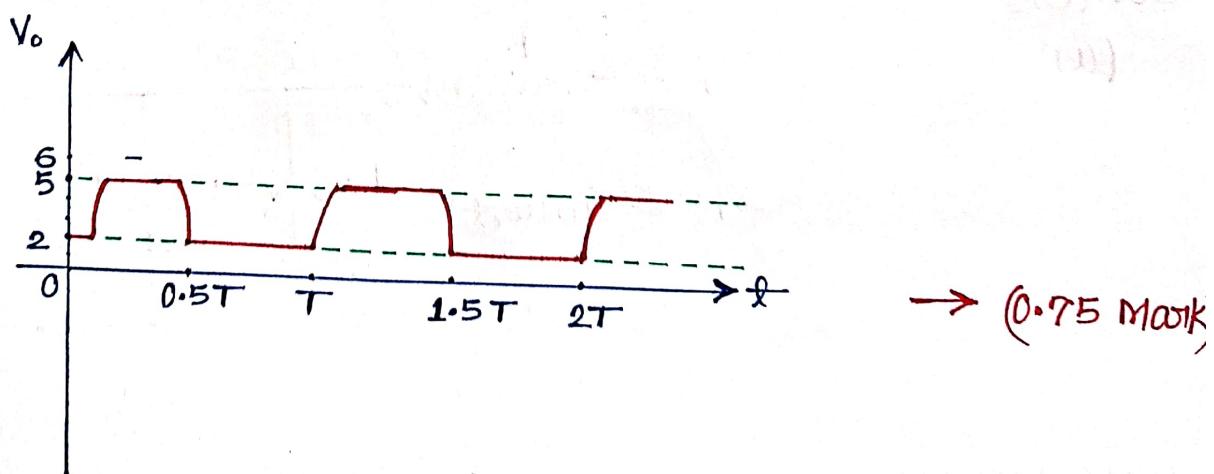
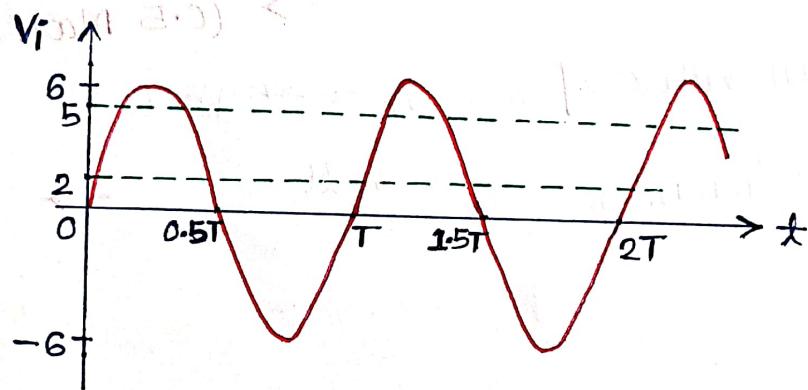
→ (0.75 Mark)

case(II): If $2V < \text{input voltage } (V_i) < 5V$ then ^{both} diode $D_1 \& D_2$ is OFF (reverse bias)

$$\text{Hence, } V_o = V_i \quad \text{--- (2)} \quad \rightarrow (0.75 \text{ MARK})$$

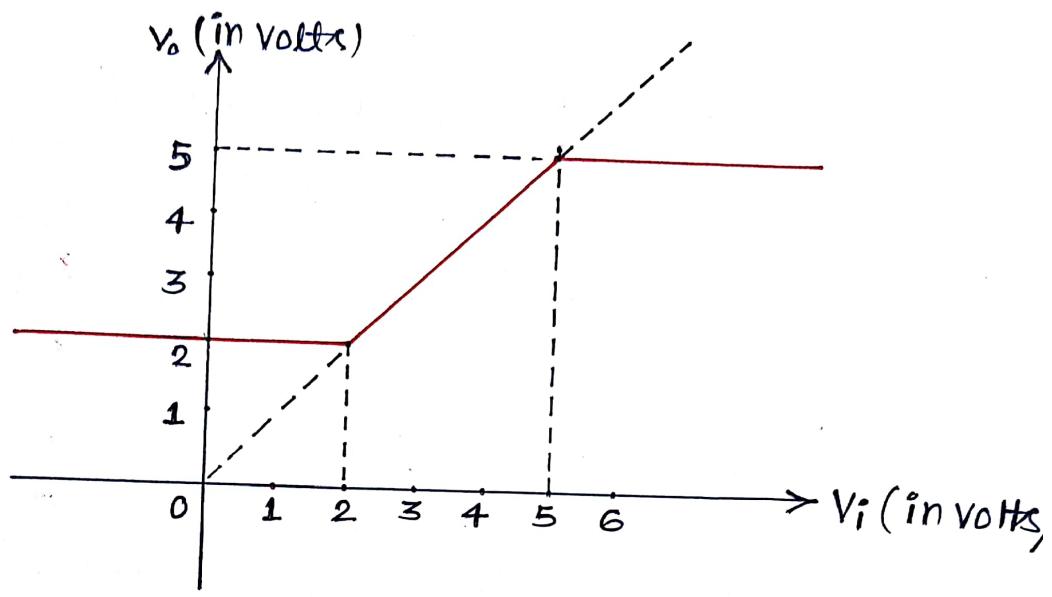
Case(III): If input voltage (V_i) $> 5V$ then diode D_1 is OFF (reverse bias) & diode D_2 is ON (forward bias).

$$\text{Hence, } V_o = 5V \quad \text{--- (3)} \quad \rightarrow (0.75 \text{ MARK})$$



$\rightarrow (0.75 \text{ MARK})$

SOL(5)(b) \div By case(I), case(II) & case(III), we get -



\rightarrow (1 MARK)