ECE Lab Guidelines
Digital Circuits

Guidelines to be followed strictly in DC Lab sessions

1. Entry is allowed in the lab at 1:00 pm (after your TAs arrive). No entry after 1.15 pm.
2. Briefing will start at sharp 1:05 pm.
3. After the briefing is done, students will get a 10 min freeze time, where they will make the circuit without any TA assistance.
4. Lab handout will be shared in advance. You are suggested to go through it once before coming to your lab, so that you have an idea what will be covered in the lab.
5. Mobile phones and laptops are strictly prohibited inside the labs.
6. Any food/drink items except water is not allowed.
7. There will be no extra time slot provided for lab practice, so utilize your lab timings for learning.
8. Don't write your lab report during the lab session, as this time is exclusively dedicated to conducting experiments and practical work. Utilize the entire lab session solely for experimental purposes.
9. Ensure that you have individually completed your lab reports for the previous lab sessions. Do not share your lab files with anyone, including your lab partner, as any **similarities will be regarded as plagiarism**.
10. Prior to commencing the lab experiment, **ensure to submit your lab file to the TAs**. Any failure to do so will result in non-evaluation, and your submission will be regarded as if no lab file was submitted.
11. The final lab exam will be performed individually, so utilize the lab time for your learning even if you are working in a group.
12. **Makeup lab sessions** will be allowed only to students with medical leave that has been officially **approved by the academic section.**
13. Attendance will be considered only when the provided resources are at their assigned place at the end of the lab session.

Penalties :-

(-1 for each of the below point)

1. Damaging the components (ICs, LEDs, etc)
2. Misbehaving with the TAs, Research Engineers, colleagues
3. Any type of indiscipline.
4. Using the phone/leaving the lab without permission.
5. Not keeping the components in its place/ leaving the resources switched ON/ not arranging chairs before leaving the room/ not cleaning your table.
6. Sharing/shifting of resources without permission of Research Engineer
7. Missing any component in the lab file (Index, Aim, Components required, Circuit diagram, Experimental results, Conclusion)

(-5 for the below point)

1. Failure to submit the lab file

(Need to purchase the below items if damaged by you)

1. Damaging high end resources like Power Supply, Multimeter, Digital Trainer Kit

What to submit :-

1. You are supposed to write the observations in a rough notebook and get it verified by the TAs during the lab session.
2. Make a small DC lab file (one side blank, one side ruled) and write all the points as mentioned below:
   a. Aim
   b. Components required
   c. Circuit diagram (on left side black page)
   d. Observation Table/Experimental results
   e. Conclusion

The completed lab file will be checked in the next lab session. You need to submit it to your TAs after entering the lab.

Grading :-

Each Lab session is of 10 marks:

(i) **Lab Attendance** :5 marks
(ii) **Lab Experiment**: 5 marks

Contact the respective Research Engineers for any lab-related queries

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