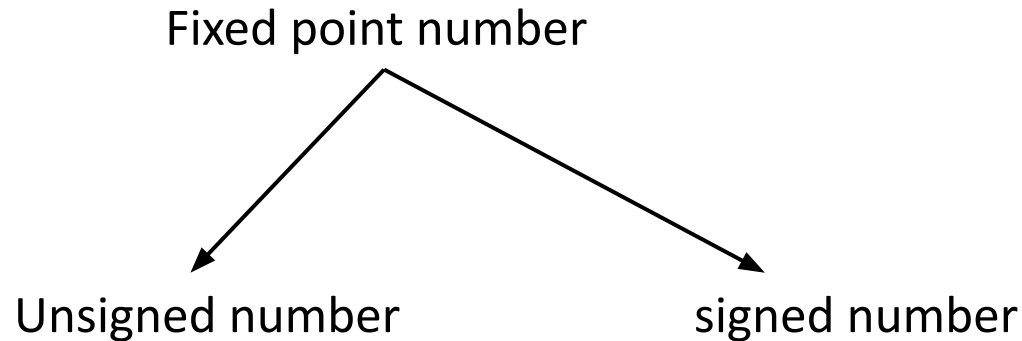


Fixed Point Number Representation



In case of unsigned number, the range of n bit number is
0 to $2^n - 1$

For 4 bit number the range is
0 to 15

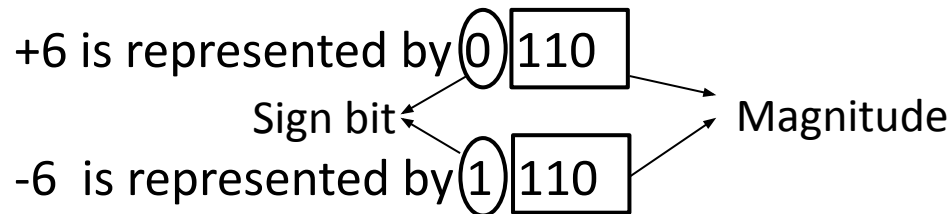
For 8 bit number the range is
0 to 255

Signed number representation:

1. Signed-magnitude representation
2. 1's complement representation
3. 2's complement representation

Signed-magnitude representation

In sign magnitude representation, if the number is positive then sign bit is 0 and if the number is negative then sign bit is 1.



In sign magnitude representation, the range of n bit number is $-(2^{n-1}-1)$ to $+(2^{n-1}-1)$

For 4 bit number the range is

-7 to +7

For 8 bit number the range is

-127 to +127

There are two different representation of 0 (+0 and -0).

4 bit representation of +0 is 0000

4 bit representation of -0 is 1000

1's complement representation

In 1's complement representation, Positive number is represented by same as Sign magnitude but negative number is the 1's complement of positive number.

+6 is represented by 0 110

-6 is represented by 1 001

In 1's complement representation, the range of n bit number is

$$-(2^{n-1}-1) \text{ to } +(2^{n-1}-1)$$

For 4 bit number the range is

-7 to +7

For 8 bit number the range is

-127 to +127

There are two different representation of 0 (+0 and -0).

4 bit representation of +0 is 0000

4 bit representation of -0 is 1111

2's complement representation

In 2's complement representation, Positive number is represented by same as Sign magnitude but negative number is the 2's complement of positive number.

+6 is represented by 0 110

1's complement of +6 is 1 001

-6 is represented by 1 010 (that is 1 001 +1)

In 2's complement representation, the range of n bit number is

$-(2^{n-1})$ to $+(2^{n-1}-1)$

For 4 bit number the range is

-8 to +7

For 8 bit number the range is

-128 to +127

There is a unique representation of 0 (+0 and -0).

4 bit representation of +0 is 0000


4 bit representation of -0 is 0000

Arithmetic Addition

$$\begin{array}{r} (+7) \quad 00111 \\ (+5) \quad 00101 \\ \hline (+12) \quad 01100 \end{array}$$

Arithmetic Subtraction using 2's Complement method

$$\begin{array}{r} (+7) \quad 0111 \\ (-5) \quad 1011 \\ \hline (+2) \quad \textcircled{1}0010 \end{array}$$


carry bit

(+5) 0101
1's complement of +5 is 1010
2's complement of +5 is 1011

Incase of 2's complement carry bit will be discarded

Overflow in 2's Complement arithmetic

Perform the following calculation

Case1:

$$\begin{array}{r} +4 \quad 0100 \\ -7 \quad 1001 \\ \hline -3 \quad 1101 \end{array}$$

(+7) 0111
1's complement of +7 is 1000
2's complement of +7 is 1001

If the sign of operands are different then overflow never occur.

Case2:

$$\begin{array}{r} -2 \quad 1110 \\ -4 \quad 1100 \\ \hline -6 \quad 1\ 1010 \end{array}$$

(+2) 0010
1's complement of +2 is 1101
2's complement of +2 is 1110

(+4) 0100
1's complement of +4 is 1011
2's complement of +4 is 1100

Case3:

$$\begin{array}{r} -5 \quad 1011 \\ -6 \quad 1010 \\ \hline -11 \quad 1\ 0101 \end{array}$$

(+5) 0101
1's complement of +5 is 1010
2's complement of +5 is 1011

(+6) 0110
1's complement of +6 is 1001
2's complement of +6 is 1010

If the sign of operands are same then overflow may

occur
In case2: the sign of both operands are negative and the sign of the result also negative

so here overflow is not occurred and the extra bit is carry bit. Discard the carry bit

In case3: the sign of both operands are negative and the sign of the result is positive.

so here overflow is occurred and the extra bit is taken under consideration

Thank You