

# Truth table of AND gate

A	B	F
0	0	<b>0</b>
0	1	<b>0</b>
1	0	<b>0</b>
1	1	<b>1</b>

# Truth table of OR gate

A	B	F
0	0	<b>0</b>
0	1	<b>1</b>
1	0	<b>1</b>
1	1	<b>1</b>

# Truth table of half adder

A	B	S	C
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

$$S = A'.B + A.B' = A \oplus B$$

$$C = A.B$$

# Truth table of full adder

A	B	Cin	S	Cout
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

$$\begin{aligned} S &= A' B' \text{Cin} + A' B \text{Cin}' + A B' \text{Cin}' + A B \text{Cin} \\ &= A' B \text{Cin}' + A B' \text{Cin}' + A B \text{Cin} + A' B' \text{Cin} \\ &= (A' B + A B') \text{Cin}' + (A B + A' B') \text{Cin} \\ &= (A \oplus B) \text{Cin}' + (A \oplus B)' \text{Cin} \\ &= A \oplus B \oplus \text{Cin} \end{aligned}$$

$$\begin{aligned} \text{Cout} &= A' B \text{Cin} + A B' \text{Cin} + A B \text{Cin}' + A B \text{Cin} \\ &= \text{Cin} (A' B + A B') + A B (\text{Cin}' + \text{Cin}) \\ &= A B + \text{Cin} (A \oplus B) \end{aligned}$$

# Truth table of half subtractor

A	B	d	b
0	0	0	0
0	1	1	1
1	0	1	0
1	1	0	0

$$d = A'.B + A.B'$$

$$b = A'.B$$

# Truth table of full subtractor

A	B	bin	d	bout
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1

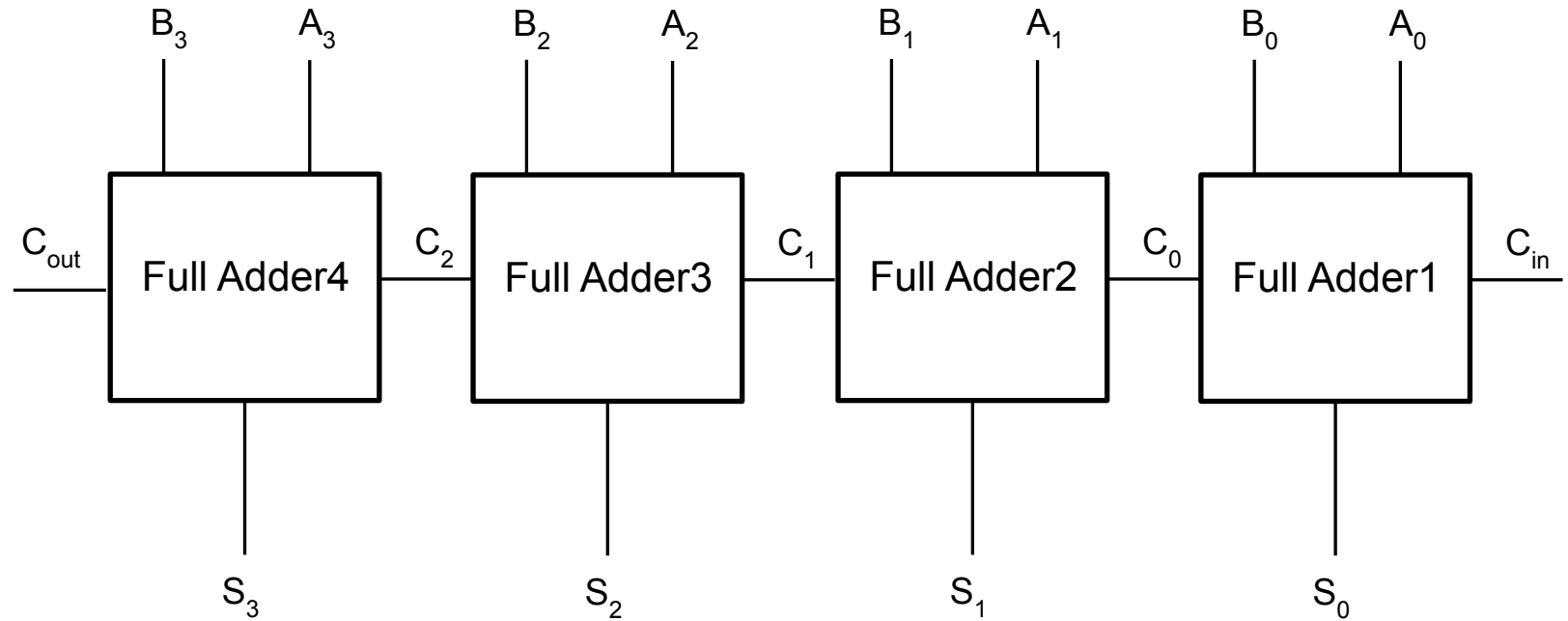
$$\begin{aligned}
 d &= A'.B'.bin + A'.B.bin' + A.B'.bin' + A.B.bin \\
 &= A'.B.bin' + A.B'.bin' + A.B.bin + A'.B'.bin \\
 &= (A'.B + A.B') .bin' + (A.B + A'.B').bin \\
 &= (A \oplus B). bin' + (A \oplus B)'. bin \\
 &= A \oplus B \oplus bin
 \end{aligned}$$

$$\begin{aligned}
 bout &= A'.B'.bin + A'.B.bin' + A'.B.bin + A.B.bin \\
 &= A'.B.bin' + A'.B.bin + A.B.bin + A'.B'.bin \\
 &= A'.B.( bin' + bin) + (A.B + A'.B').bin \\
 &= A'.B + (A \oplus B)'. bin
 \end{aligned}$$

# Addition of two 4 bit binary numbers

$$\begin{array}{r} \phantom{A} \phantom{B} \phantom{S} \phantom{10} 1 \phantom{0} 1 \\ A \phantom{B} \phantom{S} \phantom{10} 1 \phantom{0} 1 \phantom{1} \\ B \phantom{S} \phantom{10} 1 \phantom{0} 0 \phantom{1} 1 \\ \hline S \phantom{10} 1 \phantom{0} 1 \phantom{0} 0 \phantom{0} \end{array}$$

# Ripple Carry Adder





# Carry look ahead adder

$$S_i = A_i \oplus B_i \oplus C_i = P_i \oplus C_i$$

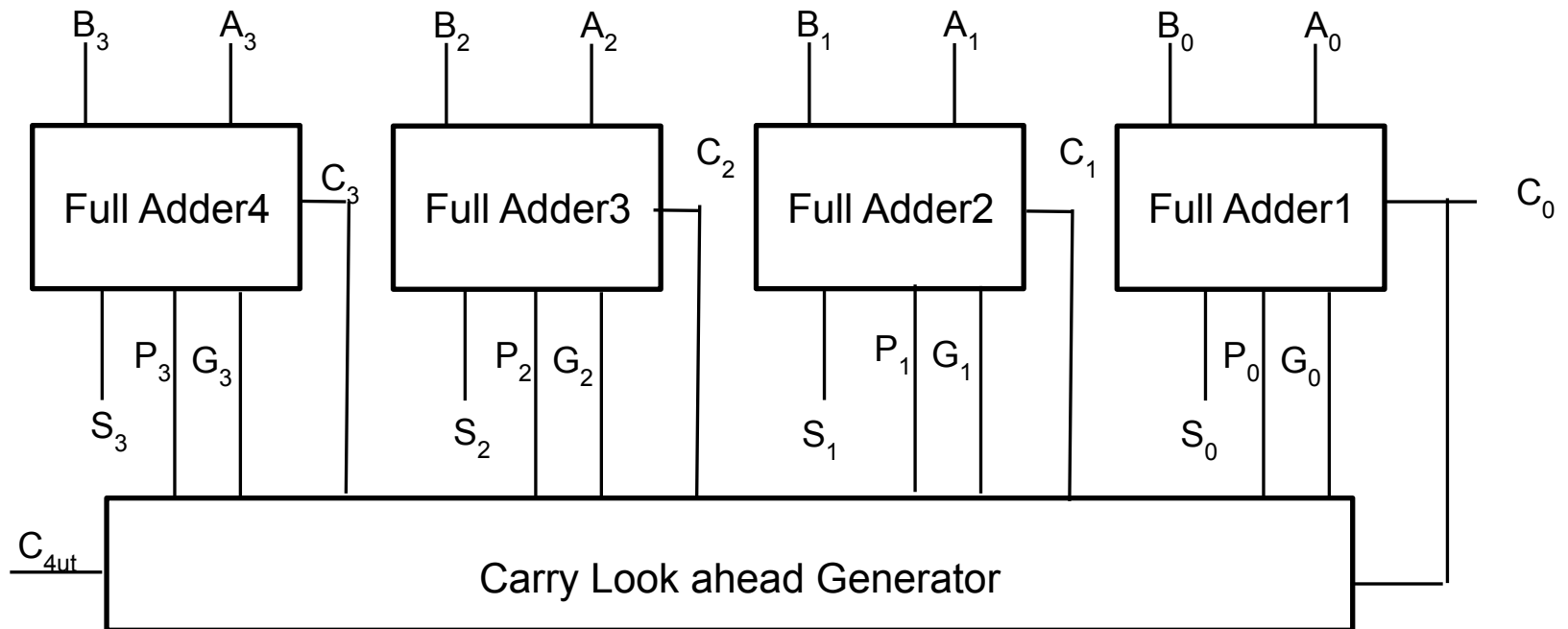
$$C_{i+1} = A_i \cdot B_i + (A_i \oplus B_i) \cdot C_i = G_i + P_i \cdot C_i$$

$$C_1 = G_0 + P_0 \cdot C_0$$

$$C_2 = G_1 + P_1 \cdot C_1 = G_1 + P_1 \cdot G_0 + P_1 \cdot P_0 \cdot C_0$$

$$C_3 = G_2 + P_2 \cdot C_2 = G_2 + P_2 \cdot G_1 + P_2 \cdot P_1 \cdot G_0 + P_2 \cdot P_1 \cdot P_0 \cdot C_0$$

$$C_4 = G_3 + P_3 \cdot C_3 = G_3 + P_3 \cdot G_2 + P_3 \cdot P_2 \cdot G_1 + P_3 \cdot P_2 \cdot P_1 \cdot G_0 + P_3 \cdot P_2 \cdot P_1 \cdot P_0 \cdot C_0$$



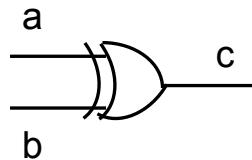
# Addition- Subtraction Example

$$\begin{array}{r} 0111 \text{ (A)} \\ 0011 \text{ (B)} \\ \hline 1010 \text{ (S)} \end{array}$$

$$\begin{array}{r} 0111 \text{ (A)} \\ 0011 \text{ (B)} \\ 1' \text{ s complement of B is } 1100 \\ 2' \text{ s complement of B is } 1100 + 1 \\ -B = 1101 \\ 0111 \text{ (A)} \\ 1101 \text{ (-B)} \\ \hline 10100 \text{ (S)} \end{array}$$

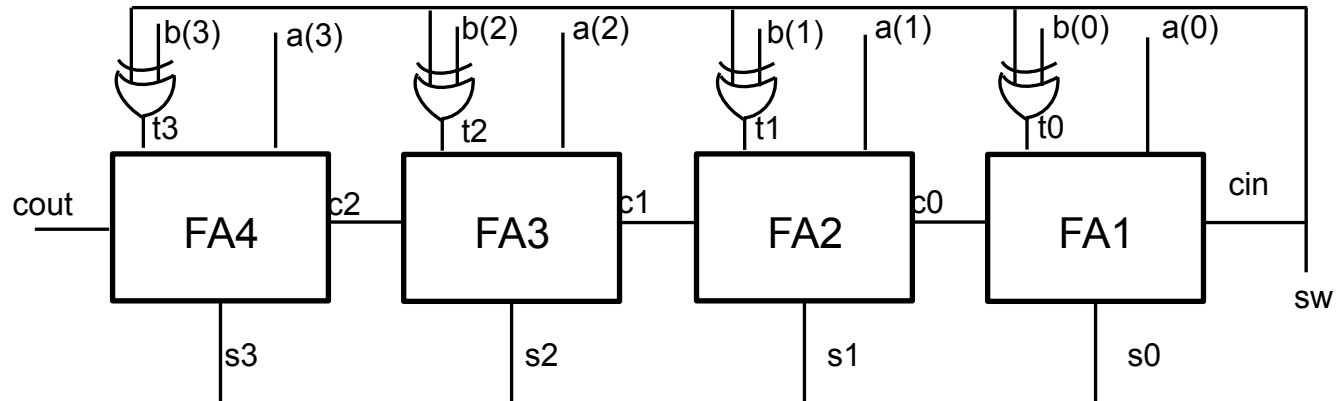
# Truth table of XOR gate

A	B	F
0	0	<b>0</b>
0	1	<b>1</b>
1	0	<b>1</b>
1	1	<b>0</b>



$$c = a \oplus b$$

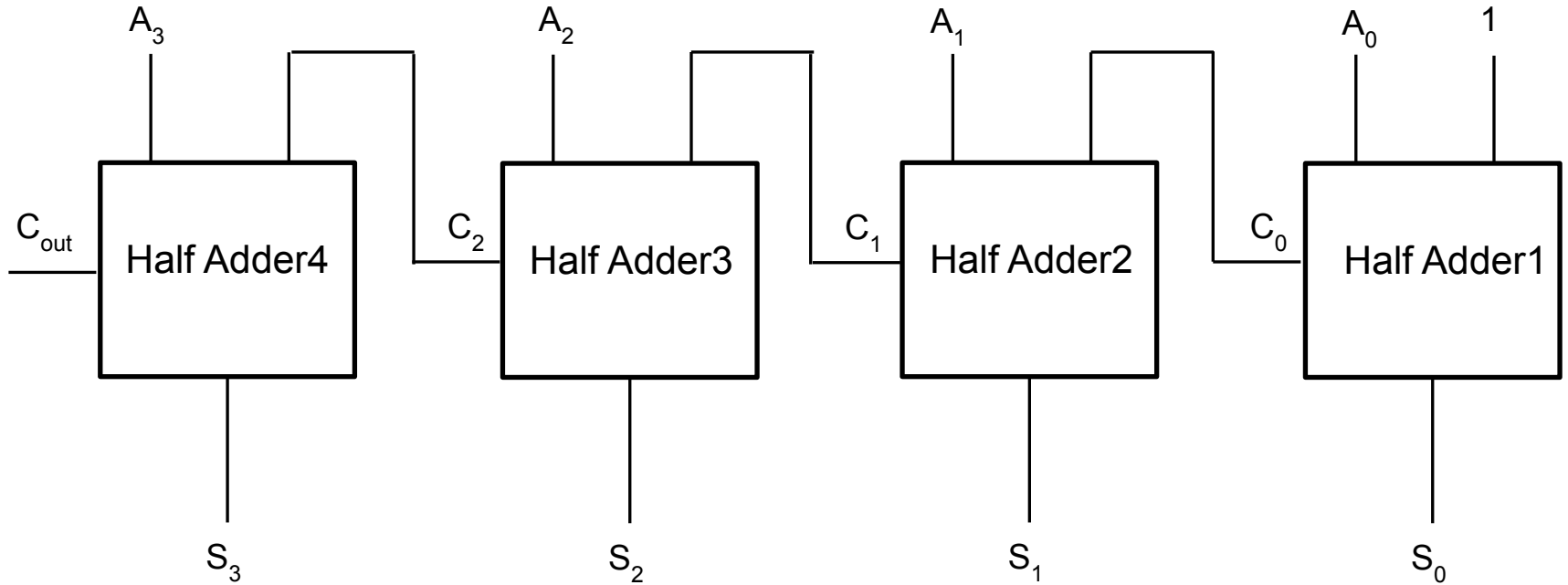
# Adder – Subtractor composite unit



# Binary 4 bit incrementer

$$\begin{array}{r} \phantom{A} \phantom{+} \phantom{1} \phantom{0} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ \phantom{A} \phantom{+} \phantom{1} \phantom{0} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ A \phantom{+} \phantom{1} \phantom{0} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ + \phantom{1} \phantom{0} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ \hline S \phantom{+} \phantom{1} \phantom{0} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \\ \phantom{S} \phantom{+} \phantom{1} \phantom{0} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \phantom{1} \end{array}$$

# Binary 4 bit incrementer circuit

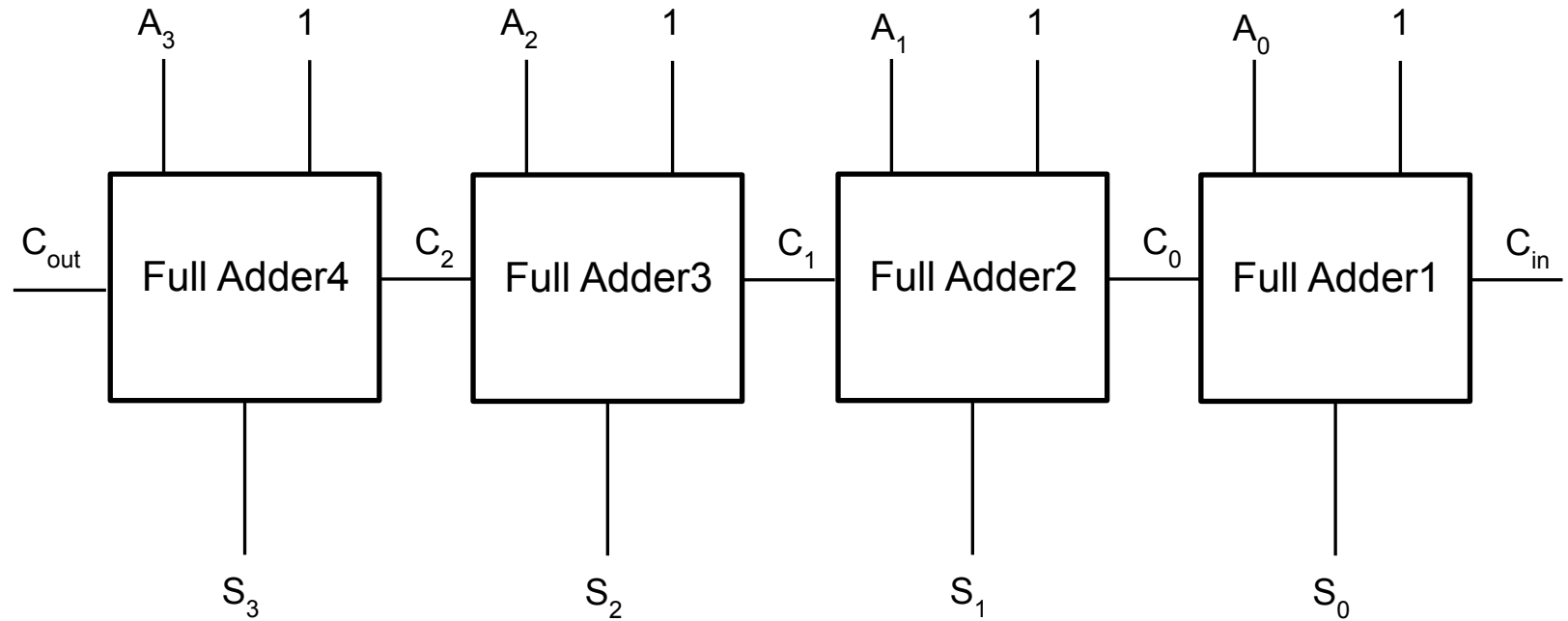


# Binary 4 bit decrementer

4 bit representation of	+1	0001	
1's complement		1110	+1
2's complement		1111	(-1)

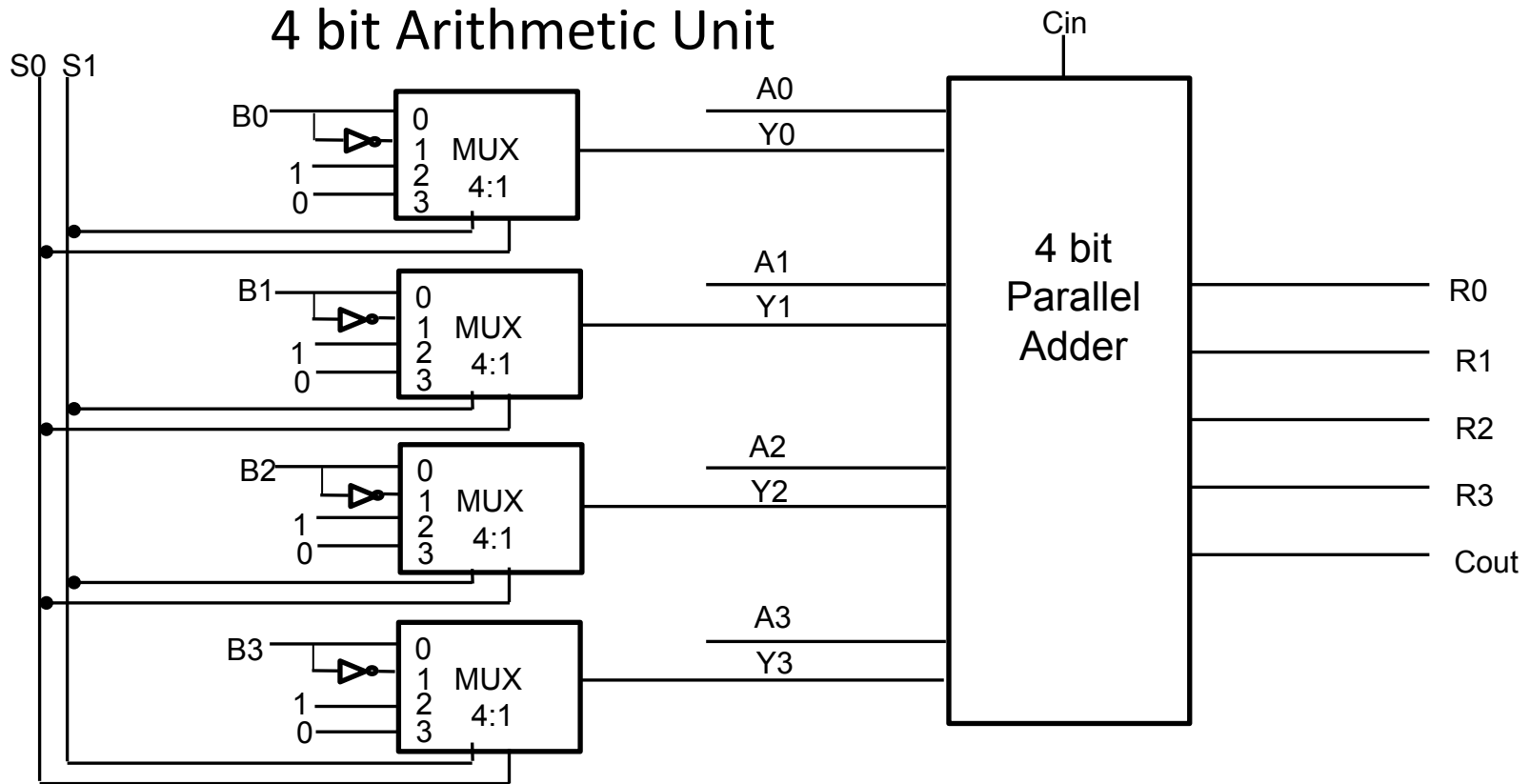
$$\begin{array}{r}
 \phantom{A} \phantom{+} \phantom{S} \phantom{0} \phantom{1} \phantom{1} \phantom{0} \phantom{(6)} \\
 \phantom{A} \phantom{+} \phantom{S} \phantom{0} \phantom{1} \phantom{1} \phantom{0} \phantom{(6)} \\
 A \phantom{+} \phantom{S} \phantom{0} \phantom{1} \phantom{1} \phantom{0} \phantom{(6)} \\
 + \phantom{S} \phantom{0} \phantom{1} \phantom{1} \phantom{0} \phantom{(6)} \\
 \hline
 S \phantom{0} \phantom{1} \phantom{1} \phantom{0} \phantom{(6)}
 \end{array}$$

# Binary 4 bit decrementer circuit





# 4 bit Arithmetic Unit



S1	S0	Cin	Y	$F=A+Y+Cin$	Operation
0	0	0	B	$F=A+B$	Addition
0	0	1	B	$F=A+B+1$	Addition with carry
0	1	0	$\bar{B}$	$F=A+\bar{B}$	Subtraction with borrow
0	1	1	$\bar{B}$	$F=A+\bar{B}+1$	Subtraction
1	0	0	1	$F=A-1$	Decrement
1	0	1	1	$F=A$	Transfer
1	1	0	0	$F=A$	Transfer
1	1	1	0	$F=A+1$	Increment

Thank You