Block A: upper post C = Tout input. 33788 port A = output mode = 1.

memory mapped 1/6 (P)(1)

oun memory mapped up, menjare, the both the.

90 ICHESAH HERS

10 addresses & the memory addresses needle

in the same address space. It the memosing address spall is IN byte.

· merefore, while executing instructions in seperate.

in structions are required for your perations!

· Also, no special instructions are deducated for · AJACA memory rapped 110.

when rompared to insolated you the metrochou gets executed much iffenter of IA VOM

· no uneassary memory space us utilized have the available memory spaces are utilised expected

- Abithametic and regimal operations can be performed directly wither the sipput data.

· In memory mapped ito, the work ay the possible recognish solution will present

i.e. ALE, IDIM , DTIR, DEN , RD, WP, address etc.

MICROPROCESSO LABORAT

> ABINAYA P AKSHARAA GAYATHRI INIYAA

> > SNESHA

BACHELOR

ARTMENT OF PSG C

OOBFF ! 000 down port 4325 ma 2001. Post 25%. adoress brow - store is the premoving object book addies solven solves 8pace .01 Port 3. more separation of the market source Post 1 00003 Post 2 000 00000 Port 0: (16 bit)

00003 Post 2.

000000 Post 2.

00000 Post 0: (16 bit)

00000 Post 0: No sextriction to use wede.

AX/AL sugister in memory

mapped repred 0004 (16 bit) Ad. 1801ated 1/0

in the medical lo interlate la la constation of the miles espace is given a dedicated space composed

to memory address space, it is not attended

with the nomony address space:

Henre white especial instructions are gramed you resoluted 1/0 tie volated to 1/0 operations and aprocessing.

when reomposed to memory mapped 1/0, the. instructions are executed much shower.

The Drug ALIAX register wan be used in the reast of inolated 110 othis restricts the on of execution of metrochors selvers in more

me-

