serial peripheral Interface (spi) Built * It is synchronous bi-directional, full duplex, four-wire Serial interface bus. It is single master and multi-slave System. It requires 4 signal lines for communication. Master out slave In (MOSI): - Signal line carries the data from master to slave device. It is known as slave In slave data in (SDI). I man describe the supposed man of the Master In slave out (MISO):- signal line carries the data from slave to master device. It is known as slave out Slave data out. Serial clock (SCIK): - Signal line carrying the clock signals. Slave select (SS): - signal line for slave device select. It is active low. Most MISO MOSI slave - 1 up/controller Spidevice MISO 551 EX :- EEPROM 5511 MOSI 5521 SCL Slave - 2 MISO 551 EX:- LCD SPI BUS

fig:- spi bus înterface