Embedded firmware design approaches:

Firmware design depends on the complexity of functions to be performed and speed of operation required. Two basic approaches of Embedded firmware design are:

- 1) Conventional procedural based firmware design (Super loop model)
- 2 Embedded operating system (08) based design.
- 1) Super loop based model: walt moders
 - * Super loop firmware dazign approach is adopted for applications that are not time critical and where the response time is not so important
 - * These are the embedded systems where the code is executed task by task where mining deadlines are acceptable.
- * Super loop based design doesnot require an operating system since there is no need for scheduling and assigning of priority to each task.
- * In this design, priority of tasks are fixed and the order of execution is also fixed.
- Ex: Reading / Writing data to and from a card using card reader requires a sequence of operations like checking the presence of card, authentication of the operation, reading / writing etc.

It should follow a sequence and the combination of all these series of tasks constitute a single task.

Advantages: * Superloop design is simple and straight forward without any OS related overheads. * No need of special operating system (OS) Disadvantages: Tisadvantages:

* Any failure in the part of task will affect the total system. * Lack of real timeliners brings the probability of missing events. missing events. The firmware execution flow of superloop model will be 1 Configure the command parameters and perform initialization of various hardware components, memory, registers etc. 100 start the first task and execute it. * These are the ex 3 Execute second task you hast be best so 4 Execute next task are acceptable. Super loop based design doesnot require on 100 Execute the last defined task.

(8) Jump back to the first task and follow the same flow. Ex: - Void mainers asket to ptinoing regised with on E configurations (); the order of execution is Initializations(); of the pristing pribasis card reader requires a sequence (1) show like checking the presence of Cilly aut Task2(); griting pribase, notherago soit he taskn(); bus sequence and follow blue is Ill these raises of tasks constitute a single

(2) Embedded Operating System (OS) based approach: There are two types of operating systems. They are: (a) General Purpose Operating System (GPOS) Ex:- Wendows XP, Unix, Linux (b) Real time Operating System (RTOS)

[Ex: Symbian, Elinux, Thread X, Vx Works etc.] (a) General Purpose Operating System: This is very similar to conventional personal computer based application development where the

device contains an operating system and user

applications will run on top of it.

Ex: Windows, Unix, Linux etc. 1

Examples of Embedded products which use GPOS are Personal Digital Assistants (PDA's), Hand held devices/ portable devices and point of sales (Pos) terminals.

(b) Real time Operating system: RTOS Contains real time Kernel responsible for performing pre-emptive multitasking, schedules for

scheduling task, multiple threads etc.

RTOS allows flexible scheduling of system resources like the cpu and memory and offers some way to communicate between tasks.

Ex: Windows CE, PSOS, Thread x, Embedded linux, Symbian Micro C/OS-II etc. Most of the mobile phones are built around the popular RTOS 'symbian'.

* Machine language and Assembly