Serial Peripheral Interface (SPI) Bus :-

* It is synchronous bi-directional, full duplex, four-wire serial interface bus. It is single master and multi-slave system. It requires 4 signal lines for communication.

Master out slave In (MOSI) :- Signal line carries the data from master to slave device. It is known as slave In | slave data in (SDI).

Master In slave Out (MISO) :- signal line carries the data from slave to master device. It is known as slave out | slave data out.

Serial clock (SCIK) :- Signal line carrying the clock signals.

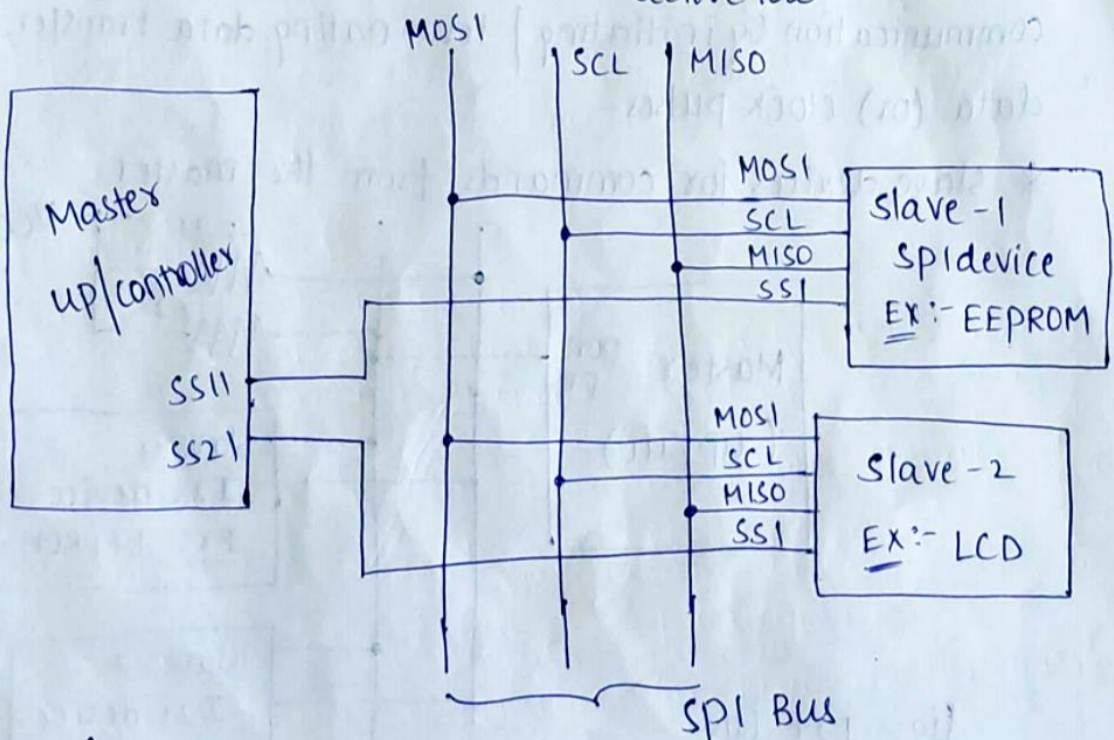Slave Select (SS) :- signal line for slave device select. It is active low.

fig:- SPI bus interface