

Syntax Directed Translations

11/10/22

1) Definition

Attributes / semantic rules.

2) Translation

$$E = E + T$$

Attribute \rightarrow val, attached

$$E.\text{val} = E.\text{val} + T.\text{val}$$

to every
grammar symbol

\downarrow
semantic
rule.

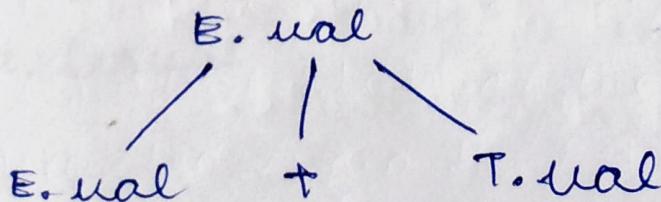
attribute

synthesized inherited

A syntax directed definition is a generalization of a CFG in which each grammar symbol had an associated set of attributes, partitioned into two subsets called the synthesized and inherited attributes of that grammar symbol. An attribute can represent a number, string, memory location etc.

The value of an attribute at a parse tree node is defined by a semantic rule associated with the production used at that node. The value of a synthesized attribute at a node is computed from the values of attributes at the children of that node in the parse tree; the value of an inherited attribute is computed from the values of attributes at the siblings and parent of that node.

$$E.\text{val} = E.\text{val} + T.\text{val}.$$



$A \rightarrow \alpha$

Semantic rule, $b = f(c_1, c_2, c_3, \dots, c_n)$

This is the form of a syntax directed definition.

Desk calculator program

Production

$L \rightarrow E_n$

$E \rightarrow E_1 + T$

$E \rightarrow T$

$T \rightarrow T_1 * F$

$T \rightarrow F$

$F \rightarrow (E)$

$F \rightarrow \text{digit}$

Semantic rules

~~$E_{\cdot} \text{val} = E_{\cdot-1} \text{val} + T_{\cdot} \text{val}$~~

$E_{\cdot} \text{val} = T_{\cdot} \text{val}$

$T_{\cdot} \text{val} = T_{\cdot-1} \text{val} * F_{\cdot} \text{val}$

$T_{\cdot} \text{val} = F_{\cdot} \text{val}$

$F_{\cdot} \text{val} = E_{\cdot} \text{val}$

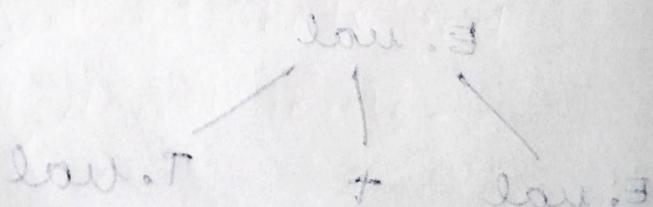
$F_{\cdot} \text{val} = \text{digit}$

$\text{print}(E_{\cdot} \text{val})$

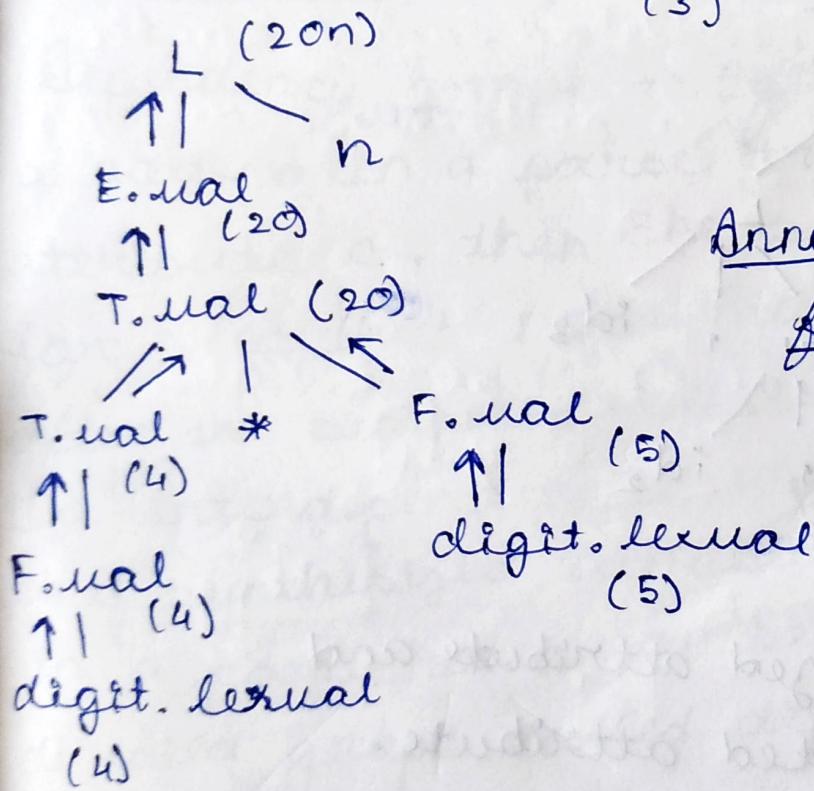
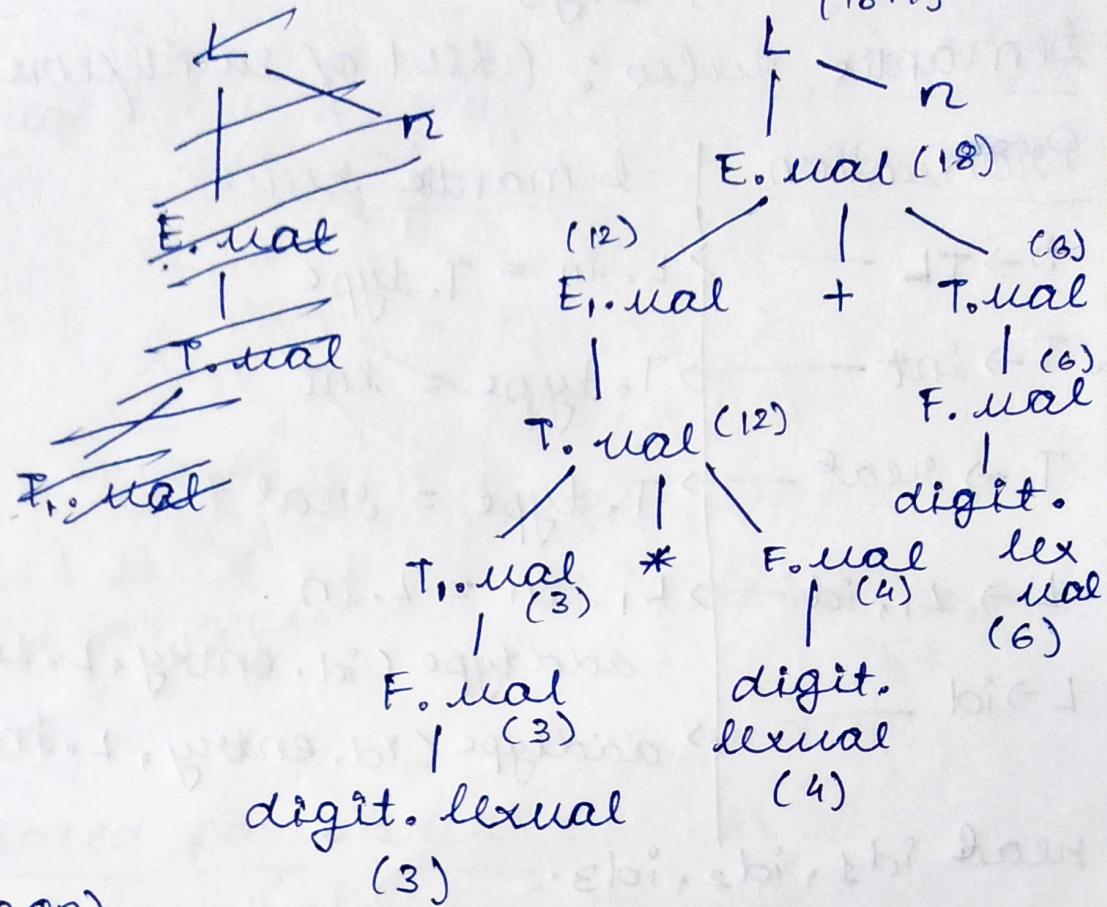
Two attributes \rightarrow val and lexical

↓
all NT ↓
all digit T

$3 * 4 + 6n$



Annotated parse tree →



Annotated parse tree

for $\underline{4 * 5n}$

$$A \cdot a = f(x \cdot x, y \cdot y)$$

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Semantic rules: (List of identifiers)

Production | Semantic rules

$$D \rightarrow TL \rightarrow L \cdot in = T \cdot type$$

$$T \rightarrow int \rightarrow T \cdot type = int$$

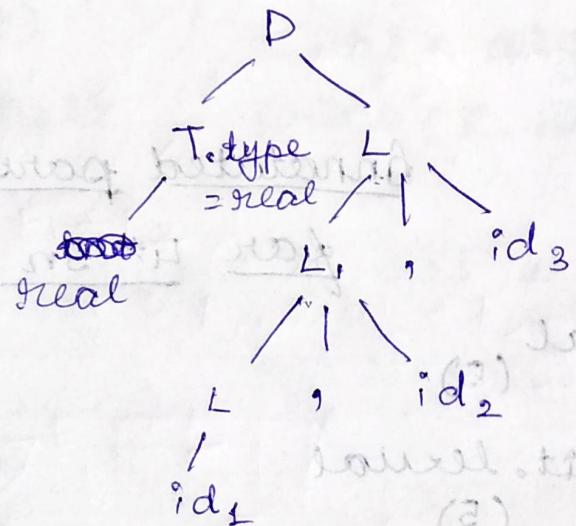
$$T \rightarrow real \rightarrow T \cdot type = real$$

$$L \rightarrow L_1, id \rightarrow L_1 \cdot in = L \cdot in$$

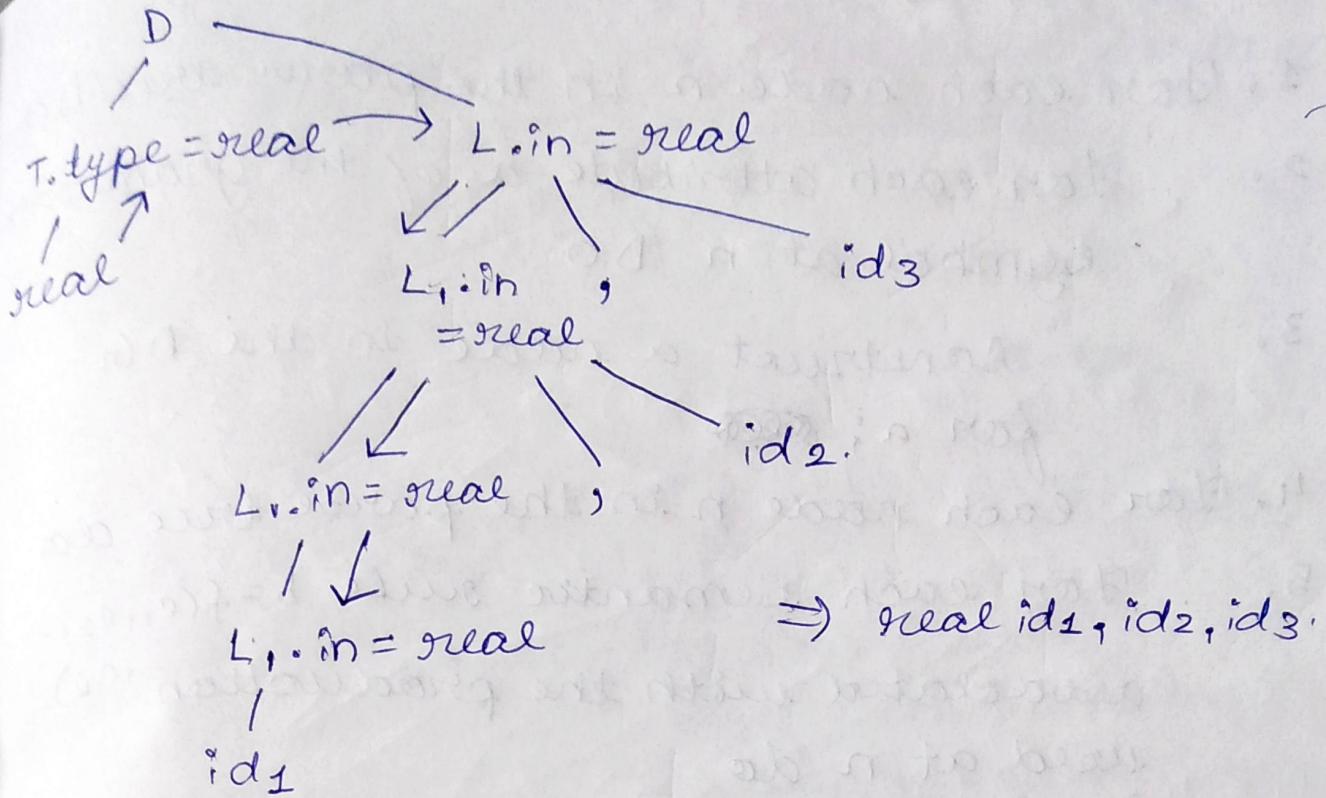
$$L \rightarrow id \rightarrow addtype(id \cdot entry, L \cdot in)$$

$$read id_1, id_2, id_3 \rightarrow addtype(id \cdot entry, L \cdot in)$$

$$real id_1, id_2, id_3$$



real is synthesized attribute and
in is ~~synthesized~~ inherited attribute.



Annotated parse tree

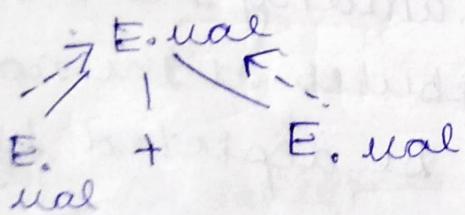
Dependency graph \rightarrow If an attribute b at a node \bullet in a parse tree depends on an attribute c , then the semantic rule for b at that node must be evaluated after the semantic rule that defines c . The interdependencies among the inherited and synthesized attributes at the nodes in a parse tree can be depicted by a directed graph called dependency graph.

The DG is constructed as:

1. For each node n in the parse tree D_G
2. For each attribute a of the grammar symbol at n do
 3. Construct a node in the D_G for a ; ~~node~~
 4. For each node n in the parse tree do
 5. For each semantic rule $b = f(c_1, c_2, \dots)$ associated with the production ' c_k ' used at n do
 6. for $i = 1$ to K do
 7. construct an edge from the node for c_i to the node for b .

e.g. $E \rightarrow E_1 + E_2$

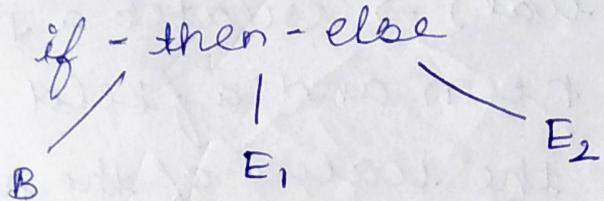
sem. rule : $E_1 \cdot \text{val} + E_2 \cdot \text{val} = E \cdot \text{val}$



construction of syntax tree \rightarrow

A ~~syntax~~ syntax tree is a condensed form of parse tree useful for representing language constructs.

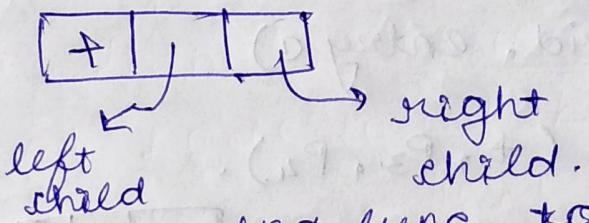
if B then E₁ else E₂



$a + b * 4$

$a + b * 4$

Each node in a syntax tree can be implemented as a record with several fields. In the node for an operator, one field identifies the operator and the remaining fields contains pointers to the nodes for the operator.



We use the following func. to create the nodes of syntax tree for expression with binary operators. Each func. returns a pointer to a newly created node.

$\Rightarrow \text{mknode}(\text{op}, \text{left}, \text{right}) \rightarrow$ creates an operator node with field op and two fields containing pointer to left and right.

2) $\text{mk_leaf}(\text{id}, \text{entry})$ → creates an identifier node with ~~label~~^{label} id and a field containing entry, a pointer to the symbol table entry to the ~~id~~ identifier.

3) $\text{mk_leaf}(\text{num}, \text{val})$ → creates a number node with label num and a field containing val, the value of the number.

e.g. $a - 6 + c$

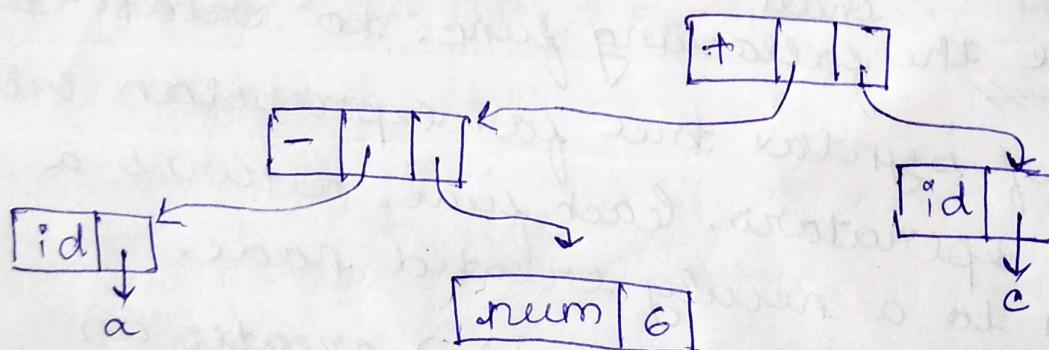
$P_1 = \text{mk_leaf}(\text{id}, \text{entry } a)$

$P_3 = \text{mk_node}(\text{-}, P_1, P_2)$.

$P_2 = \text{mk_leaf}(\text{num}, 6)$

$P_4 = \text{mk_leaf}(\text{id}, \text{entry } c)$

$P_5 = \text{mk_node}(+, P_3, P_4)$.



syntax tree

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Directed acyclic graph (DAG). set

$$i := i + 10$$

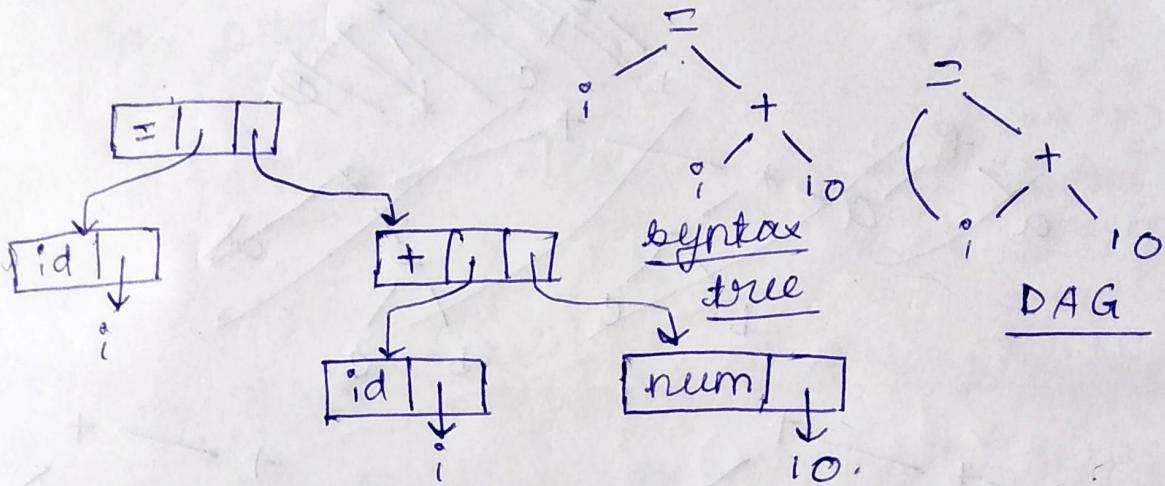
$$i = i + 10$$

$$i = i + 10$$

$$i \cdot \text{val} = i \cdot \text{val} + 10$$

$$i \cdot \text{val}$$

$$i \cdot \text{val} + 10$$

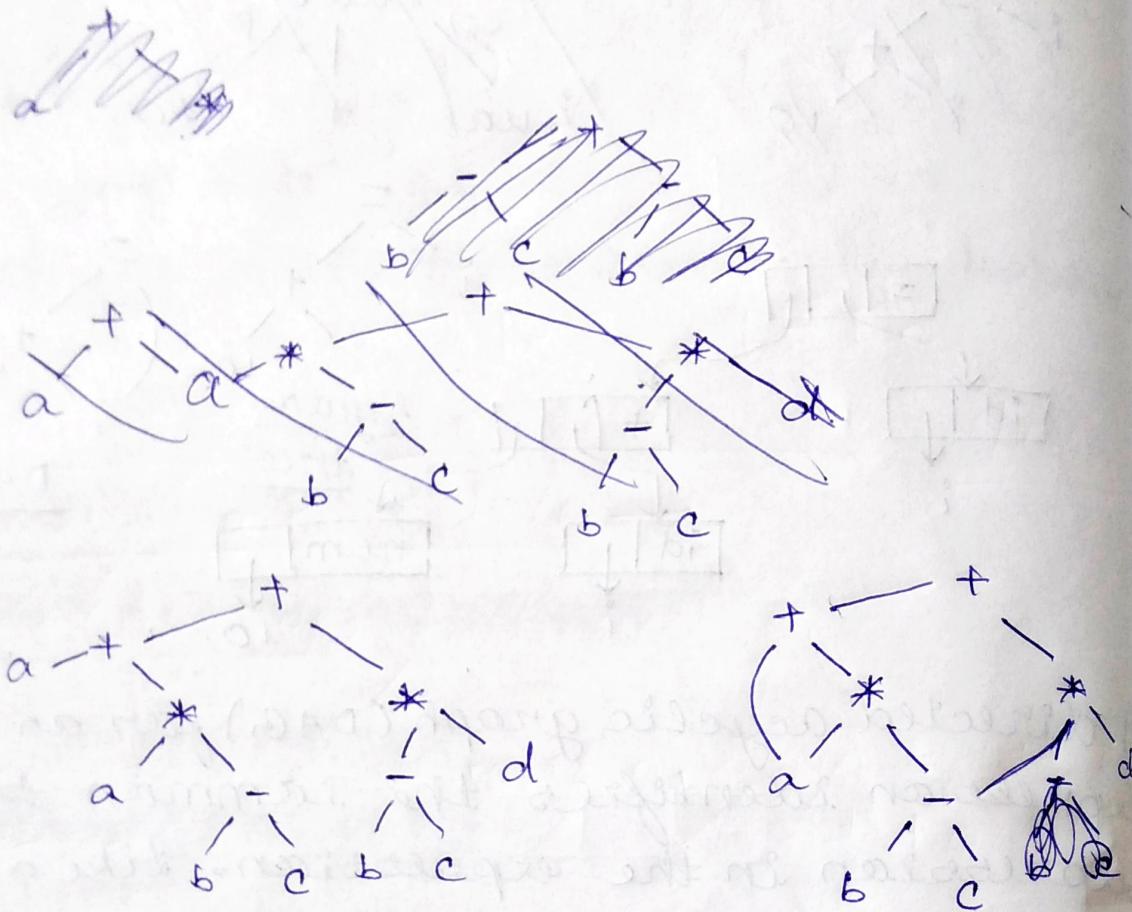


A directed acyclic graph (DAG) for an expression identifies the common sub-expression in the expression. Like a syntax tree, a DAG has a node for every sub-expression of the expression; and interior node represents an operator and its children represents its operators.

The difference is that a node in a DAG representing a common sub-expression has more than one parent in a syntax tree.

The common sub-expression would be represented as a duplicate subtree.

e.g. $a + a * (b - c) + (b - c) * d$.



Bottom-up evaluation of

S-attribute definition

Synthesize attributes can be evaluated by a bottom-up parser as the input is being parsed. The parser can keep the

DAG

values of the synthesized attributes associated with the grammar symbol on its stack. Whenever a reduction is made, values of the new synthesized attributes are computed from the attributes appearing on the stack for the grammar symbol on the RHS of the reducing production.

$$\begin{aligned}
 L &\rightarrow E_n \\
 E &\rightarrow E, + T \\
 E &\rightarrow T \quad T \rightarrow T, * F \\
 T &\rightarrow F \quad F \rightarrow (E) \\
 F &\rightarrow \text{digit}
 \end{aligned}$$

<u>Input</u>	<u>state</u>	<u>val</u>	<u>Production used</u>
$3 * 5 + 4 n$	-	-	
$* 5 + 4 n$	3	3	F → digit
$* 5 + 4 n$	F	3	$F \rightarrow \text{digit}$
$* 5 + 4 n$	T	3	$T \rightarrow F$.
$5 + 4 n$	$T * 5$	3 -	
$+ 4 n$	$T * 5$	3 - 5	
$+ 4 n$	$T * F$	3 - 5	$F \rightarrow \text{digit}$.
$+ 4 n$	T	15	$T \rightarrow T * F$.
$4 n$	$T +$	15 -	
n	$T + 4$	15 - 4	

n T + F 15 - 4 $F \rightarrow \text{digit}$

n T + T 15 - 4 $T \rightarrow \text{digit}$

n E + T 15 - 4 $E \rightarrow T$

n E 19 $E \rightarrow E + T$

- En 19 -

- L 19 - $L \rightarrow En$

Depth-First Evaluation Order for attributes in a Parse tree → 18/10/22

dfvisit (n : node)

begin

for each child m of n, from left to right do

begin

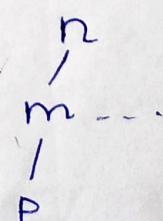
evaluate inherited attributes of m;

dfvisit(m)

END

evaluate synthesized attributes of n

END



L-attributed definitions

A syntax directed definition is L-attributed if each inherited attribute of x_j , $1 \leq j \leq n$ on the right side of production

$A \rightarrow x_1 x_2 \dots x_n$, depends only on

- i) the attributes of the symbol x_1, x_2, \dots, x_{j-1} to the left of x_j on the production.
- ii) the inherited attributes of A.

Translation Scheme

A translation scheme is a CFG in which attributes are associated with the grammar symbol and semantic actions enclosed ~~within~~ between braces {} are inserted within the right side of the production.

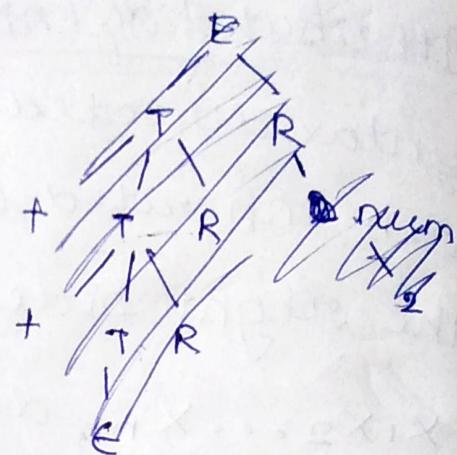
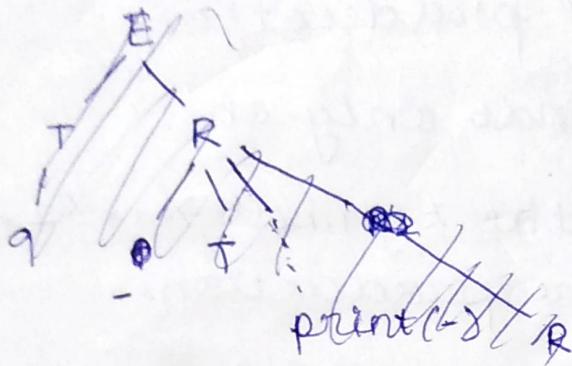
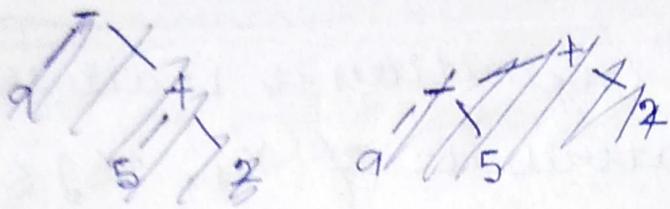
$E \rightarrow TR$

$R \rightarrow \text{addop} T \{ \text{print}(\text{addop. lexeme}) \} R, | \in I$

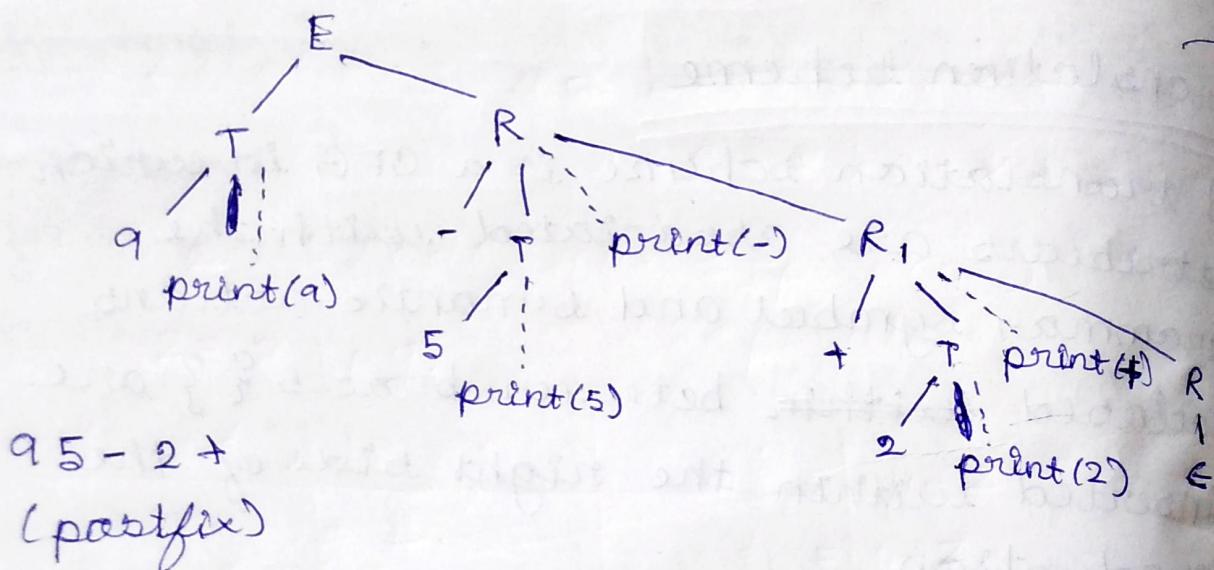
$T \rightarrow \text{num} \{ \text{print}(\text{num. val}) \}$.

$$9 - 5 + 2 \quad 9 + (-5) + 2.$$

Show action for infix to postfix using parse tree.



Infix input →
9 - 5 + 2



Bottom-up evaluation of Inherited attributes

real p, q, r.

- - D D → TL.

$D \rightarrow TL$
 $T \rightarrow int$
 $T \rightarrow real$
 $L \rightarrow L_1, id$
 $L \rightarrow id$

Input
~~real P, 2, r~~
~~real L, 2, r~~
~~real L, r~~

Input
~~real P, 2, r~~

~~P, 2, r~~

~~P, 2, r~~

~~1, 2, r~~

~~1, 2, r~~

~~2, r~~

~~1, r~~

~~r~~

~~-~~

~~-~~

state

-

state

-

real

+

TP

TL

TL,

TL, 2

TL

TL,

TL, r

TL

Production presed

~~L → P~~

~~L → L_1, id~~

Production used

~~Presed~~ -

~~Presed~~ -

~~Presed~~ -

~~Presed~~ -

~~L → id~~

~~L → id~~

~~L → id~~

~~L → L_1, id~~

~~L → L_1, id~~

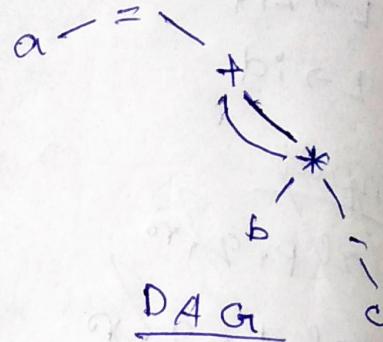
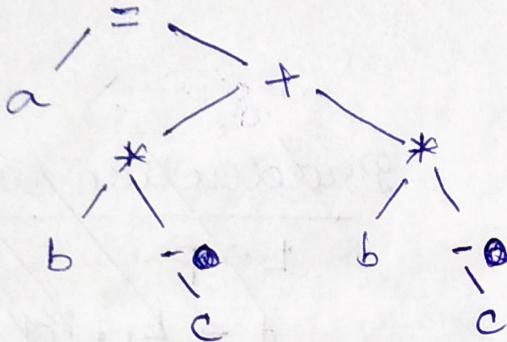
~~L → L_1, id~~

~~L → L_1, id~~

Intermediate Code Generation

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$$a = b * -c + b * -c$$



Syntax tree

Postfix notation: ~~abc - * b - * c - *~~

ab c - * bc - * + =

Three address code (max. 3 operands)

$z = x \text{ op } y.$

2 operators.

e.g. if () then

goto L.

$a = x + y + z$

$t_1 = y + z.$

$a = x + t_1,$

↓

3-address code.

~~DDDDDDDD~~

~~DDDDDDDD~~

$$\begin{cases}
 t_1 = -c \\
 t_2 = t_1 * b \\
 t_3 = t_2 + t_2 \\
 a = t_3
 \end{cases}$$

Three address code.

$$\begin{aligned}
 t_1 &= -c \\
 t_2 &= b * t_1 \\
 t_3 &= -c \\
 t_4 &= b * t_3 \\
 a &= t_2 + t_4
 \end{aligned}$$

$$\begin{aligned}
 t_1 &= -c \\
 t_2 &= b * t_1 \\
 a &= t_2 + t_4 \\
 &\quad \text{① } \cancel{a = t_2}
 \end{aligned}$$

DAG.

~~syntax tree~~
ICG (3-address code)

Types of 3-address code →

- i) $z = x \text{ op } y / x = y \text{ op } z$.
- ii) $x = \text{op } y$
- iii) $x = y$
- iv) goto L
- v) if x relational operator y goto L
(relap)
- vi) $x = y[i] / x[i] = y$
- vii) $x = \&y ; x = *y$.

Implementation of 3 address statements →

A 3-address statement is an abstract form of intermediate code. In a compiler these statements can be implemented as records with fields

for the operators and operands.

3 such representations are ~~quadtrape~~
~~quad~~ quadruples, triples and indirect
triples.

quadruples \rightarrow It is a record structure
with 4 fields: (operator) op, arg₁, arg₂,
result

$$a = b * -c + b * -c$$

Implementing using quaduple.

The op field contains an internal code for
the operator. The three address statement
 $x = y \text{ op } z$ is represented by placing y in
arg₁, z in arg₂ and x in result.

	op	arg ₁	arg ₂	result
(0)	-	c		t ₁
(1)	*	b	t ₁ t₂	t ₂
(2)	-	c		t ₃
(3)	*	b	t ₃	t ₄
(4)	+	t ₂	t ₃ t ₄	a

	op	arg 1	arg 2
(0)	-	c	
(1)	*	b	(0)
(2)	-	c	
(3)	*	b	(2)
(4)	+	(3)	(1)

Triple representation

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Indirect triple → Another implementation of 3-address code that has been considered is that of listing pointers to triples, rather than listing the triples themselves. This implementation is called indirect triples.

	Statement
(10)	(10)
(11)	(11)
(12)	(12)
(13)	(13)
(14)	(14)
(15)	(15)

Basic Block and Flow graph

A graph representation of 3-address statement is called a flow graph. It is useful for understanding code generation algorithm.

A basic block is a sequence of consecutive statements in which flow of control enters at the beginning and leaves at the end without halt or possibility of branching except at the end.

Partition - into - basic - blocks

Inputs - a sequence of 3-address statements
outputs → a list of basic blocks with each 3-address statement in exactly one block.

Method

- 1) we first determine the set of leaders, the first statement of basic block. The rules are the following.
- 2) the first statement is a leader.

ii) Any statement that is the target of a conditional or unconditional goto is a leader.

iii) Any statement that immediately follows a goto or conditional goto statement is a leader.

⇒ For each leader its basic block consists of the leader and all statements upto but not ~~included~~ ~~or~~ including the next leader for the end of the program.

when each basic block is connected it forms a flow graph.

e.g. $\Rightarrow \text{pred} = 0$

⇒ $i = 1$

iii) $t_1 = 4 * i$

iv) $t_2 = \del{a[i]}{a[t_1]}$

v) $t_3 = 4 * i$

vi) $t_4 = b[t_3]$

vii) $t_5 = t_2 * t_4$

$$viii) t_6 = p\text{rad} + t_5.$$

$$ix) p\text{rad} = t_6$$

$$x) t_7 = i+1$$

$$xi) i = t_7$$

xii) ~~if~~ if $i \leq 20$ goto 3.

$$\boxed{\begin{array}{l} p\text{rad} = 0 \\ \rightarrow i = 1 \end{array}} \quad \begin{array}{l} (i) \\ (ii) \end{array}$$

$$t_1 = 4 * i \quad (iii)$$

$$t_2 = a[t_1] \quad (iv)$$

$$t_3 = 4 * i \quad (v)$$

$$t_4 = b[t_3] \quad (vi)$$

$$t_5 = t_2 * t_4 \quad (vii)$$

$$t_6 = p\text{rad} + t_5 \quad (viii)$$

$$p\text{rad} = t_6 \quad (ix)$$

$$t_7 = i+1 \quad (x)$$

$$i = t_7 \quad (xi)$$

if $i \leq 20$ goto 3 (xii)

Transformation on basic blocks

1/11/22

A basic block computes a set of expressions. These expressions are the values of the names left or exit from the block. Two bb are said to be equivalent if they compute the same set of expressions. A number of transformation can be applied to a bb without changing the set of expressions computed by the block. Many of these transformations are useful for improving the quality of code that will be generated from a bb. There are 2 important classes of local transformation that can be applied to bb. These are the structure preserving transformation and algebraic transformation.

- i) Structure preserving transformation →
The primary SPT on bb are →
 - i) common subexpression elimination
 - ii) Dead ~~code~~ elimination
 - iii) Renaming of temporary variables
 - iv) Interchange of two independent adjacent statements.

$$a = b + c$$

$$b = a - d$$

$$c = b + c$$

$$d = a - d$$

eliminating the \downarrow sub-common expression,

$$a = b + c$$

$$b = a - d$$

$$c = b + c$$

$$d = b$$

Dead code elimination \rightarrow Remove any expression that is not used further in the basic block.

Renaming of temp. variables \rightarrow

$$t = b + c \Rightarrow u = b + c$$

Renaming variable t with u .

Interchange of statements \rightarrow

$$t_1 = b + c \Rightarrow t_2 = x + y$$

$$t_2 = x + y \quad t_1 = b + c$$

e.g. $x = x * 1$ \rightarrow algebraic transformation.
 $x = x + 0$

$$x = y^{**} 2$$

$$x = y * y$$

DAG representation of basic blocks.

- (1) $t_1 = 4 * i$
- (2) $t_2 = a[t_1]$
- (3) $t_3 = 4 * i$
- (4) $t_4 = b[t_3]$
- (5) $t_5 = t_2 * t_4$
- (6) $t_6 = p\text{rad} + t_5$
- (7) $p\text{rad} = t_6$
- (8) $t_7 = i + 1$
- (9) $i = t_7$
- (10) if $i \leq 20$ goto (1)

DAGs are useful data structures for implementing transformation on basic blocks. A DAG gives a picture of how the value computed by each statement in basic block is used in subsequent statements of the block. Constructing a DAG from 3-address statements is a good way of determining common subexpression within a block, determining which names are used inside the block but evaluated outside the block, and determining which statements of the

block would have their computed value used outside the block.

A DAG for a basic block is a directed acyclic graph with the following label

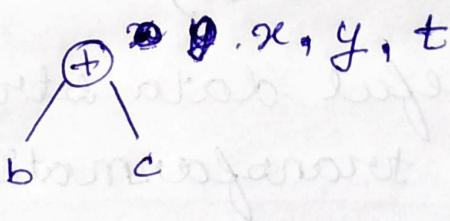
~~label~~ of nodes -

- i) leaves are ~~label~~ labelled by unique identifiers, variable names or constants.
- ii) interior nodes are labelled by an operator symbol.
- iii) nodes are also optionally given a sequence of identifier for labels.

$$x = b + c$$

$$y = x$$

$$t = y$$



e.g.

