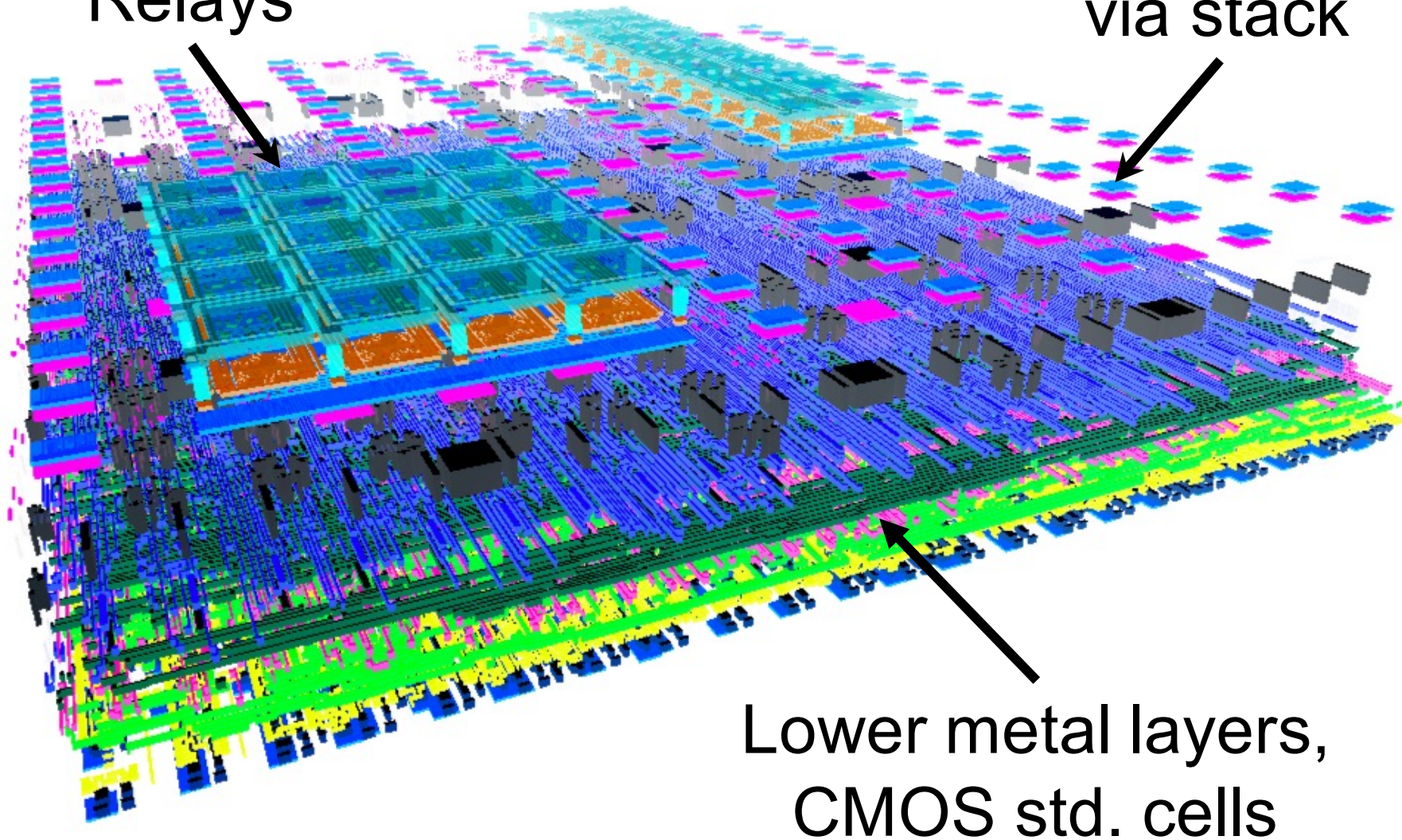


NEM
Relays

Power grid
via stack



Lower metal layers,
CMOS std. cells