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Generated by: Genus(TM) Synthesis Solution 17.22-s017\_1

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Module: ASTS

Operating conditions: slow (balanced\_tree)

Wireload mode: enclosed

Area mode: timing library

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Gate Instances Area Library

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AND2X2 4 53.222 tsmc18

AOI221XL 1 23.285 tsmc18

AOI22X1 4 66.528 tsmc18

AOI22XL 3 49.896 tsmc18

AOI2BB2X1 1 23.285 tsmc18

DFFRHQX1 17 1187.525 tsmc18

DFFRHQXL 1 69.854 tsmc18

DFFRX1 1 76.507 tsmc18

DFFRXL 3 229.522 tsmc18

DFFSHQX1 1 66.528 tsmc18

DFFSX1 2 126.403 tsmc18

DFFSXL 1 63.202 tsmc18

INVX1 5 33.264 tsmc18

INVX2 1 9.979 tsmc18

INVX4 1 13.306 tsmc18

INVXL 5 33.264 tsmc18

NAND2BX1 1 13.306 tsmc18

NAND2BXL 1 13.306 tsmc18

NAND2X1 4 39.917 tsmc18

NAND2X2 1 16.632 tsmc18

NAND3BX1 1 16.632 tsmc18

NAND3BXL 2 33.264 tsmc18

NAND3X1 2 26.611 tsmc18

NAND3XL 1 13.306 tsmc18

NAND4BXL 10 199.584 tsmc18

NAND4X1 1 16.632 tsmc18

NAND4XL 1 16.632 tsmc18

NOR2BX1 2 26.611 tsmc18

NOR2X1 9 89.813 tsmc18

NOR2X2 1 16.632 tsmc18

NOR2XL 7 69.854 tsmc18

NOR4BX1 5 99.792 tsmc18

NOR4BXL 2 39.917 tsmc18

OAI21XL 8 106.445 tsmc18

OAI222XL 5 133.056 tsmc18

OAI22X1 3 59.875 tsmc18

OAI22XL 2 39.917 tsmc18

OAI2BB1X1 1 16.632 tsmc18

OAI2BB1XL 1 16.632 tsmc18

OAI2BB2XL 2 46.570 tsmc18

OAI32X1 2 46.570 tsmc18

TLATX1 2 73.181 tsmc18

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total 128 3412.886

Type Instances Area Area %

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sequential 28 1892.722 55.5

inverter 12 89.813 2.6

logic 88 1430.352 41.9

physical\_cells 0 0.000 0.0

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total 128 3412.886 100.0