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Generated by: Genus(TM) Synthesis Solution 17.22-s017\_1

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Module: ASTS

Technology library: tsmc18 1.0

Operating conditions: slow (balanced\_tree)

Wireload mode: enclosed

Area mode: timing library

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Leakage Dynamic Total

Instance Cells Power(nW) Power(nW) Power(nW)

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ASTS 128 93.923 1402092.948 1402186.871