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Toy Cool Programs

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Incorrect Cool Programs

Program - I:

Cause: Type Identifier should begin with capital letter

Error: "incorrect1.cl", line 3: syntax error at or near OBJECTID = string

Compilation halted due to lex and parse errors

Program - II:

Cause: Non-escaped characters shouldnot be present

Error: "incorrect2.cl", line 3: syntax error at or near OBJECTID = string

Compilation halted due to lex and parse errors

Program - III:

Cause: Right Star is missing in comment

Error: "incorrect3.cl", line 7: syntax error at or near ERROR = EOF in comment

Compilation halted due to lex and parse errors

Program - IV:

Cause: case sensitive keyword

Error: "incorrect4.cl", line 2: syntax error at or near TYPEID = False

Compilation halted due to lex and parse errors

Program - V:

Cause: Zero Width Space is not recognized by cool

Error: "incorrect5.cl", line 5: syntax error at or near ERROR = $\342$

Compilation halted due to lex and parse errors

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Correct Cool Programs

The following are some common MIPS instructions which are used in MIPS assembly language.

- $addiu \rightarrow Add$ immediate unsigned (No overflow)
- $sw o ext{Store Word}$
- $move \rightarrow$ Move value stored at address to registers
- $bne \rightarrow Branch on not equal$
- $la \rightarrow Load address$
- $li \rightarrow Load immediate$
- $jal \rightarrow \text{Jump and link}$
- \$so \$s7 \rightarrow Saved values representing final computed results
- \$to \$t9 \to Temporary variables
- \$ao \$a3 \rightarrow Arguments for a subroutine
- $\$ra \rightarrow \text{Return address}$
- class_nameTab section contains information about the classes like str_const parts
- str_const(I) sections contains all the string literals in our code section.
- Function calls corresponds to using branch instructions in the assembly

The MIPS generated codes for the correct programs are as follows:

Program - I : Exponent

```
1 Main.exponent:
    addiu $sp $sp -12
       $fp 12($sp)
3
       $s0 8($sp)
       $ra 4($sp)
5
    addiu $fp $sp 4
6
          $s0 $a0
       $a0 str_const0
    sw $a0 0($sp)
9
    addiu $sp $sp -4
    move $a0 $s0
11
    bne $a0 $zero label6
    la
       $a0 str_const4
13
    li
       $t1 1
14
    jal _dispatch_abort
```

Listing 1: exponent.s

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Program - II: LCM

```
Main.LCM:
    addiu $sp $sp -16
   sw $fp 16($sp)
3
   sw $s0 12($sp)
4
   sw $ra 8($sp)
   addiu $fp $sp 4
   move $s0 $a0
   la $a0 str_const0
   sw $a0 0($sp)
9
   addiu $sp $sp -4
10
   move $a0 $s0
11
   bne $a0 $zero label10
12
   la $a0 str_const4
13
   li $t1 1
14
jal _dispatch_abort
```

Listing 2: LCM.s

Program - III: Parity Check

```
1 Main.parity:
    addiu $sp $sp -12
2
    sw $fp 12($sp)
    sw $s0 8($sp)
4
    sw $ra 4($sp)
    addiu $fp $sp 4
6
    move $s0 $a0
    la $a0 str_const0
8
    sw $a0 0($sp)
9
    addiu $sp $sp -4
10
    move $a0 $s0
11
    bne $a0 $zero label11
12
    la $a0 str_const4
13
    li $t1 1
14
    jal _dispatch_abort
```

Listing 3: parity.s

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Program - IV: Pascal Traingle

```
Main.Pascal_traingle:
    addiu $sp $sp -20
    sw $fp 20($sp)
3
   sw $s0 16($sp)
4
   sw $ra 12($sp)
   addiu $fp $sp 4
   move $s0 $a0
   la $a0 str_const0
   sw $a0 0($sp)
9
   addiu $sp $sp -4
10
   move $a0 $s0
11
    bne $a0 $zero label0
12
   la $a0 str_const4
13
   li $t1 1
14
jal _dispatch_abort
```

Listing 4: Pascal_triangle.s

Program - V: Subsequence Check

```
Main.subsequence:
2 Main.main:
    addiu $sp $sp -12
    sw $fp 12($sp)
4
    sw $s0 8($sp)
5
    sw $ra 4($sp)
6
    addiu $fp $sp 4
    move $s0 $a0
8
    la $a0 str_const0
9
    sw $a0 0($sp)
10
    addiu $sp $sp -4
11
    move $a0 $s0
12
    bne $a0 $zero label13
13
    la $a0 str_const5
14
    li $t1 1
15
   jal _dispatch_abort
```

Listing 5: subsequence.s

LATEX generated document