

Lab assignment 5 / Week 5

To be done during week 25/03/19 - 01/04/19.

Preparation for the lab

Please update your code with the repository. In your "intro_hdl" folder do:

```
git pull
```

The new folder is named "comm" and it contains two subfolders "serial" and "jtag"

Task 1. Go to folder named "serial".

A. Look at the test bench, you have to write a testbench to write the ascii letter "A" using the uart module. The uart module comes from the PDF file that is contained in the folder.

B. In the next task you will use a memory array to write a sequence of letters to do the same, e.g. write "hello world" using what you have done in A.

Task 2.

Go to jtag folder.

A. Run the code and analyse using a testbench.