10 11 2022 Intermediate Code Greneration Static > ICG ilm code Code - front end -> includes type checking Intermediate representations: High-level/low-level Src Pam > Highlul > . . > Low Trees. > Target Code

Slm rep. Lyl Ilm rep. Directed Acyclic Graph (DAGI): - identifies common subexpressions of expression -efficient syntax tree. -Ex: DAG for a+ a + (b-c) + (b-c) +d dx(E-6)+(c-6)+a+a deb-+cb-a++a+ Postfir. aabc-x+bc-dx+ Three-Address Code: -Atmost one operator on RHS of instruction -> 3-addr. code for prev. DAG: t3= a+ta ta= a*t1 | t4= t1*d HOROGONA to= t3+t4

> Built from a concepts: address and instructions * Address: -constant - compiler generated temporary * Instructions: - Assignment: x=40p= x= op y - Copy: x = 4 - Unconditional jump: goto L - Conditional jumps: if a goto L if False goto L if x relopy goto L -Procedure calls: param x, param 20 (return 4) param an callp,n /y=callpin - Indexed copy: x=4[i] x[i]=4 - Address & Pointer assignments: a= Ly 3(4) hald (x) har the *x=4 sets rul(x) to the contents at really) Quadraples: y: pointer -7 Called as quad. set's rvalue of obj. pointed & -> 4 fields: by x to rvalue of y No of the

-> Edge cases:

- x= minus y (or) x=y do not use arga
- param operator neither use args nor result
- Jumps put target label in results.

>Ex: a=bx-c+bx-c;

3-addr. code:	Op argi arge					result
$t_1 = minus c$ $t_2 = b * t_1$	nîn	0	minus *	c b	tı	t1 t2
t3 = minusc			ninus *	c •	t3	t3
ta= b*t3 t5= t2+t4		4		t ₂	ta	ts a
a = t5		5	=	ots		~

Triples:

-op -argi -arg2

-> Result is referred by position

Indirect Triples:

-> An optimizing compiler can move an instruction by reordering inst list without affecting triplets

> Ex: a= b+-c+b+-c

inst	eruction	n 🕾	indirect triples			iples
400	,			op	argi	arg2
35	(0)		0 N	iinus	C	
36	(1)		1	*	Ь	(0)
38	(3)	,		ninus		(2)
39	(4)	-	3	*	(ı) p	(3)
фo	(5)	1	4	+	۵	(4)
	Į.	1	D	, –	0.0	

Static Single-Assignment Form:

-> I'm rep. that facilitates code optimizations.

-> All assignments in SSA are to variables with distinct names

Duderi	C HEVILLE	525-77 A
→ Ex:	3-addr.code	88A
	p=a+b	$p_1 = a + b$
	9=p-c	a1 = p1-c
	p=qxd	p2 = 91 +d
	p=e-p	p3 = e- p2
	q= p+q	Q3 = P3+Q1

> How to handle a control paths for a variable value? Ex: if (flag) x=-1; else x=1; y=x+a; Here, SSA was a notational convention called the ϕ -function: if (flag) $x_1 = -1$; else $x_2 = 1$; $x_2 = \phi(x_1, x_2)$;

 $x_3 = \phi(x_1, x_2);$ $y = x_3^* \alpha;$

Types and Declaration:

Type checking: - uses logical rules to reason about the behaviour of a pam at run time.

- ensures that the type of an operand matches with the type expected by an operator.

- Translation Applications: - From type of name, compiler determine storage needed for name at run time - Calculate addr. by array ref., explicit type conversions, correct version of arithmatic op.

Type Expressions (TE).

- Either a basic type or is formed applying type constructor operator.
- Basic Type: boolean, char, integer, float, void
- Type name
- Array type const. to a number and TE
- Record: DS with named fields -> Applying record type const. to fieldnames & types.
- 3 >t: function from type s to type t.
- 8xt is also TE (if s and t are TE's)
- TE may contain variables whose values are TE.

Type Equivalence:

- If a TE are equal, then return a certain type; else error.

- Ambiguity: - when names are given to TE, and those

names are used in subseq. TE.

- Whether a name in a TE stands for itself or it is an abor. for another TE.

-TE -represented by Grouph, 2 types are structurally equivalent if & only if lone of the following is true):

* They are same basic type

* Formed by applying same const. to structurally equiv. types

* One is a type name that denotes the other.

Storage Layout (for Local Names):

-> Width of a type - no. of storage units needed for obj.

> In SDT, syn. attr. type & width - for each NT, a variables t & w to pass info.

-> InSDD, + & w - inherited attr.

-> Girammar for type & width:

T-> B &t=B-type; w=B.width; 3

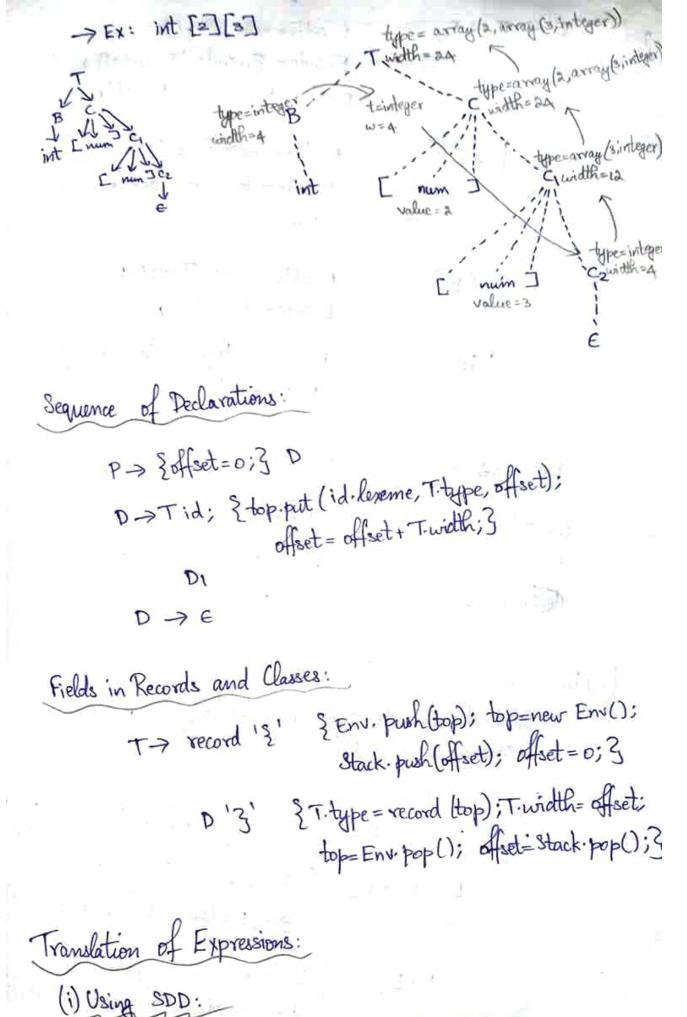
c &T-type = C-type; T-width = C-width;}

B > int {B-type = integer; B width=4;}

B > float & B. type = float; B. width = 8;}

C→E {C-type=t; C-width=w;}

c > [num] c, { c-type=array(num.value, C, type); c-width= num.val x G-width; }

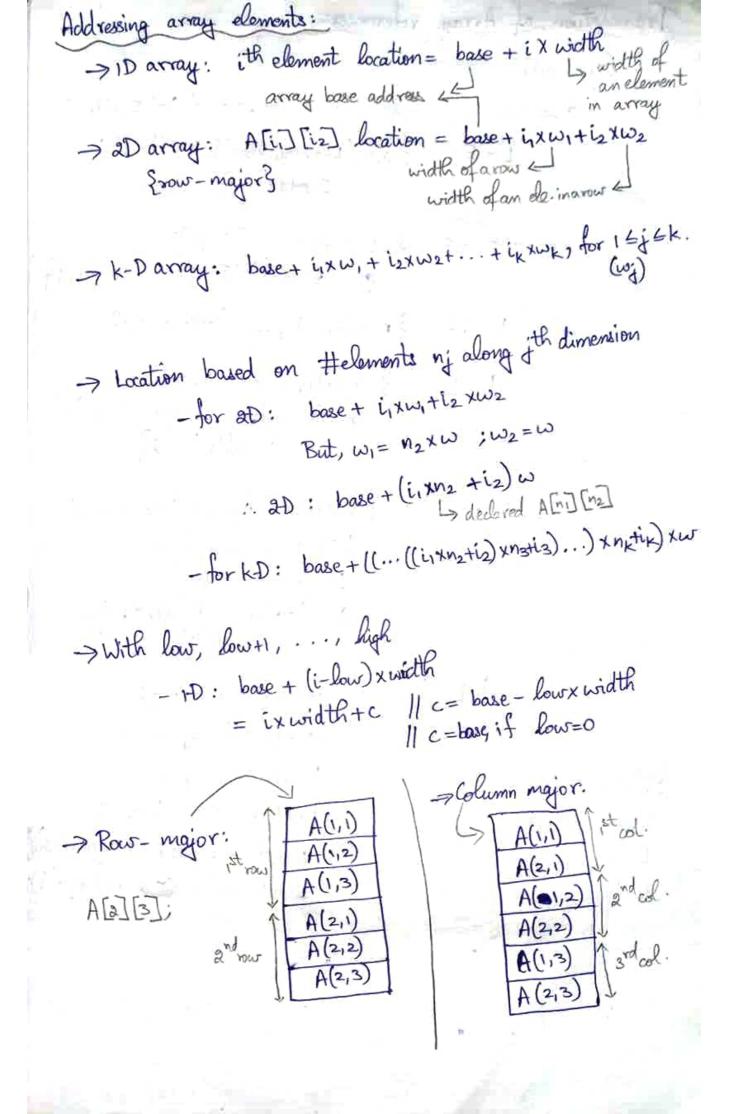


Production Semantic Rules S->id = E; . S-code = E-code || (id-leverne) = E

S-code = E-code || gen (top.get (id-lexeme)'=' E-addr)

E-addr= new Temp(); $E \rightarrow E_1 + E_2$ E.code = E1.code || E2.code || gen(E.addr='E1.addr'+'E2.addr) E.addr= new Temp(); E. code = E1. code | | gen (E-addr'=1 'minus' E1. addr) E. addr = new Temp(); 1 (E) E.code = Ej.code E. addr = top.get (id.lexeme); 1 id E. code = " EX: a=b-c 3-addr. code: ti= minus c t= b+t, a=ta (ii) Using SDT: -Generate 3-addr. code incrementally to avoid long string manipulations. - Grammar: 8 → id = E; Sgen (top. get (id. lexeme) = 'E-addy) E>E1+E2; { E.addr= new Temp(); gen(E.addr = E1.addr+ E2.addr);} { E-addr=new Templ); 1-E, gen (E-addx'= 'minus 'E1-addx);} { E. addr=E1.addr} 1 (E)

{E.addr=top.get(id.lexeme);}



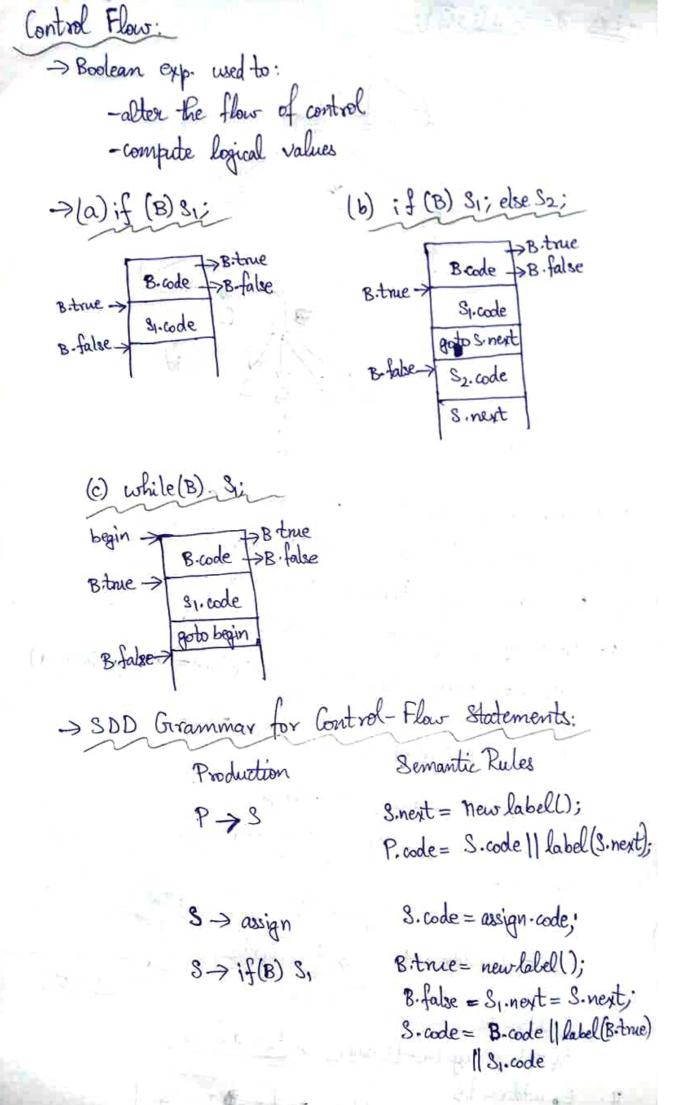
Translation of Array references: - Grammar: ¿gen (top.get(id.lexeme) = E.addr; } } gen (L-array.base ['L-addr'] = E-addr);3 E → E, + E2 { E. addr = new Temp(); gen (E.addr = Eraddr + Ez.addr);} { E. addr = top.get (id·lexeme); } { E-addr = new Temp(); gen (E-addr'=' L-array.base 'C' L. addr'];} { L. array = top.get (id. lexeme); Litype = Larray type elem; L-addr= new Temp(); gen (L. addr'=' E. addr * L.type. width); 3 [LI [E] { Larray = 4. array; L. type = 4. type. elem;

Loddr > igxwg.
Lorray > ptrto ST
Lotype > type of subarr.

gen by L

L. type = L. type. elem; t = new Temp(); L. addr = new Temp(); gen(t'='E. addr'+'L. type. width); gen(L. addr'=' L. addr'+'t);

-Ex: d=c+a[i][]; E(7) [BEL E1. addr = top. get (id. lexeme) => E1. addr = c Eq. addr = top.get (id.lexeme) => Eq. addr = i Es.addr= top.get (id.lexeme) ⇒E5. addr=j 4. 4-array = top.get(id.lexeme) => Lyarray=a Litype = Liarray. type. element => Litype = array(3; integer) ti = Eq. addr * 4. type. width = = i*12 Laddr = to 5. Larray = 4. array = Larray = a L.type = Litype. elem => Litype=integer L.addr= t3 $t_3 = L \cdot addr + t_2 \Rightarrow t_3 = t_1 + t_2$ Eaddr=ta 6. t4= Larray base [Laddr] > [t4= a[t3] to Funday Founday - to= c+ta E, addr=ts



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S > if (B) S, else S2
                                         B. true = new labell);
                                         B-fake = new label ();
                                          S_1.next = S_2.next = S.next;
                                          3. code = B.code | label (
                                                  Bitrue) 1/31. code 11
                             gen (goto S. next) 11 label (B. false) 1/32. code;
                                        begin newlabell);
           S -> while (B) Si
                                        B.true = new label():
                                          B. false = S. next;
                                          Sinext = begin;
                                          8.code = label (begin) 11
                                               B.code 11 label (Btrue)11
                                               31. code | gen ('goto'
                                                begin);
                                   Si. next = newlabell);
          S > S, S2
                                     Sz.next = S.next;
                                     S. code = S1. code | label (S1-next)
                                               1182.code;
-> SDD Grammar for Boolean Expressions:
                                          Semantic Rules
                  Production
                                            B1.true = B.true;
             B -> B1 11B2
                                            B1. false = new labell);
                                            B2.true=B.true;
                                            B2. false = B. false;
                                            B-code = B1-code | label
                                              (B1-false) | B2. code;
                                           B1. false = B. false;
             B -> B1 & & Ba
                                          Bi-true = new label();
                                           B2.true = B.true;
```

Ba. false=B. false;
B. code = B1-code | label (B1:true)
11 B2-code

B -> ! B1

B₁.tme = B-false B₁.false = B.tme B.code = B₁.code

B > E, rel E2

B-code = E1-code || E2-code ||
gen(if' E1-addr rel-op E2-addr
'goto' B.true) || gen
(goto' B.false);

 $B \rightarrow true$ $B \rightarrow fake$ B.code = gen (goto B.true) B.code = gen (goto B.false)

Ex: Generate 3-addr cade for: if (x4100,11 x7200 &&x1.=4) x=0;

3-addr: if x L100 goto L2
goto L3
L3: if x7200 goto L4

goto L1
L4: if xl=y goto L2

L4: if xl=y goto L2

L3: x=0;

J. P.

Alternatively: (using iffalse)

if x 4100 goto La

iffalse x7200 fotoLi

iffalse x!=4 gotoLi

Lz: x=0;

Back pateling:
-> Problem: if (B) S; Exertains a jump on false to S. next
Ly How to determine S. next?
Solution: inherited attributes La requires more than i pass.
lition: Backpatching - list of jumps are
- taget of jump 13 wings
- each jump is put on a list have same -all jumps on a list have same target label.
One Pass Code Greneration using Backpatching: Syn. attr.: B-truelet ? list of jump inst. on which we B falselist I must insert label to which
sinextlist: list of jumps to inst. imm.
> 3 fns: * makelist (i): create new list
* marge (p1, p2): concatenates list p1 21 p2 * merge (p1, p2): concatenates list p1 21 p2 * backpatch (p,i): insert i as target label to each inst. pointed by p.
rust. Political 2

-> Boolean Exp. Grammar

1. B > B1 | MB2 { backpatch (B1-falselist, Minstr);

B. truelist = merge (B1. truelist,

B2. truelist);

B-falselist = B2. falselist; 3

Total Service

- 2. B -> By && MB2 & backpatch (B1. truelist, M. instr);

 B. truelist = B2. truelist;

 B. falselist = merge(B1. falselist,

 B2. falselist);
- 3. B -> ! B,

 B truelist = B, truelist; }

 B falselist = B, truelist; }
- 4. B -> (B) { B. truelist = B1. truelist; }
 B. falsdist = B1. falselist; }
- 5. B > E, rel Ez { B. truelist = makelist (nextinatr); B. talselist = makelist (nextinatr+1); gen ('if' E1. addr rel. op E2. addr 'goto' -'); 11 (gen ('goto -'); }
 - 6. B > true { B-truelist = makelist(nextinstr); gen ('goto '); }
- 7. B > false & B. falselist = makelet (nextinate);
 gen (goto _);
 - 8. M→ ∈ §M. instr = nextinstr;}

-> Control Statements Grammar:

1. 8 > if (B) M S, { backpatch (B. truelist, M. instr); 3. next list = merge (B. falselist, S. next list);}

2. Saif (B) MISIN else M2 S2

& backpatch (B. truelist, M. instr); backpatch (B. falselist, M2. instr); temp= merge (81. nextlist, N. nextlist); S. nextlist = merge (temp, 82. nextlist); 3

3. S> while M. (B) M2 S1

{ backpatch (Si. nextlist, Minster); backpatch (B. truelist, M2. instr); 3. nextlist = B-falselist; gen (goto Miinstr); 3

4· S -> { L }

5. 8-> A;

6. M→ 6;

7. N-> E)

8. L > L,M8

&S.nextlist = L.nextlist; 3

3 3. nextlist = null; 3

&Minsto = nextinstr; 3

EN. nexthat = makelist (nextinotr); gen ('goto - '); 3

3 backpatch (L1.nexthat, Minster); - L. nextlist = S. nextlist; 3

& Linexthat = S. nexthist; 3

9. L->3

Ex: Generate 3 addr code for the following code with & without backpatching: a=c*d if ((~ (a>b)) && (cc=d) 11(f!=0)) p = a+s Ans: (i) without Backpatching: if a76 goto La goto L3 102. L3: if Cc L=d goto B.true goto La La: if fl=q goto B.true goto LI 105: B.true: goto 4: a=b++ Lo: p= a+s m.

(ii) with backpatching:

