



# Windows Internals

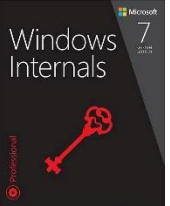
7  
SEVENTH  
EDITION



Professional

02) Debugging

# Chapter Topics

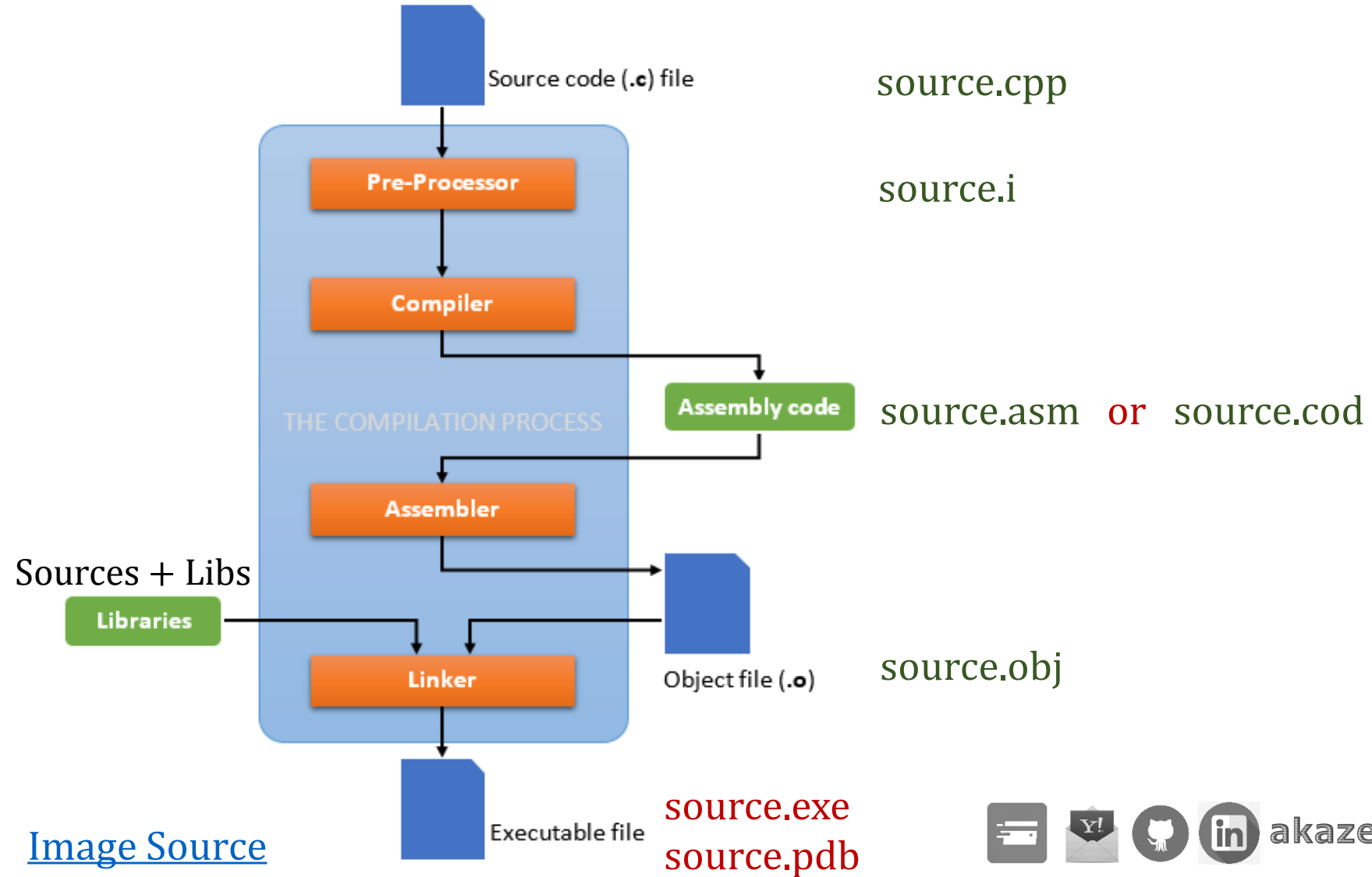


- Program Compilation & Execution
- Assembly Review (x86, x64)
- Visual Studio Debugging
- Introducing Windows Debuggers
- How to use WinDbg [Preview]
- Calling Conventions (x86, x64)
- Live Kernel Debugging
- Examining Windows Structures
- HyperDbg Introduction

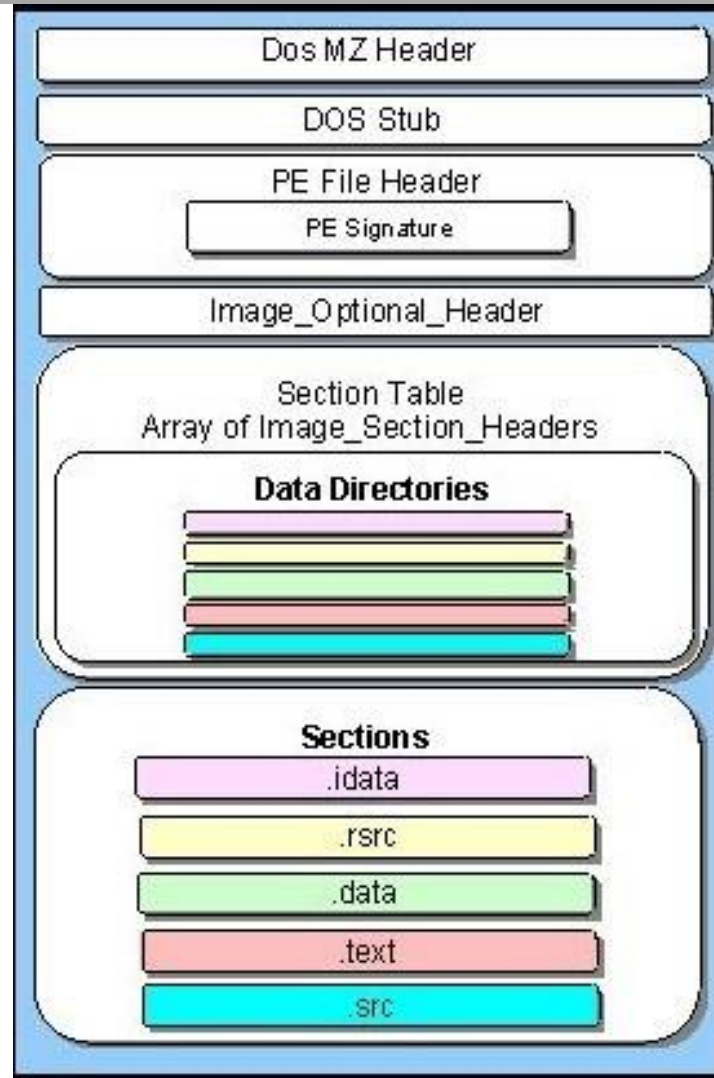


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# Compilation Stages



# Portable Executable Structure

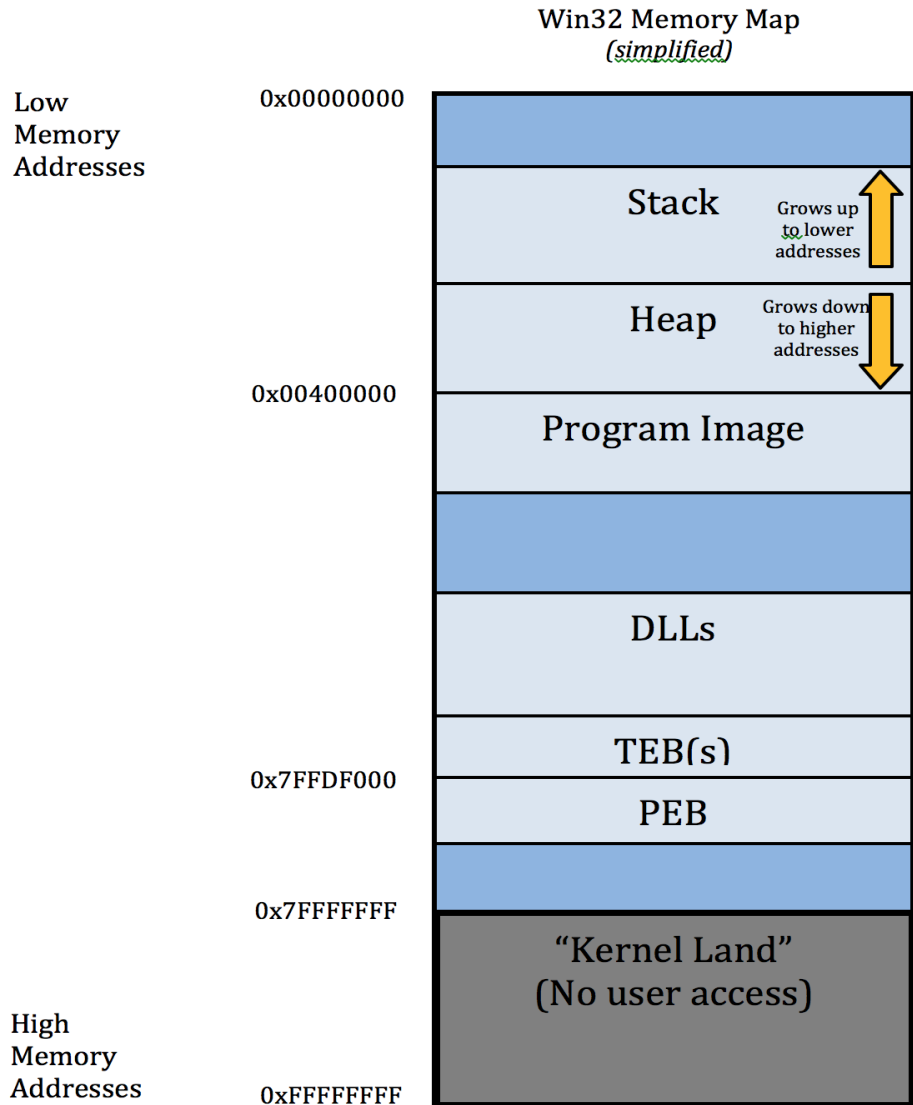


[Image Source](#)



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# Windows Process Layout

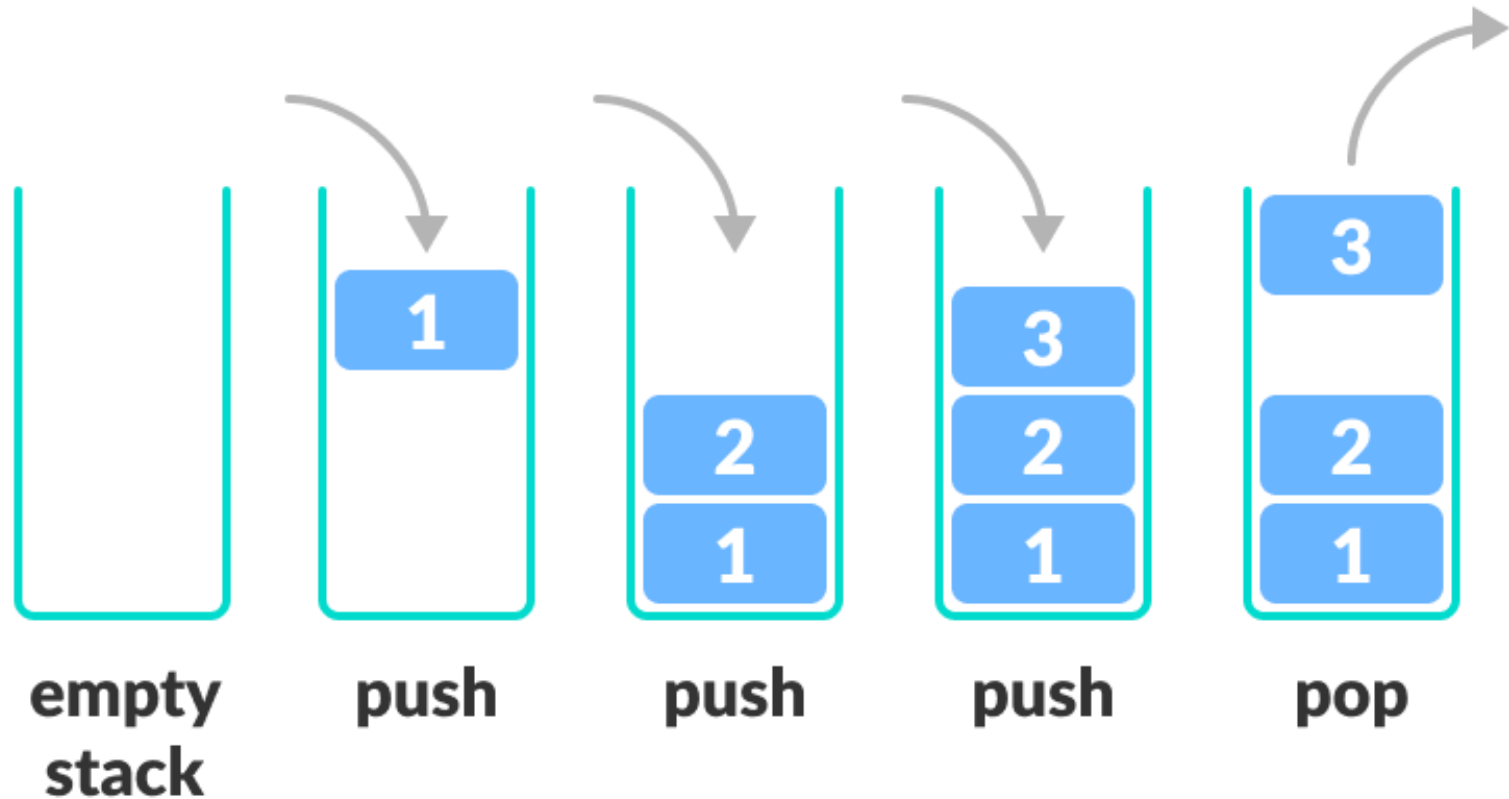


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# Stack Structure



# DEMO

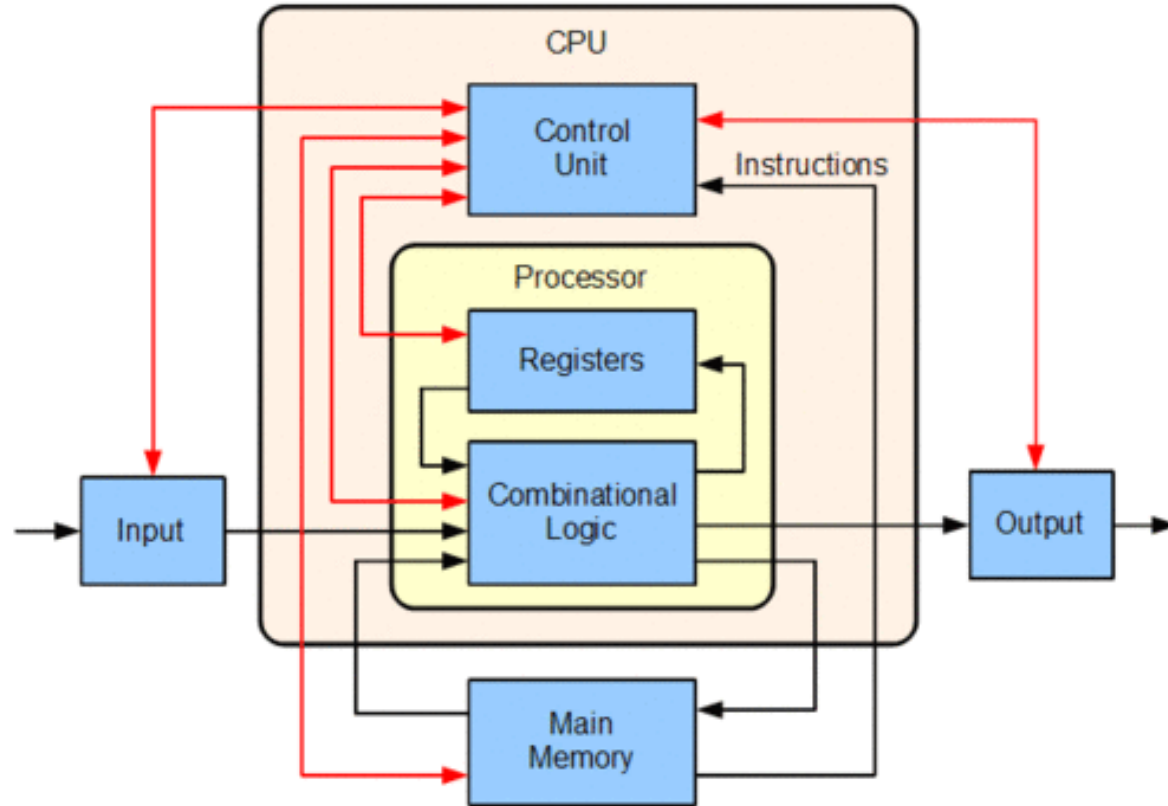


LOADING...



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# Computer Architecture

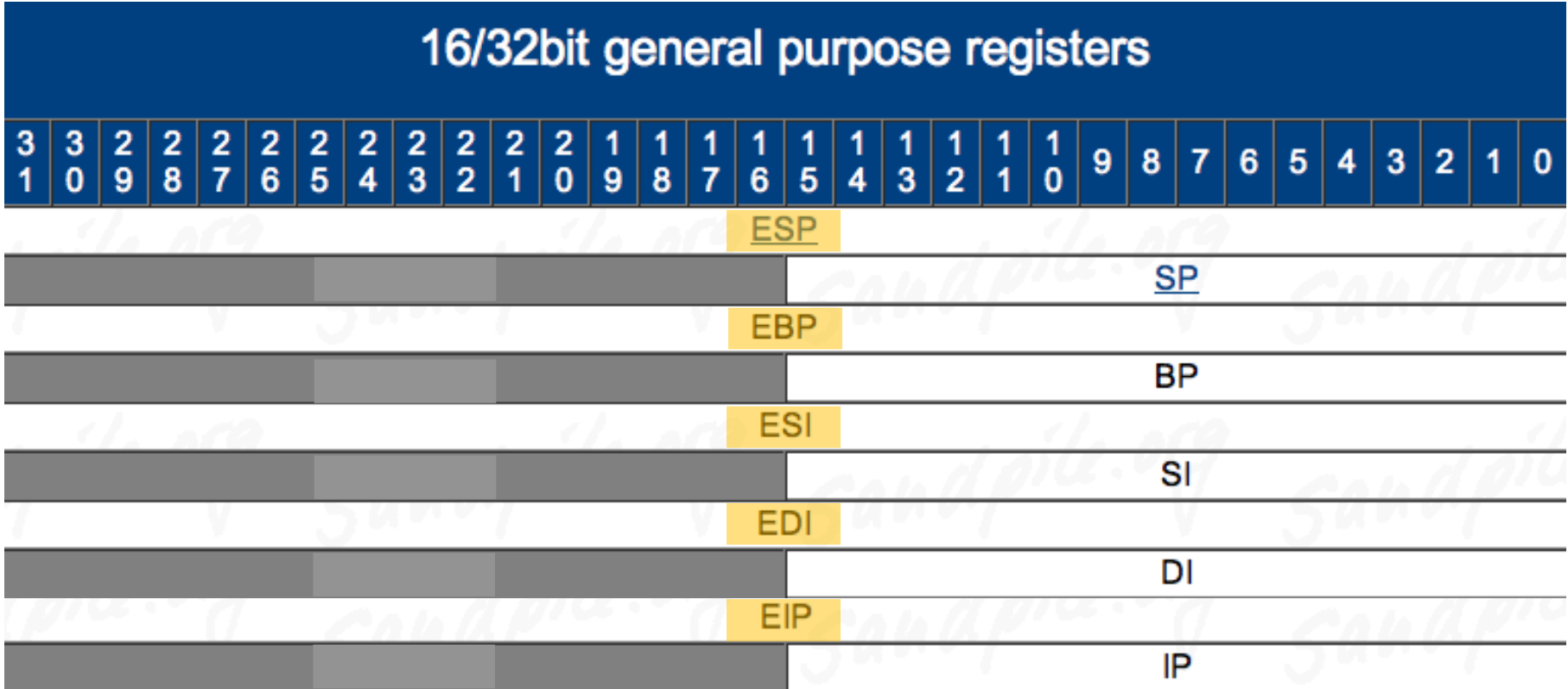




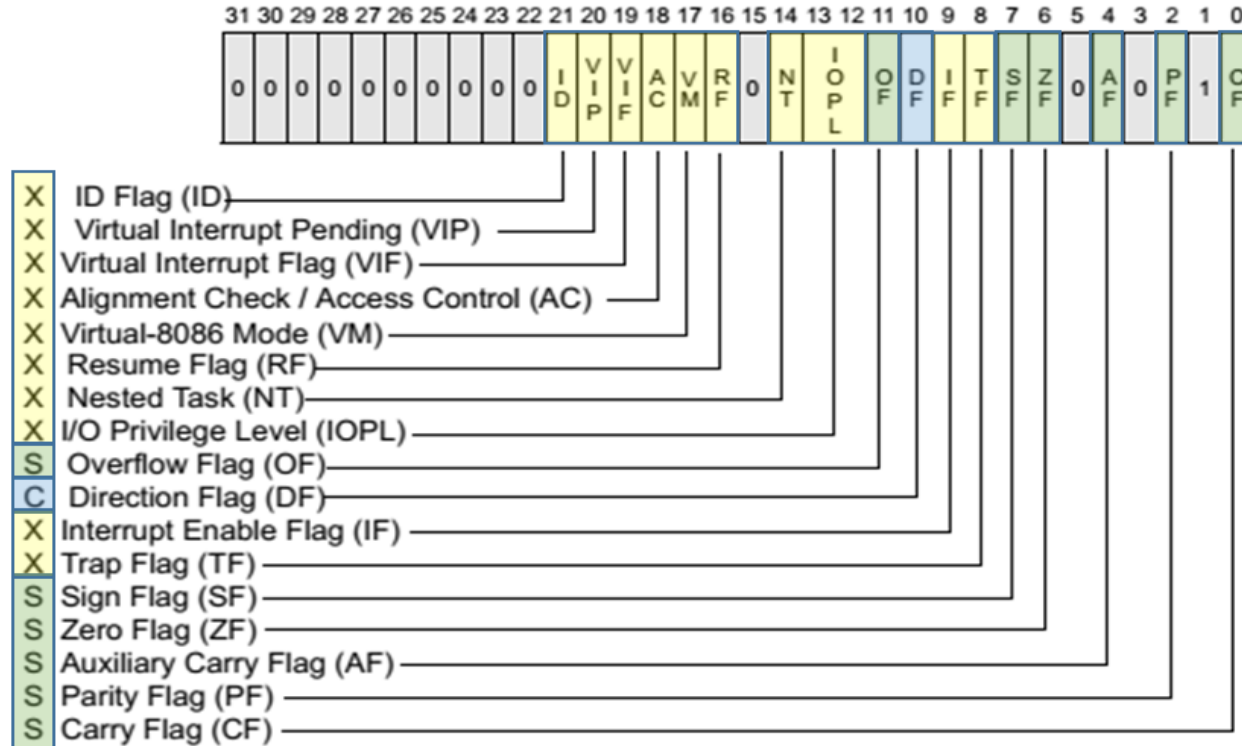
# x86 Registers

8/16/32bit general purpose registers																															
3	3	2	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	1	9	8	7	6	5	4	3	2	1	0	
1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0
																EAX															
																AX															
																AH								AL							
																ECX															
																CX															
																CH								CL							
																EDX															
																DX															
																DH								DL							
																EBX															
																BX															
																BH								BL							

# x86 Registers



# EFLAGS Register



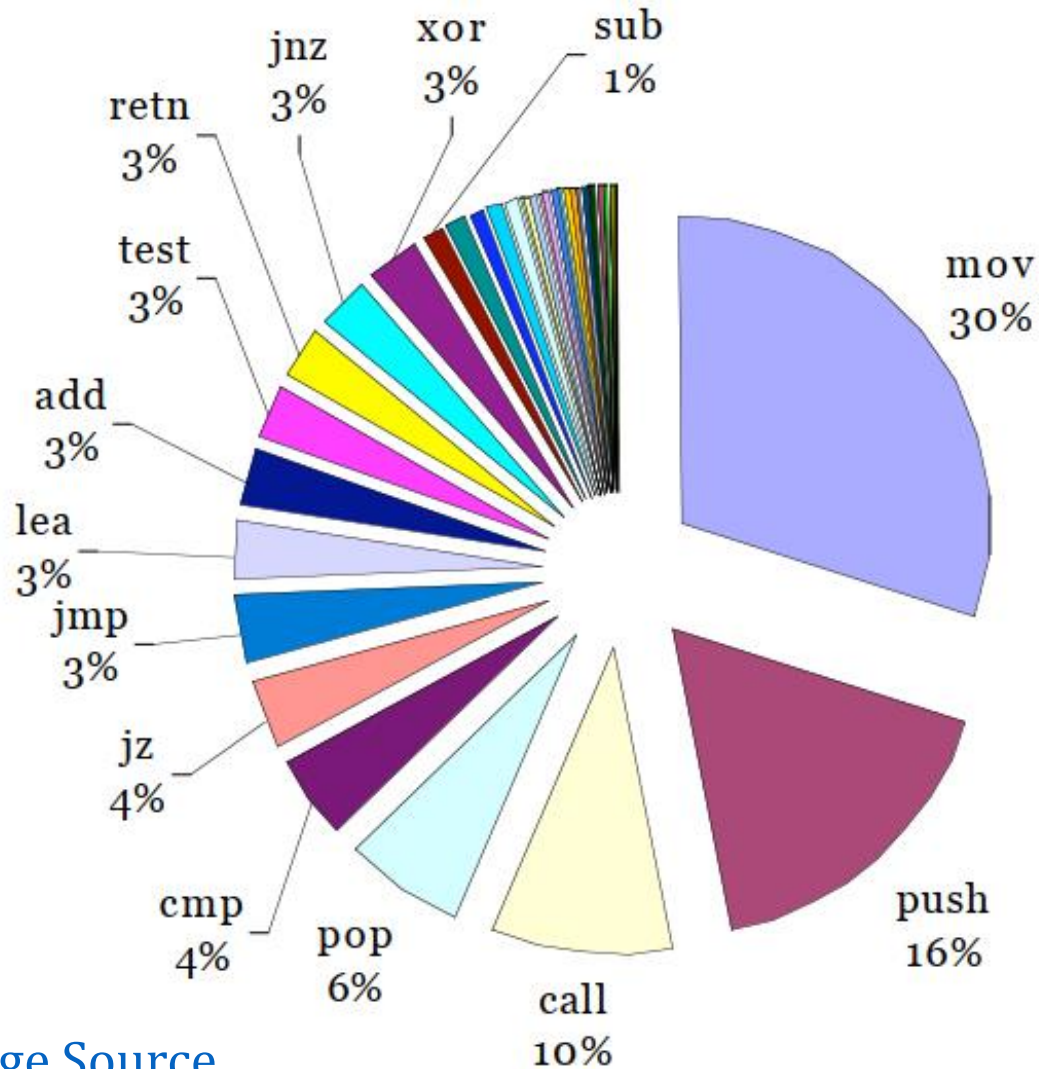
- S Indicates a Status Flag
- C Indicates a Control Flag
- X Indicates a System Flag

IA-32 32-Bit EFLAGS Register

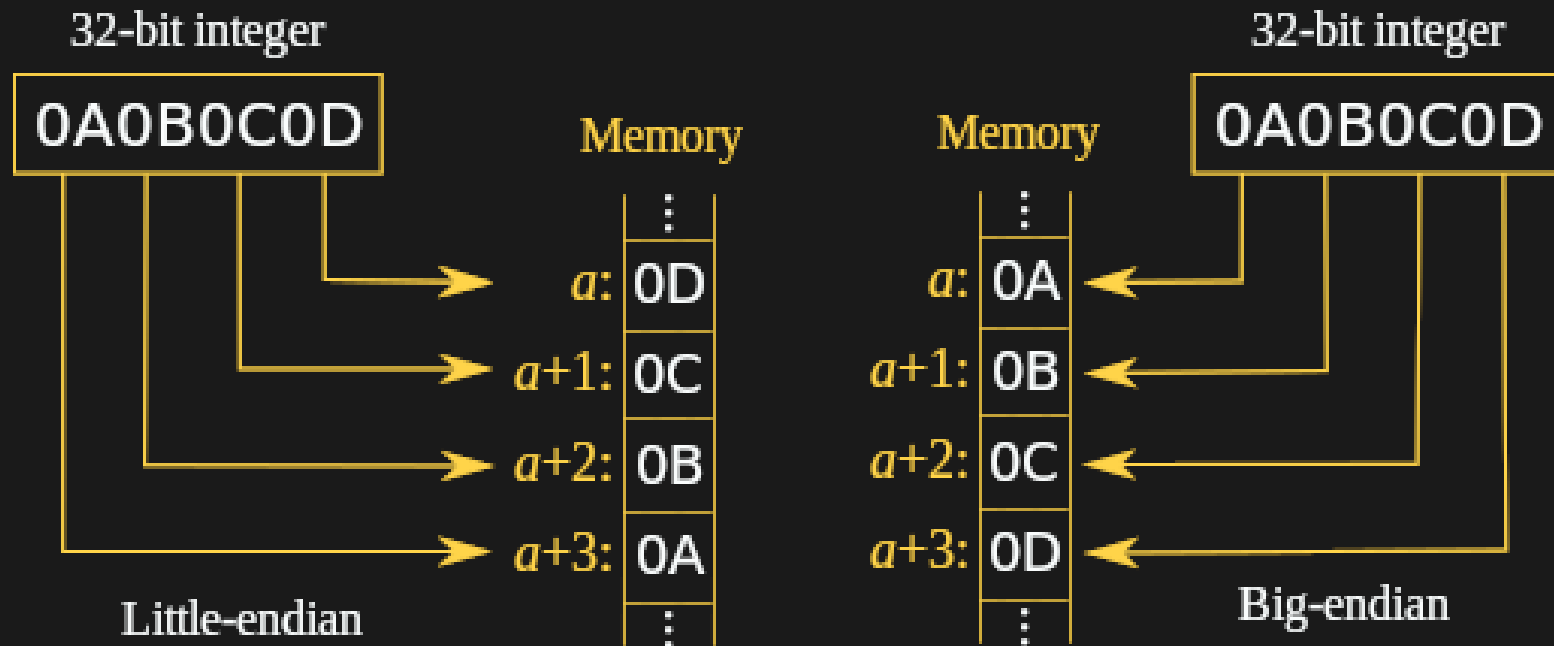
Reserved bit positions. DO NOT USE.  
Always set to values previously read.

[https://blog.csdn.net/qq\\_43401808](https://blog.csdn.net/qq_43401808)

# 90% of Assembly Codes



# Endianness



# DEMO



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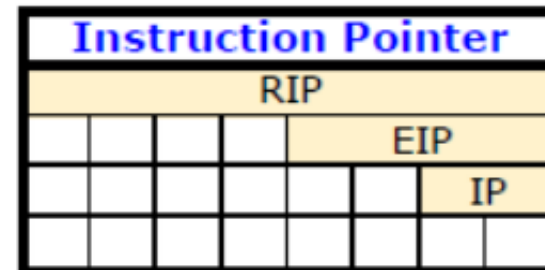
# x64 Registers

## General-purpose regs:



63 ..... 32 31..16 15 ... 0

## Stack management:

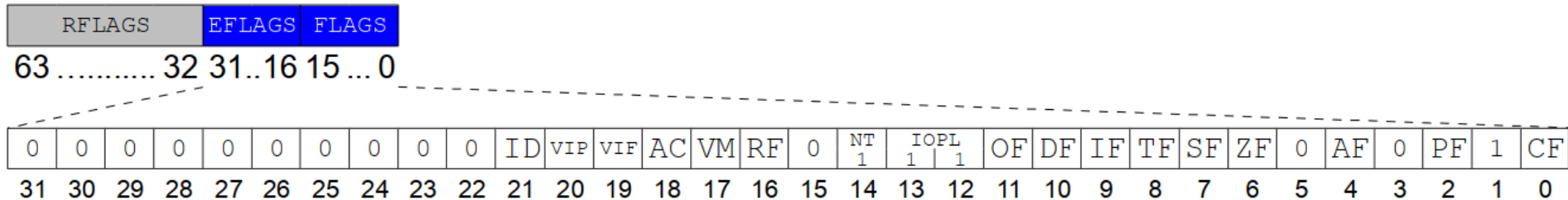


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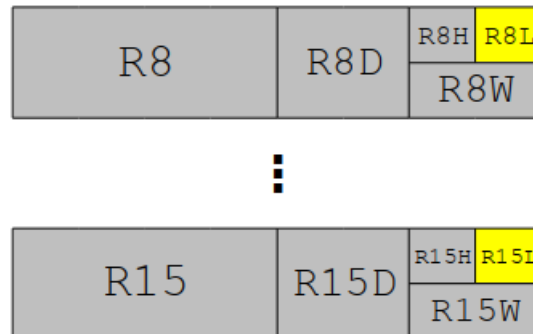
Source

# x64 Registers

## Flags register:



## 64-bit mode registers:





# DEMO



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