# David Zhao Akeley - CV

CS, Mathematics Majors **GPA** 3.762 Primary Email SMS

dza724[at]gmail.com 1-408-763-5241 dakeley[at]nvidia.com **Select Engineering Courses** Parallel & Distributed Computing Advanced Computer Architecture Machine Learning

Formal Langauges & Automata

**Select Math Courses** 

Complex Analysis Honors Algebra Honors Galois Theory Mathematical Modeling

## **Work Experience**

Work Email

## Nvidia Corporation - October 2020 - Present - Developer Technology Engineer

- 1. Collaborated with Nvidia Research on GPU algorithms for SDF-based 2D computational geometry. "How to Accelerate 2D Shape Processing for Manufacturing and Planning" - GTC 2023 (s51140)
- 2. Wrote Vulkan samples for VK EXT graphics pipeline library, VK KHR timeline semaphore, GL\_KHR\_shader\_subgroup\_shuffle, VK\_NV\_inherited\_viewport\_scissor, and ray tracing extensions.
- 3. Designed VK\_NV\_inherited\_viewport\_scissor and implemented its driver and toolchain support.
- 4. Consulted with business partners on integrating Nvidia technology: DMM, GL\_NV\_path\_rendering.

## Sholari LLC - July - September 2019 - Contractor

- 1. Worked on a tumor growth and treatment simulator written with the Unity game engine.
- 2. Implemented tools for visualizing tumor responses to treatments: line graphs, waterfall (bar) plots, and the user interface for the timeline (graph x-axis).
- 3. Wrote a multithreaded C++11 plugin for visualizing tumors & immune system responses as particle clouds, and integrated it with the single-threaded C# Unity Engine.

#### Stanford University – June - September 2018 – Undergraduate Research Assistant

- 1. Helped prototype Aetherling, a Haskell-embedded domain-specific language for designing hardware image processing pipelines with automatic parallelization and static scheduling<sup>2</sup>; co-authored paper with David Durst (lead author), Dr. Kayvon Fatahalian, and Dr. Pat Hanrahan.
- 2. Implemented a functional simulator of an early prototype of Aetherling.
- 3. Revised the type system to remove impediments to parallelizing line buffers.<sup>3</sup>

https://aetherling.org ("Type-Directed Scheduling of Streaming Accelerators" - PLDI 2020)

#### MediocrePy - March - June 2017 - Independent Project

- 1. Created an optimized library for reducing stacks of telescope images to a single image using pixel means or medians and optional outlier rejection (sigma clipping) for noise reduction.
- 2. Designed and implemented a multithreaded C core with AVX vectorization, with an optional NumPy wrapper. Decreased runtime (compared to the Python code replaced) from hours to milliseconds.
- 3. Collaborated with Dr. Zheng Cai, UC Santa Cruz Astrophysics.

https://github.com/akeley98/MediocrePy

#### Tsinghua University – July - August 2016 – Summer Intern

- 1. Designed a small library for fitting and plotting standard microlensing event light curves given discrete measurements of a star's apparent magnitude (brightness) through time.
- 2. Used Python, C++, SciPy, and Matplotlib.
- 3. Collaborated with Dr. Shude Mao, Tsinghua Department of Astronomy.

<sup>&</sup>lt;sup>1</sup>Signed Distance Field

<sup>&</sup>lt;sup>2</sup>i.e. not using ready-valid hardware interfaces.

<sup>&</sup>lt;sup>3</sup>A line buffer device reads in an image as a stream of pixel values and outputs rectangular "windows" of the image.

#### Jide Technology Co. - June - July 2015 - Summer Intern

- 1. Performed product testing for RemixOS, an Android derivative with a desktop-like interface.
- 2. Edited international marketing material and wrote user documentation in English.
- 3. Collaborated with Jason Zheng and Jeff Zhao (International Marketing Manager).

## Other Projects

WebGL Jelly Cube Project Simple mass-spring system simulation written with Javascript, WebAssembly, and WebGL 2.0 (for refractive and reflective effects). Earned third place in the UCLA computer graphics class contest, Fall 2017.<sup>4</sup>

https://github.com/akeley98/JellyMcJelloFace

https://youtu.be/YwvMSeB6NzU

Myricube – Vulkan Voxel Renderer This proof-of-concept hybrid raycast/mesh voxel renderering was inspired by my discontent with Minecraft's low render distance and slow chunk updates, even on high settings.

Conventional voxel renderers such as Minecraft's work by converting voxel models into a mesh of triangles to draw, possibly with some optimizations like hidden face removal or merging coplanar triangles. This uses the GPU for its designed purpose, but as render distance increases, the GPU is bottlenecked by the large number of small triangles generated. Myricube uses conventional mesh rendering<sup>5</sup> for nearby voxels but draws more distant voxels with an alternative raycasting renderer. This renderer subdivides the voxel model into cubic chunks, computes a minimal AABB<sup>6</sup> for each chunk's visible voxels, and draws the AABBs themselves with a raycasting fragment shader that only checks for intersections within the drawn AABB. Dividing the model into AABBs reduces the renderer's memory bandwidth requirements and time wasted raycasting through empty space, without requiring any acceleration structures be built.

Additionally, the project leverages memory-mapped files and Vulkan asynchronous transfers to allow for low-latency, high-throughput model upload and real-time animation.

https://github.com/akeley98/myricube

### Proposed gem5 Hardware Simulator Partial Bypassing Patch

This patch is refactored C++11 code that I wrote for my Advanced Computer Architecture course project.

In an out-of-order CPU, hardware bypassing allows instructions stalled waiting for a register to start execution as soon as the functional unit (ALU, memory port, etc.) writing to said register completes, without waiting for the actual commitment to register file. This is crucial for performance, so mainline gem5 simulates complete bypassing between all functional units in the simulated CPU. However, this is not practical to realize in modern superscalar hardware, as bypassing costs grow quadratically with the number of functional units. My patch splits the CPU's functional units into separate pools, with bypassing simulated only within pools. The commit-to-register-file delay is imposed for values communicated between pools.

The patch received positive code reviews; however, it doesn't look like there's enough interest in the feature to actually merge it into mainline gem5.

https://gem5-review.googlesource.com/c/public/gem5/+/27767

https://github.com/akeley98/FU-pools/blob/b4d291429edb6aa4988888656b6867ff99591b90/fu-pools.pdf

<sup>4</sup>https://www.facebook.com/vasilescu.alex/posts/10155206917936588

<sup>&</sup>lt;sup>5</sup>More precisely, instanced rendering to convert subsets of a list of voxels into a mesh to draw.

<sup>&</sup>lt;sup>6</sup>Axis aligned bounding box – requires only six front-facing triangles to draw.

# Appendix: UCLA Education – September 2017 - August 2020

First Major Computer Science Second Major Mathematics **GPA** 3.762 (August 2020) Title Content Notes CS 174A Intro to Computer Graphics See WebGL Jelly Cube Project 2017 A+ EE M16 Digital Systems Verilog Lab Α+ Linear Algebra Math 115A Α+ Α Math 170A Probability Theory 2018 Software Construction Lab CS 35LPOSIX basics (e.g. pthreads, bash) Α CS M146 Machine Learning Math 110A Algebra Ring Theory Α Analysis Honors Metric Spaces Math 131AH Α Α+ CS 180 Algorithms & Complexity Art of Engineering Endeavors Writing Intensive Team Project Α Engr 185EW Analysis Honors Derivation, Riemann Integration Α Math 131BH Programming Languages B-CS 131 B+ CS M152A Digital Design Lab Verilog Team Project Group Theory Math 110AH Algebra Honors Math 120A Differential Geometry CS 111 Operating Systems Principles Focus on POSIX 2019 Α-Formal Languages & Automata Α+ CS 181 Regex, CFG, Turing Machines, Decidability Math 132H Complex Analysis Honors Α-CS 161 Fundamentals of Artificial Intelligence Α-EE M116C Computer Systems Architecture A+ Overview of p-adic Numbers Theory of Numbers B+ Math 111 Computer Network Fundamentals Α-CS 118 CS 130 Software Engineering В Java Team Project Α Math 134 Systems of Differential Equations 2020 CS 133 Parallel & Distributed Computing OpenMP, OpenCL, MPI, GPGPU, FPGA A+ Advanced Computer Architecture gem5 Hardware Sim Project, Graduate Course Α+ CS 251A Α Math 110BH Algebra Honors Ring Theory, Module Theory Algebra\* Field Theory, Galois Theory Math 110C Α Math 115B Linear Algebra Α Math 142 Mathematical Modeling Game Theory Math 167

## West Valley College Education – 2015-2017

#### **GPA** 4.0

## **Select Courses**

	Title	Content Notes
Math 4B	Differential Equations	
Math 19	Discrete Mathematics	
Psych 2	Experimental Psychophysiology	Experiment Design & Paper
Phys 4D	Modern Physics	Relativity

Typeset in LATEX

Fonts: Computer Modern, FreeSans (GNU FreeFont), Ubuntu Mono (Dalton Maag & Canonical Ltd.)

<sup>\*</sup> There is no honors equivalent to the Galois Theory Course.