Yash Akhauri

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EDUCATION

BITS PILANI | B.E. in Electronics and Instrumentation
Aug 2016 - May 2020 | RJ, India
Fluent: Python3, PyTorch, Mathematica
Familiar: C++, Java, CUDA, OpenMP, Tensorflow, Android Studio, LibGDX, Docker

EXPERIENCE

INTEL | Research Scientist — Present - May 2020, Bangalore, India

XILINX RESEARCH | Visiting Scholar — Aug 2019 - May 2020, Dublin, Ireland

URANIOM | Research Intern — Jan 2019 - Jul 2019, France (Remote)

WOLFRAM | Undergraduate Researcher — June 2018 - July 2018, Massachusetts

RESEARCH EXPERIENCE

PAPERS

LogicNets: Co-Designed Neural Networks and Circuits for Extreme-Throughput Applications
[IEEE [1]] Yash Akhauri*, Yaman Umuroglu*, Nicholas J. Fraser, Michaela Blott — *FPL'20* | Sweden | Sept 2020

High-Throughput DNN Inference with LogicNets
[IEEE [1]] Yaman Umuroglu, Yash Akhauri, Nicholas J. Fraser, Michaela Blott — *FCCM'20* | AR, USA | May 2020

HadaNets: Flexible Quantization Strategies for Neural Networks
[IEEE] Yash Akhauri — *CVPR'19 Workshop* | CA, USA | Jun 2019

Exposing Hardware Building Blocks to Machine Learning Frameworks
[ArXiv -- Bachelor's Thesis] Yash Akhauri — Dec 2019

TALKS

Wolfram Technology Conference
Speaker — Champaign, IL | Oct. 2018

Intel AI Meetup
Speaker — Delhi, IN | Sept. 2018

Intel AI DevCon
Poster — San Francisco, Bangalore | May & Aug 2018

GRANTS

Intel Nervana Early Innovators Grant — $5000

Intel CVPR Travel Grant — $3000

Wolfram Student Aid — $2400

KVPY Scholar

INSPIRE Scholarship

[1] To be published
[1] To be published