Pattern Generator Module

Macro Statistics:

4-bit adder: 14-bit register: 18-bit register: 11-bit latch: 81-bit xor2: 2

Final Register Report

Advanced Macro Statistics:

Flip-Flops : 10

Number of slice LUTs : 8

Error Checking Module

Macro Statistics:

8-bit adder : 9
1-bit comparator : 8

Advanced Macro Statistics:

8-bit adder : 2 8-bit / 6-inputs adder tree : 1 1-bit comparator not equal : 8

Number of slice LUTs : 34

Error Adding Module

Macro Statistics:

8-bit xor2 : 1