



# Certificate of Participation

*This is to certify that*

***Pratapagiri vydya amar akhil***

Of

**Sreenidhi Institute of Science and Technology**

**has successfully completed the 4-Weeks Industry – Institute Collaborative Internship**

on

**Advanced VLSI Design Tools and Embedded Applications with FPGAs**

**Organized by the IEEE Electron Devices Society, Sreenidhi Institute of Science & Technology,**

**Hyderabad held from 1<sup>st</sup> to 26<sup>th</sup> November 2022**

**In association with Pozibility Technologies, Bangalore and Apply Volt Technologies, Hyderabad.**

**Dr. D. Asha Devi**  
Organizing Chair

  
**Dr. C. V. Tomy**  
Director