Q.P. Code: 3387

		(3 Hours) [Total l	Marks :100	
N.	N.B.: (1) Question No. 1 is compulsory. (2) Solve any Three out of remaining questions. (3) Assume suitable data if required.			
1.	Solve (a) (b) (c) (d) (e)	the the following:  Design a circuit to keep LED 'ON' for 30 seconds once circuit is trig. What is CMRR for op-amp and how to measure it practically? Explain first order active filter circuit. Design a 0.5A current source using IC7805. Assume RL =106. Explain 7490 Decade counter.	gered.	
2.	(a)	Design triangular waveform generator for frequency for 5 kHz ar Vopp=6V using op-amp.	nd 10	
	(b)	Explain IC 741 based RC phase shift oscillator with proper wavef Design RC phase shift oscillator to produce sinusoidal frequency of 5 kHz.		
3.	(a)	Design a high pass second order filter for the cut off frequency of and passband gain AF=2.	1 kHz 10	
	(b)	Write the advantages of precision rectifier. Explain half wave pre rectifier along with neat waveforms.	cision 10	
4.	(a) (b)	Design a voltage regulator using IC 723 to give V0=5V and output current Draw instrumentation amplifier using opamp and hence derive equ for output voltage.		
	(c)	Explain zero crossing detector with neat diagram.	4	
5.	(a)	Draw and explain the functional diagram of IC 555 and explain it operation in astable mode.	ts 10	
	(b)	With the lielp of a neat circuit diagram explain the working of 74 synchronous 4-bit binary counter.  Also illustrate the cascading connections for 74163 based counter.		
6.	Write	e short note on the following: 74181 Arithmetic Logic Unit.	20	

JP-Con. 11975-15.

Current foldback protection.

Any two applications of PLL 565. Voltage to frequency converter.

(b)

(c)

(d)