Course > Graded Quizzes (Fall 21) > Graded Quiz 01 (3rd November) > Graded Quiz 1

Graded Quiz 1

The following MCQ questions each carry 1 mark. Total 13*1 = 13 Marks. Each question has one attempt.

Multiple Choice

1.0/1.0 point (graded)

When encoding into machine code, what would be the value of RS and RT field respectively for the instruction: [LW \$7, 36(\$5)]?

00101 and 00111 respectiveley		
00101 and 01100 respectiveley		
O0111 and 00101 respectiveley		
00011 and 100100 respectiveley		
Submit You have used 1 of 1 attempt		

Multiple Choice

1.0/1.0 point (graded)

For loading UTF-16 encoded characters (Each characters are encoded using 16 bits) into a register in MIPS architecture, which of the following instructions should be used?

CLB			
LBU			

○ LH
○ rm
SB
✓
Submit You have used 1 of 1 attempt
Multiple Choice
1.0/1.0 point (graded) For the C code: $x = A[i]$, what is the MIPS code? Here x is in \$s0, base address of A in \$s1 and i is in \$s2, and the array A contains 16-bit data
sll \$s2, \$s2, 1 , add \$s0, \$s2, \$s1
sll \$s2, \$s2, 2 , add \$s0, \$s2, \$s1
sll \$s2, \$s2, 1 , addi \$s0, \$s2, \$s1
sll \$s2, \$s2, 16 , add \$s0, \$s2, \$s1
✓
Submit You have used 1 of 1 attempt
Multiple Choice
1.0/1.0 point (graded)
The instruction BLT is a/an-
R-format instruction
I-format instruction
J-format instruction

Pseudoinstruction	_
Submit You have used 1 of 1 attempt	
Multiple Choice 1.0/1.0 point (graded) Identify the correct encoding of OR \$10,\$11,\$5	
000000 01010 00101 01011 00000 100101	
000000 00101 01010 01011 00101 100101	
<u>0000000 01011 00101 01010 00000 100101</u>	
000000 00101 01010 01010 00101 100101	
000000 00101 01010 11111 00101 100101	
Submit You have used 1 of 1 attempt	
Multiple Choice 1.0/1.0 point (graded) In a procedure call, callee procedure has to save in stack which of the following register's contents?	?
\$a0	
○ \$v1	
✓	

Submit	You have used 1 of 1 attempt
Multiple (Choice
.0/1.0 point (g	
	the exact memory location of A[i] is in \$t0. Which instruction can we use to load the value of \$t1? (Consider the values of array A is in 32 bits)
Olw \$t1, 0	(\$t0)
O lw \$t1,	r(\$t0)
Olw \$t1,	(\$t0)
Olw \$t1,	6(\$t0)
✓	
Submit	You have used 1 of 1 attempt
Multiple (Choice
.0/1.0 point (g How mai	raded) By R-type instructions are possible in a 32-bit architecture?
<u>31</u>	
32	
<u>63</u>	
6 4	
~	

Answer

Correct:

As funct field is 6 bit and opcode is always 0 for R-type, we can have 2^6 = 64 different values of funct field or that many different R-type instructions

Multiple Choice

0.0/1.0 point (graded)

Let's assume \$10 holds initial value of 10. Now perform a 3-bit left shift of \$10 and store that in \$11. Now we perform 2-bit right shift of \$11 and store the value in \$11. We add value stored in \$11 with value in \$10 and save it in \$11.

What is the current value of \$11?

All the options are in binary

11100)		
11110) ✔		
010110)		

Submit

You have used 1 of 1 attempt

• Answers are displayed within the problem

Multiple Choice

1.0/1.0 point (graded)

Suppose, we want to store the values of \$s1 and \$s2 in the stack. If we consider 128-bit Architecture then what is the current position of \$sp after storing the values of \$s1 and \$s2 in the stack?

\$sp = \$sp - 4

\$sp = \$sp - 8

\$sp = \$sp - 16

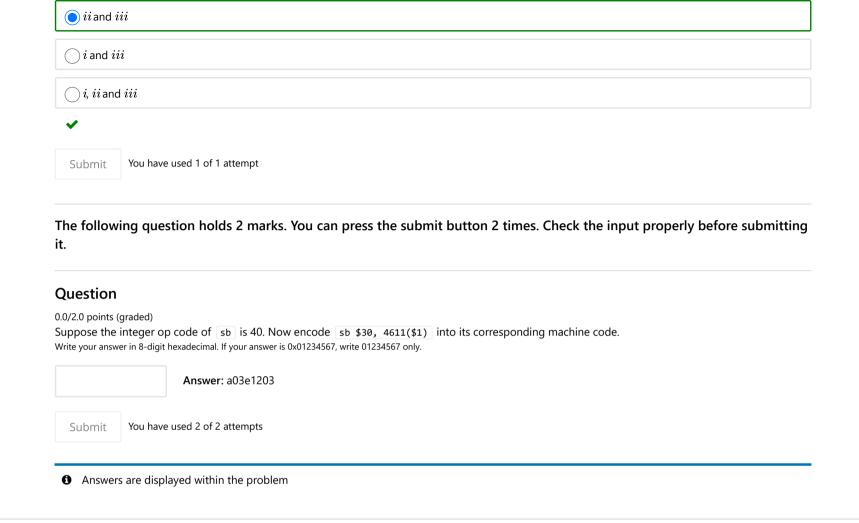
• \$sp = \$sp - 32

\$sp = \$sp - 64

\$sp = \$sp - 128

~

Submit
/Iultiple Choice
.0/1.0 point (graded)
When encoding into machine code, what would be the value of RS and RT field respectively for the nstruction: SW \$5, 12(\$3) ?
00101 and 00011 respectiveley
00101 and 01100 respectiveley
00011 and 01100 respectiveley
00011 and 00101 respectiveley
✓
Submit You have used 1 of 1 attempt
Aultiple Choice
0/1.0 point (graded) uppose you want to subtract 1 from the value that is stored in register \$t1 and then store the result in \$s1. Additionally, register \$t2 also holds 1.
subi \$s1,\$t1,1
i) addi \$s1,\$t1,-1
ii) sub $$s1,$t1,$t2$
Which of the given code snippet(s) can perform the desired operation?
$\bigcirc i$
\bigcirc ii
\bigcirc iii
igcirc i and ii



© All Rights Reserved

About Us BracU Home USIS Course Catalog