## Bipolar Junction Transistors (BJTs)

#### Introduction

- The basic of electronic system nowadays is semiconductor device.
- The famous and commonly use of this device is BJTs
  - (Bipolar Junction Transistors).
- It can be use as amplifier and logic switches.
- BJT consists of three terminal:

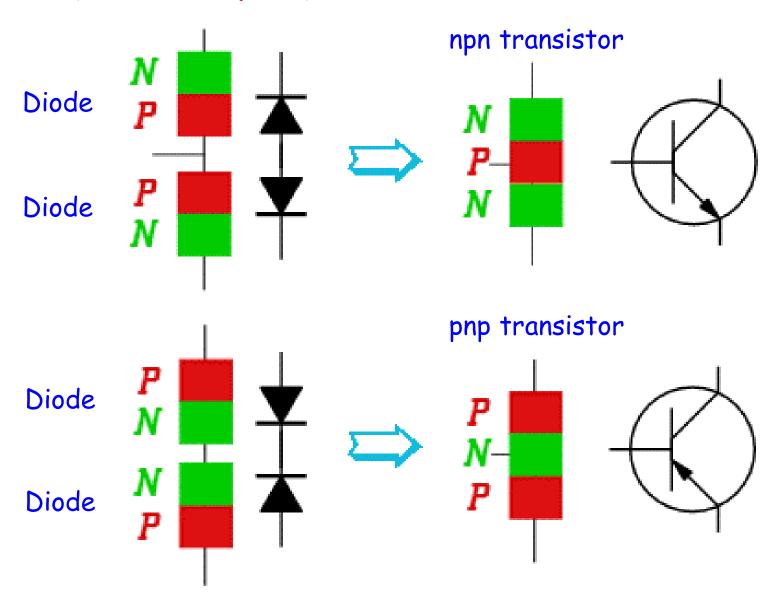
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→ collector : C→ base : B→ emitter : E
```

Two types of BJT : pnp and npn

#### **Transistor Construction**

- 3 layer semiconductor device consisting:
  - □ 2 n- and 1 p-type layers of material → npn transistor
  - □ 2 p- and 1 n-type layers of material →pnp transistor
- The term bipolar reflects the fact that holes and electrons participate in the injection process into the oppositely polarized material
- A single pn junction has two different types of bias:
  - forward bias
  - reverse bias
- Thus, a two-pn-junction device has four types of bias.

## Basic models of BJT



## Qualitative basic operation of BJTs

A BJT consists of two back-to-back p-n junctions.

The middle region, the base, is very thin.

The three regions are the emitter, base, and collector.

Carriers are injected ("emitted") into the base from the emitter.

Since the base is thin, most carriers injected into base diffuse into collector.

What does a "thin base thickness" mean? → Base thickness is much thinner than the diffusion length of carriers injected from the emitter.

Position of the terminals and symbol of BJT.

- Base is located at the middle and more thin from the level of collector and emitter
- The emitter and collector terminals are made of the same type of semiconductor material, while the base of the other type of material

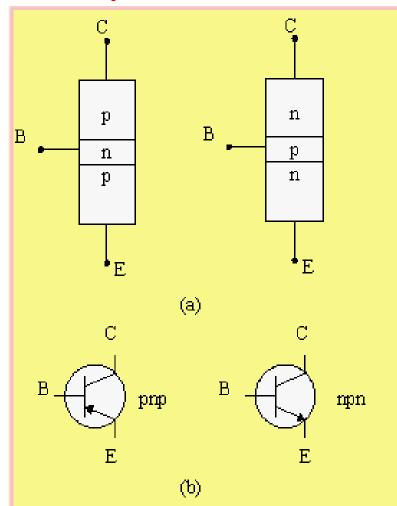
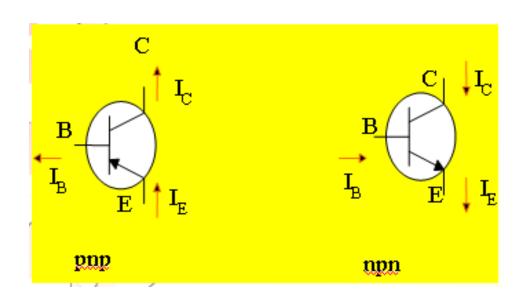


Fig. 4.1: (a) Position of terminals (b) Type of BJT

#### Transistor currents



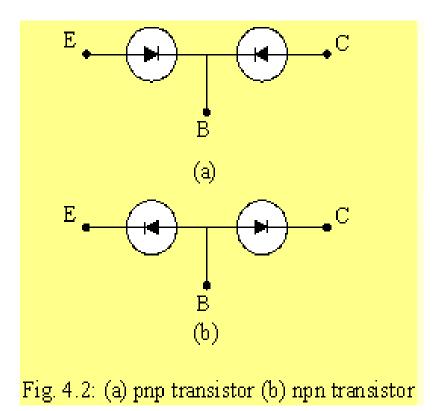
 $I_C$ =the collector current  $I_B$ = the base current  $I_E$ = the emitter current

- -The arrow is always drawn on the emitter
- -The arrow always point toward the n-type
- -The arrow indicates the direction of the emitter current:

pnp:E→ B

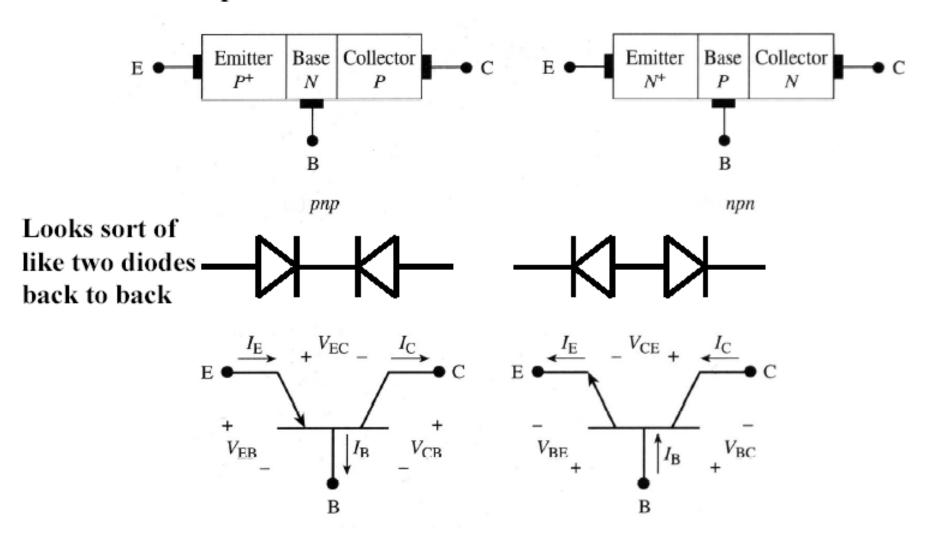
npn:  $B \rightarrow E$ 

- By imaging the analogy of diode, transistor can be construct like two diodes that connetecd together.
- It can be conclude that the work of transistor is base on work of diode.

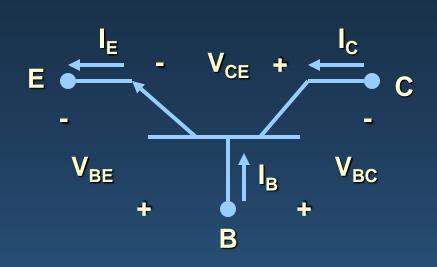


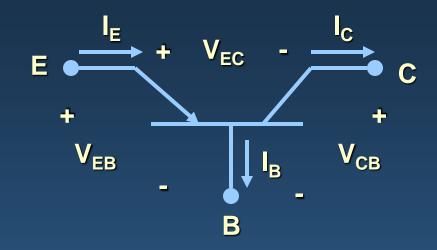
### Basic models of BJT

#### **Bipolar Junction Transistor Fundamentals**



## **BJT Relationships - Equations**





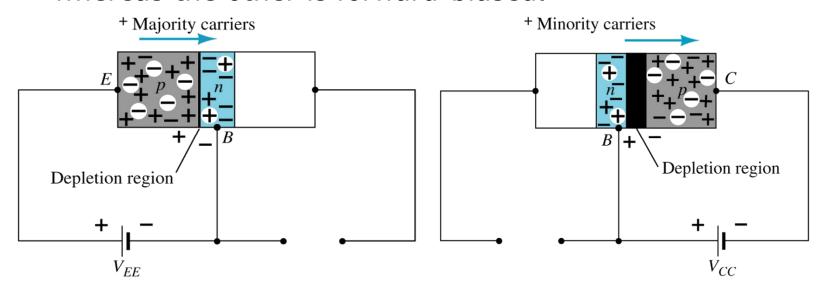
$$I_{E} = I_{B} + I_{C}$$

$$V_{EC} = V_{EB} - V_{CB}$$

Note: The equations seen above are for the transistor, not the circuit.

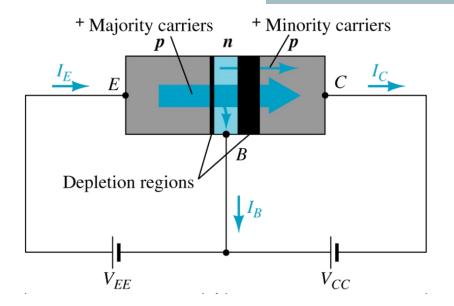
## Transistor Operation

- The basic operation will be described using the pnp transistor. The operation of the pnp transistor is exactly the same if the roles played by the electron and hole are interchanged.
- One p-n junction of a transistor is reverse-biased, whereas the other is forward-biased.



Forward-biased junction of a pnp transistor

Reverse-biased junction of a pnp transistor



- Both biasing potentials have been applied to a pnp transistor and resulting majority and minority carrier flows indicated.
- Majority carriers (+) will diffuse across the forwardbiased p-n junction into the n-type material.
- A very small number of carriers (+) will through n-type material to the base terminal. Resulting IB is typically in order of microamperes.
- The large number of majority carriers will diffuse across the reverse-biased junction into the p-type material connected to the collector terminal.

- Majority carriers can cross the reverse-biased junction because the injected majority carriers will appear as minority carriers in the n-type material.
- Applying KCL to the transistor :

$$I_E = I_C + I_B$$

 The comprises of two components – the majority and minority carriers

$$I_C = I_{Cmajority} + I_{COminority}$$

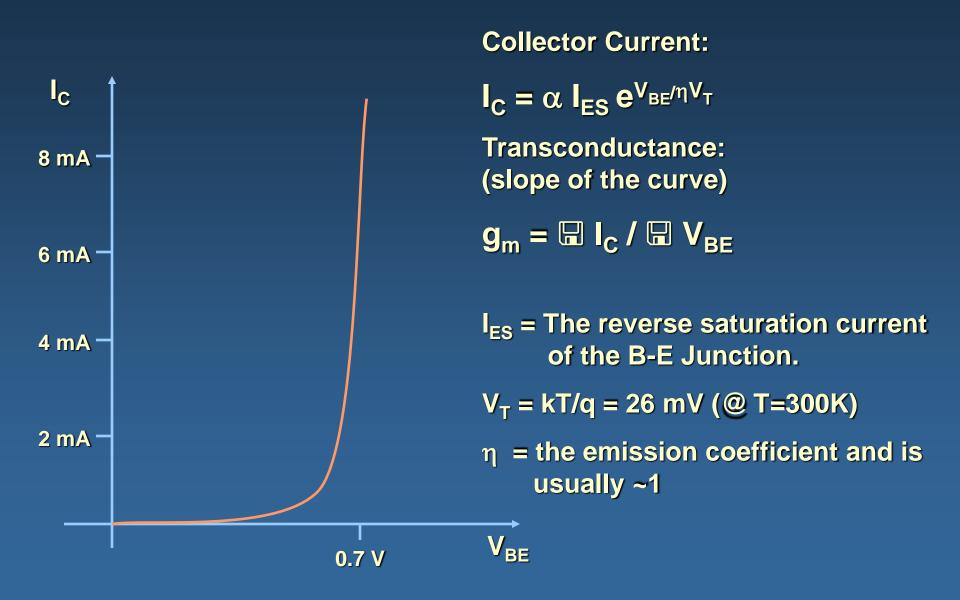
•  $I_{CO}$  –  $I_{C}$  current with emitter terminal open and is called leakage current.

## **Transistor Characteristics**

- The complete behavior of a transistor may be observed with the help of some types of curves known as characteristics curves
- Two types of characteristics curves
  - Input characteristics curves
    - Relates input current and input voltage for a given value of output voltage
  - Output characteristics curves
    - Relates output current and output voltage for a given value of input voltage

## **BJT Transconductance Curve**

Typical NPN Transistor <sup>1</sup>



## **Modes of Operation**

#### **Active:**

- Most important mode of operation
- Central to amplifier operation
- The region where current curves are practically flat

#### **Saturation:**

 Barrier potential of the junctions cancel each other out causing a virtual short

#### **Cutoff:**

- Current reduced to zero
- Ideal transistor behaves like an open switch

\* Note: There is also a mode of operation called inverse active, but it is rarely used.

## Three Types of BJT Biasing

Biasing the transistor refers to applying voltage to get the transistor to achieve certain operating conditions.

Common-Base Biasing (CB) : input =  $V_{EB} \& I_{E}$ 

output =  $V_{CB} \& I_{C}$ 

Common-Emitter Biasing (CE): input =  $V_{BE} \& I_{B}$ 

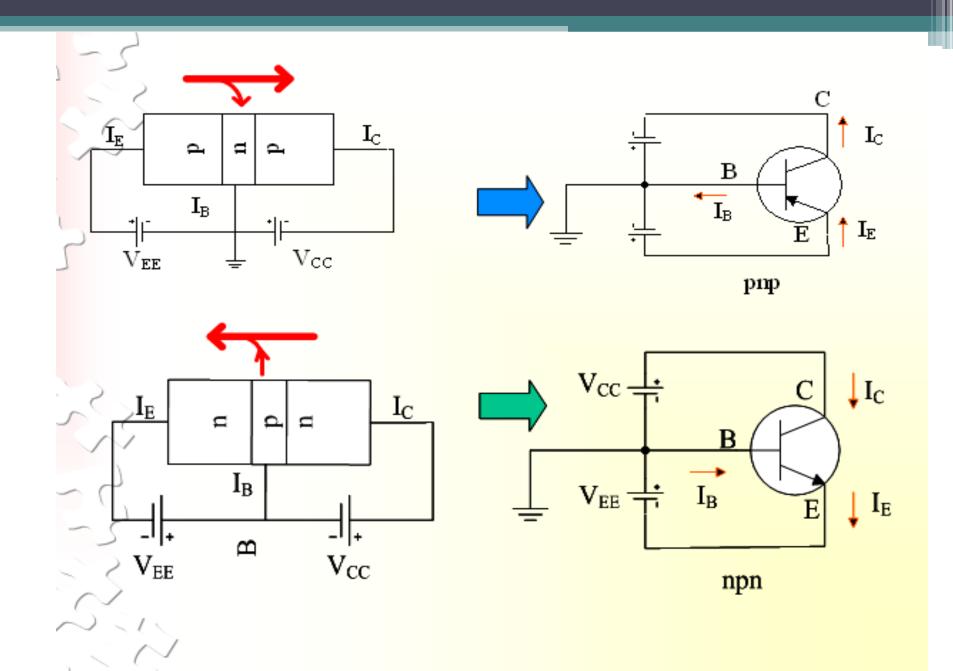
output =  $V_{CE} \& I_{C}$ 

Common-Collector Biasing (CC): input =  $V_{BC} \& I_{B}$ 

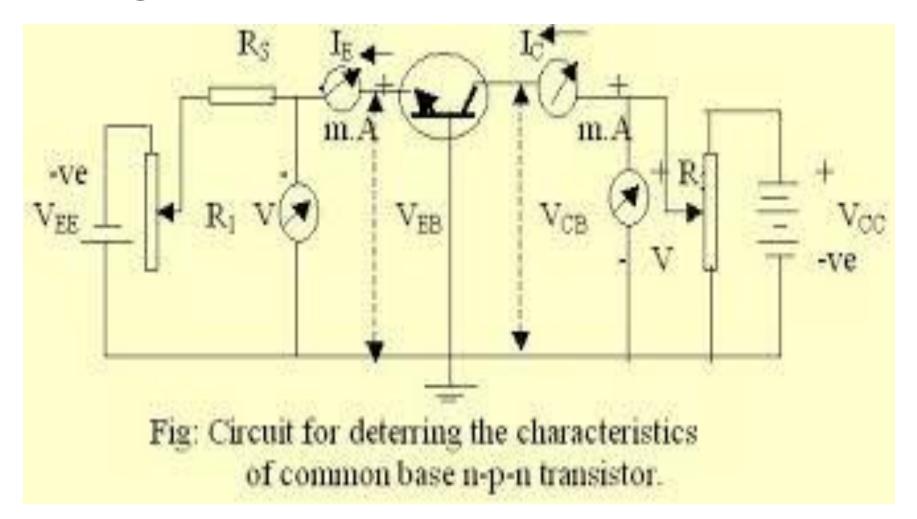
output =  $V_{EC} \& I_{E}$ 

## Common-Base Configuration

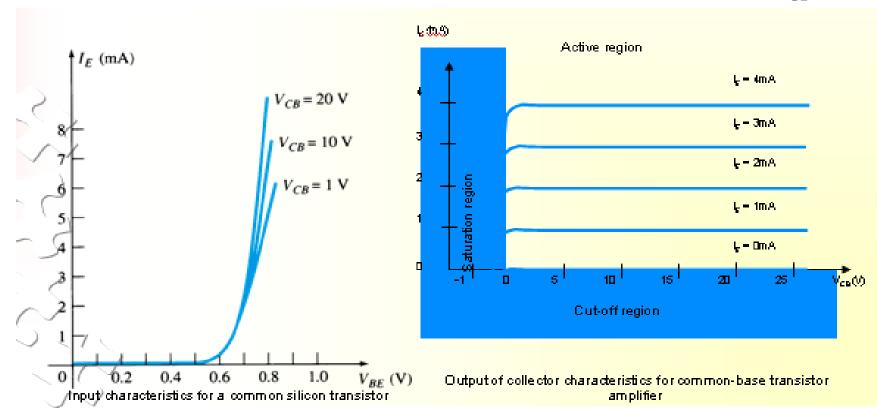
- Common-base terminology is derived from the fact that the :
  - base is common to both input and output of the configuration.
  - base is usually the terminal closest to or at ground potential.
- All current directions will refer to conventional (hole) flow and the arrows in all electronic symbols have a direction defined by this convention.
- Note that the applied biasing (voltage sources) are such as to establish current in the direction indicated for each branch.



# Transistor Characteristics in CB Configuration



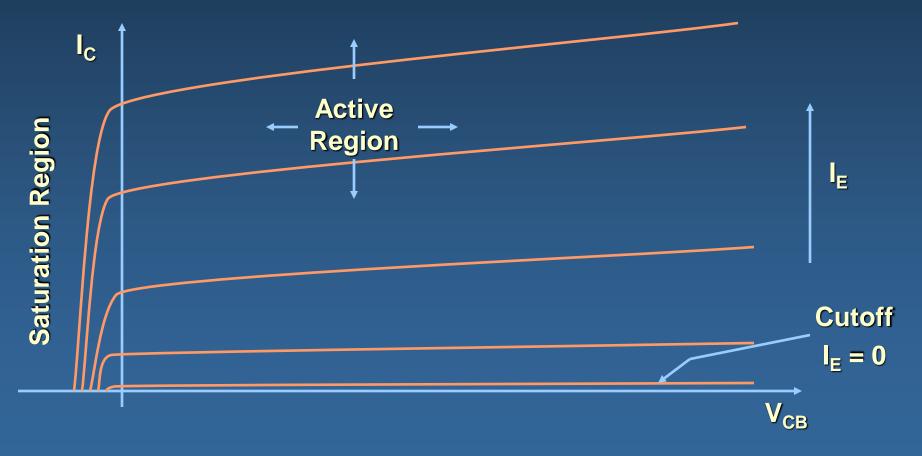
- To describe the behavior of common-base configuration requires two set of characteristics:
  - Input or driving point characteristics.
  - Output or collector characteristics
- The output characteristics has 3 basic regions:
  - Active region -defined by the biasing arrangements
  - Cutoff region region where the collector current is 0A
  - Saturation region region of the characteristics to the left of  $V_{CB} = 0V$



## **Common-Base**

Although the Common-Base configuration is not the most common biasing type, it is often helpful in the understanding of how the BJT works.

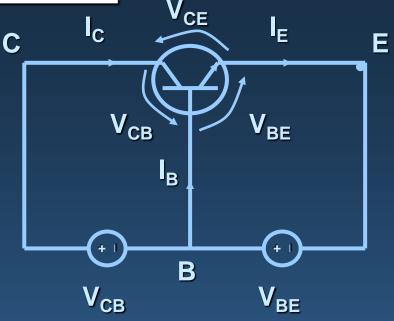
#### **Emitter-Current Curves**



## **Common-Base**

**Circuit Diagram: NPN Transistor** 

The Table Below lists assumptions that can be made for the attributes of the common-base biased circuit in the different regions of operation. Given for a Silicon NPN transistor.



Region of Operation	I <sub>c</sub>	V <sub>CE</sub>	V <sub>BE</sub>	V <sub>CB</sub>	C-B Bias	E-B Bias
Active	βl <sub>B</sub>	=V <sub>BE</sub> +V <sub>CE</sub>	~0.7V	O 0V	Rev.	Fwd.
Saturation	Max	~0V	~0.7V	-0.7V <v<sub>CE&lt;0</v<sub>	Fwd.	Fwd.
Cutoff	~0	=V <sub>BE</sub> +V <sub>CE</sub>	<b>◎</b> 0V	O 0V	Rev.	None /Rev.

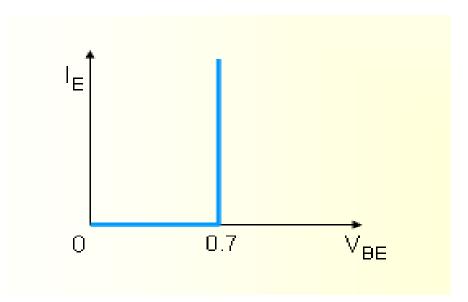
Active region	Saturation region	Cut-off region
•IE increased, Ic increased •BE junction forward bias and CB junction reverse bias •Refer to the graf, Ic≈ IE •Ic not depends on VcB •Suitable region for the transistor working as amplifier	<ul> <li>BE and CB junction is forward bias</li> <li>Small changes in VcB will cause big different to Ic</li> <li>The allocation for this region is to the left of VcB = 0 V.</li> </ul>	<ul> <li>Region below the line of IE=0 A</li> <li>BE and CB is reverse bias</li> <li>no current flow at collector, only leakage current</li> </ul>

• The curves (output characteristics) clearly indicate that a first approximation to the relationship between  $I_{\text{E}}$  and  $I_{\text{C}}$  in the active region is given by

$$I_C \approx I_E$$

 Once a transistor is in the 'on' state, the base-emitter voltage will be assumed to be

$$V_{BF} = 0.7V$$



 In the dc mode the level of I<sub>C</sub> and I<sub>E</sub> due to the majority carriers are related by a quantity called alpha

$$\alpha = \frac{\mathbf{I}_{\mathbf{C}}}{\mathbf{I}_{\mathbf{E}}}$$

$$I_C = \alpha I_E + I_{CBO}$$

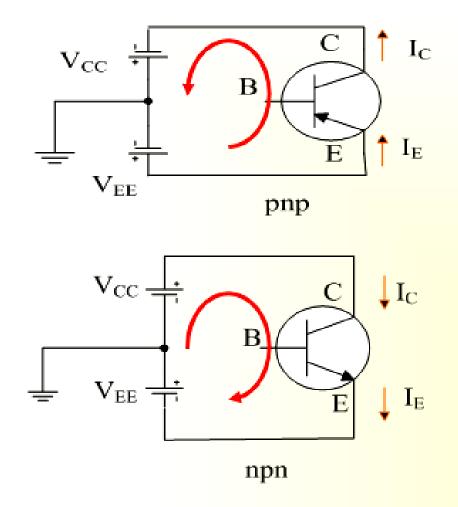
- It can then be summarize to  $I_{\text{C}} = \alpha I_{\text{E}}$  (ignore  $I_{\text{CBO}}$  due to small value)
- For ac situations where the point of operation moves on the characteristics curve, an ac alpha is defined by

$$\alpha = \frac{\Delta I_{\rm C}}{\Delta I_{\rm E}}$$

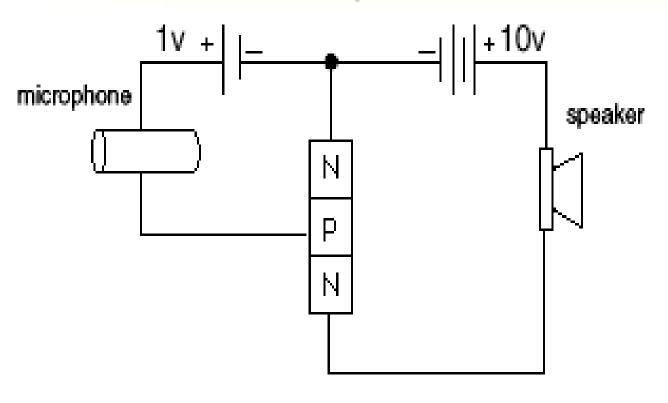
• Alpha is a common base current gain factor that shows the efficiency by calculating the current percent from current flow from emitter to collector. The value of  $\alpha$  is typical from 0.9  $\sim$  0.998.

## Biasing

• Proper biasing CB configuration in active region by approximation  $I_{C}\approx I_{E}$  ( $I_{B}\approx 0$  uA)



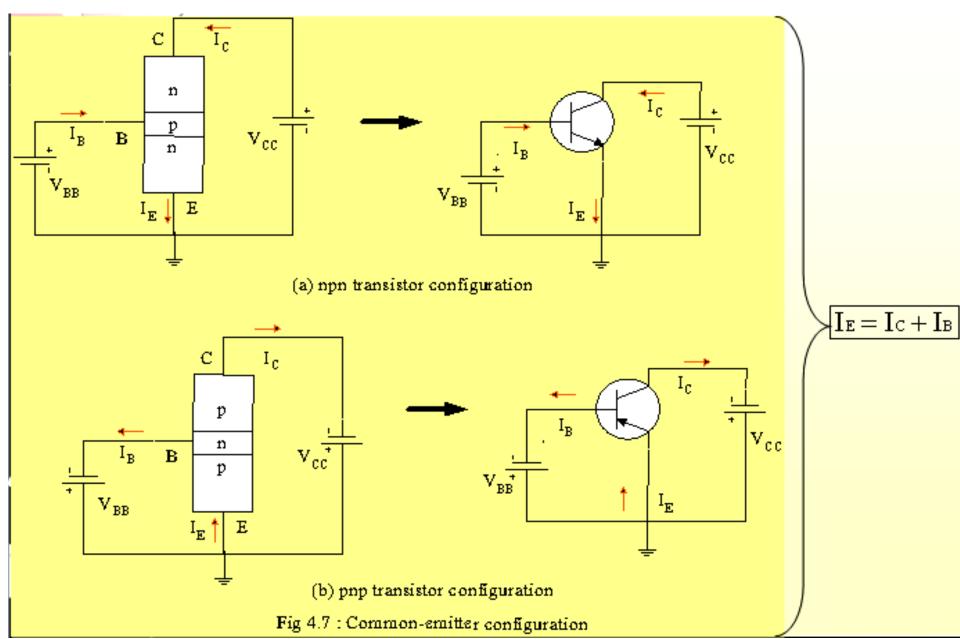
## Simulation of transistor as an amplifier

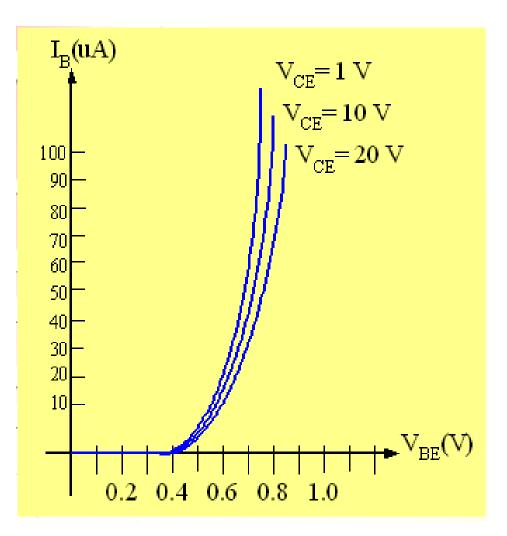


## Common-Emitter Configuration

- It is called common-emitter configuration since :
  - emitter is common or reference to both input and output terminals.
  - emitter is usually the terminal closest to or at ground
    - potential.
- Almost amplifier design is using connection of CE due to the high gain for current and voltage.
- Two set of characteristics are necessary to describe the behavior for CE; input (base terminal) and output (collector terminal) parameters.

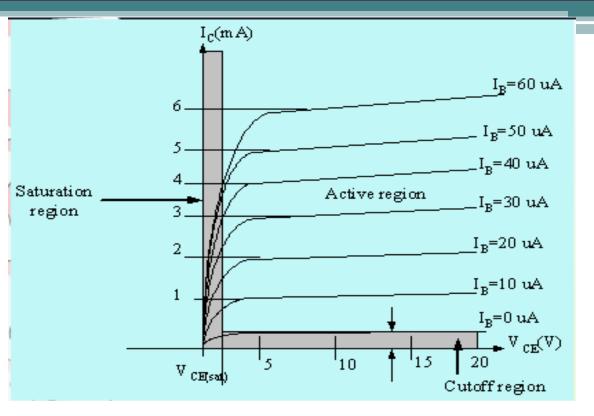
#### Proper Biasing common-emitter configuration in active region





Input characteristics for a common-emitter NPN transistor

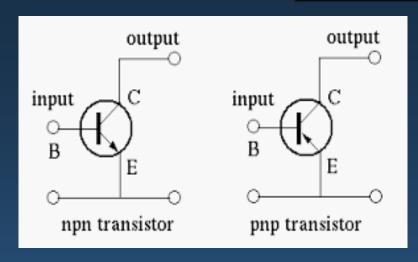
- I<sub>B</sub> is microamperes compared to miliamperes of I<sub>C</sub>.
- $I_B$  will flow when  $V_{BE} > 0.7V$  for silicon and 0.3V for germanium
- Before this value  $I_B$  is very small and no  $I_B$ .
- Base-emitter junction is forward bias
- Increasing V<sub>CE</sub> will reduce I<sub>B</sub> for different values.



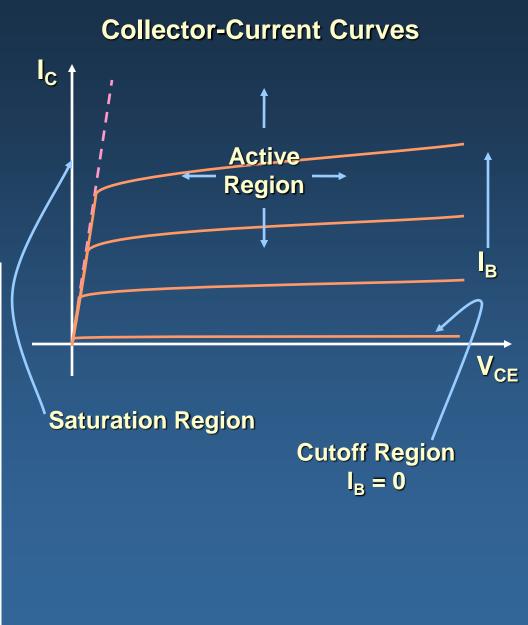
Output characteristics for a common-emitter npn transistor

- For small  $V_{\text{CE}}$  ( $V_{\text{CE}} < V_{\text{CESAT}}$ ,  $I_{\text{C}}$  increase linearly with increasing of  $V_{\text{CE}}$
- $V_{CE} > V_{CESAT} I_C$  not totally depends on  $V_{CE} \rightarrow$  constant  $I_C$
- $I_B(uA)$  is very small compare to  $I_C$  (mA). Small increase in  $I_B$  cause big increase in  $I_C$
- $I_B=0 A \rightarrow I_{CEO}$  occur.
- Noticing the value when I<sub>C</sub>=0A. There is still some value of current flows.

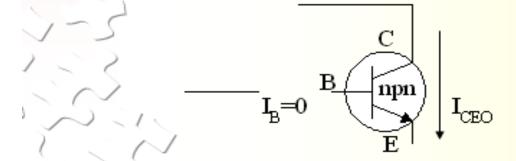
## **Common-Emitter**

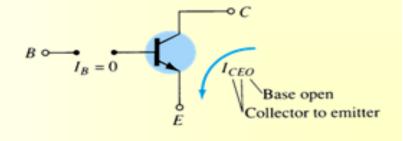


Region of Operation	Description
Active	Small base current controls a large collector current
Saturation	V <sub>CE(sat)</sub> ~ 0.2V, V <sub>CE</sub> increases with I <sub>C</sub>
Cutoff	Achieved by reducing I <sub>B</sub> to 0, Ideally, I <sub>C</sub> will also equal 0.



Active region	Saturation region	Cut-off region
<ul> <li>B-E junction is forward bias</li> <li>C-B junction is reverse bias</li> <li>can be employed for voltage, current and power amplification</li> </ul>	<ul> <li>B-E and C-B junction is forward bias, thus the values of I<sub>B</sub> and I<sub>C</sub> is too big.</li> <li>The value of V<sub>CE</sub> is so small.</li> <li>Suitable region when the transistor as a logic switch.</li> <li>NOT and avoid this region when the transistor as an amplifier.</li> </ul>	<ul> <li>region below I<sub>B</sub>=0µA is to be avoided if an undistorted o/p signal is required</li> <li>B-E junction and C-B junction is reverse bias</li> <li>I<sub>B</sub>=0, I<sub>C</sub> not zero, during this condition I<sub>C</sub>=I<sub>CEO</sub> where is this current flow when B-E is reverse bias.</li> </ul>





## Beta (β) or amplification factor

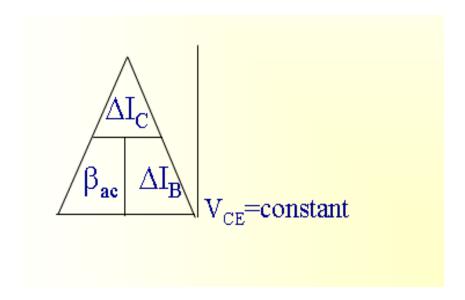
• The ratio of dc collector current  $(I_C)$  to the dc base current  $(I_B)$  is dc beta  $(\beta_{dc})$  which is dc current gain where  $I_C$  and  $I_B$  are determined at a particular operating point, Q-point (quiescent point).

It's define by the following equation:

$$30 < \beta_{dc} < 300 \rightarrow 2N3904$$

• On data sheet,  $\beta_{dc} = h_{FE}$  with h is derived from ac hybrid equivalent cct. FE are derived from forward-current amplification and common-emitter configuration respectively.

- For ac conditions an ac beta has been defined as the changes of collector current (I<sub>C</sub>) compared to the changes of base current (I<sub>B</sub>) where I<sub>C</sub> and I<sub>B</sub> are determined at operating point.
- On data sheet,  $\beta_{ac} = h_{fe}$
- It can defined by the following equation:



## Relationship analysis between a and B

CASE 1 
$$I_{E} = I_{C} + I_{B} \qquad \qquad (1)$$
 subtitute equ. 
$$I_{C} = \beta I_{B} \text{ into } (1) \text{ we get}$$
 
$$I_{E} = (\beta + 1)I_{B}$$

CASE 2

known : 
$$\alpha = \frac{I_c}{I_E} \Rightarrow I_E = \frac{I_c}{\alpha}$$
 (2)

known : 
$$\beta = \frac{I_c}{I_B} \Rightarrow I_B = \frac{I_c}{\beta}$$
 (3)

subtitute (2) and (3) into (1) we get,

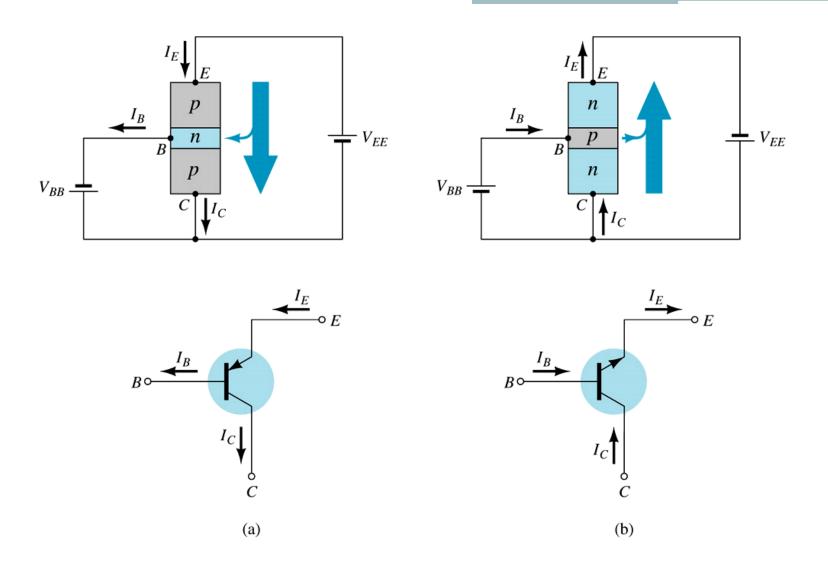
$$\alpha = \frac{\beta}{\beta + 1}$$

and

$$\beta = \frac{\alpha}{1 - \alpha}$$

## **Common – Collector Configuration**

- Also called emitter-follower (EF).
- It is called common-emitter configuration since both the signal source and the load share the collector terminal as a common connection point.
- The output voltage is obtained at emitter terminal.
- The input characteristic of common-collector configuration is similar with common-emitter, configuration.
- Common-collector circuit configuration is provided with the load resistor connected from emitter to ground.
- It is used primarily for impedance-matching purpose since it has high input impedance and low output impedance.



Notation and symbols used with the common-collector configuration: (a) pnp transistor; (b) npn transistor.

• For the common-collector configuration, the output characteristics are a plot of  $I_{\text{E}}$  vs  $V_{\text{CE}}$  for a range of values of  $I_{\text{B}}.$ 

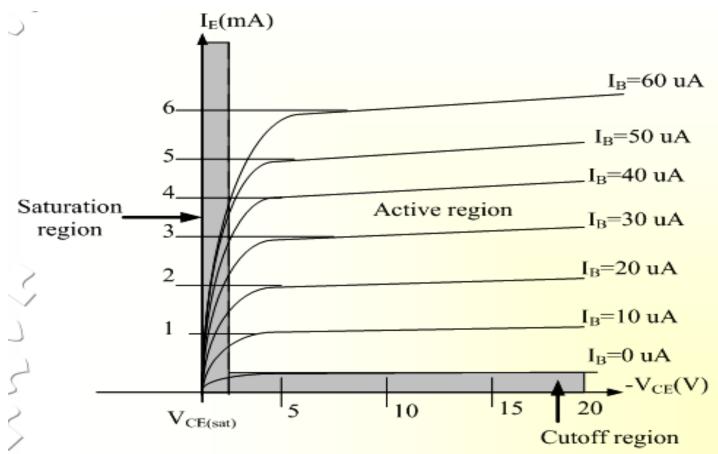
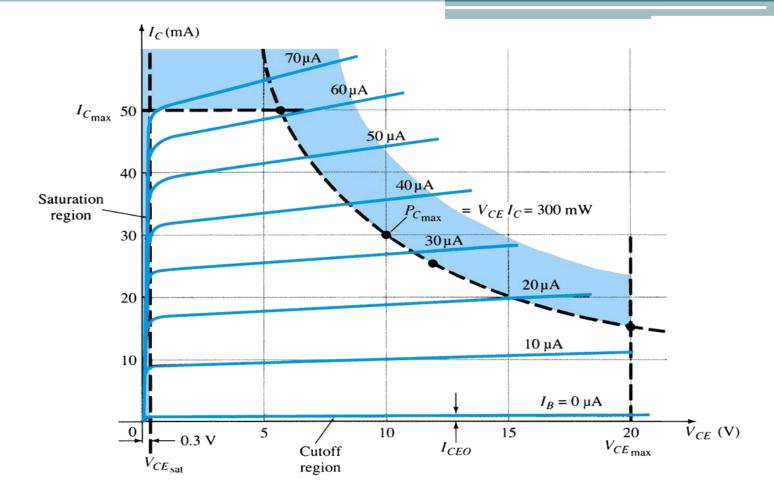


Fig 4.9: Output characteristic in CC configuration for npn transistor

## **Limits of Operation**

- Many BJT transistor are used as an amplifier. Thus it is important to notice the limits of operations.
- At least 3 maximum values is mentioned in data sheet.
- These are:
  - a) Maximum power dissipation at collector:  $P_{\text{Cmax}}$  or  $P_{\text{D}}$
  - b) Maximum collector-emitter voltage:  $V_{CEmax}$  sometimes named as  $V_{BR(CEO)}$  or  $V_{CEO}$ .
    - c) Maximum collector current: ICmax
- There are few rules that need to be followed for BJT transistor used as an amplifier. The rules are:
  - i) transistor need to be operate in active region!
  - ii)  $I_C < I_{Cmax}$
  - ii)  $P_C < P_{Cmax}$



Note:  $V_{CE}$  is at maximum and  $I_{C}$  is at minimum ( $I_{Cmax} = I_{CEO}$ ) in the cutoff region.  $I_{C}$  is at maximum and  $V_{CE}$  is at minimum ( $V_{CE}$  max =  $V_{CESat}$  =  $V_{CEO}$ ) in the saturation region. The transistor operates in the active region between saturation and cutoff.

## Example 1: P<sub>D</sub>: maximum power $I_{c}(mA)$ dissipation line I<sub>B</sub>=60uA I<sub>8</sub>=50uA I<sub>e</sub>≠40uA Saturation/ Active region L<sub>=</sub>30uA region L=20uA L=10uA I<sub>B</sub>=OuA $V_{CE}(V)$ 10 Outoff region

Refer to the fig.

## Step1:

<u>Step 2:</u>

The maximum collector power dissipation,

$$P_{D} = I_{Cmax} \times V_{CEmax}$$
 (1)  
= 18m x 20 = 360 mW

At any point on the characteristics the product of and must be equal to 360 mW. Ex. 1. If choose  $I_{Cmax} = 5$  mA, subtitute into the (1), we get  $V_{CEmax}I_{Cmax} = 360$  mW  $V_{CEmax}(5 \text{ m}) = 360/5 = 7.2 \text{ V}$ 

Ex.2. If choose  $V_{CEmax}=18 \text{ V}$ , subtitute into (1), we get  $V_{CEmax}I_{Cmax}=360 \text{ mW}$  (10)  $I_{Cmax}=360 \text{ m/}18=\underline{20 \text{ mA}}$ 

# Derating P<sub>Dmax</sub>

- P<sub>Dmax</sub> is usually specified at 25°C.
- The higher temperature goes, the less is P<sub>Dmax</sub>
- Example;
  - A derating factor of 2mW/°C indicates the power dissipation is reduced 2mW each degree centigrade increase of temperature.

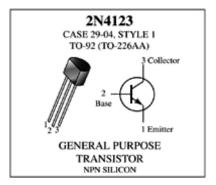
# Transistor Specification Sheet

#### MAXIMUM RATINGS

Rating	Symbol	2N4123	Unit
Collector-Emitter Voltage	VCEO	30	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	40	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector Current - Continuous	Ic	200	mAde
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	625 5.0	mW mW°C
Operating and Storage Junction Temperature Range	T <sub>j</sub> ,T <sub>stg</sub>	-55 to +150	·c

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>µDC</sub>	83.3	'C W
Thermal Resistance, Junction to Ambient	R <sub>silA</sub>	200	.C.M.

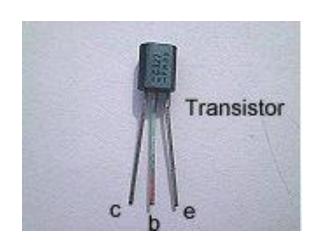


#### ELECTRICAL CHARACTERISTICS ( $T_A = 25$ 'C unless otherwise noted)

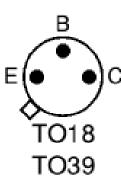
Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage (1) (I <sub>C</sub> = 1.0 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CEO</sub>	30		Vdc
Collector-Base Breakdown Voltage ( $I_C = 10 \mu Adc, I_E = 0$ )	V <sub>(BR)CBO</sub>	40		Vdc
Emitter-Base Breakdown Voltage ( $I_E = 10 \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	5.0	-	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 20 Vdc, I <sub>E</sub> = 0)	Icao	-	50	nAde
Emitter Cutoff Current ( $V_{BB} = 3.0 \text{ Vdc. } I_C = 0$ )	I <sub>EBO</sub>	-	50	nAdc
ON CHARACTERISTICS				
DC Current Gain(1) (I <sub>C</sub> = 2.0 mAde, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 50 mAde, V <sub>CE</sub> = 1.0 Vdc)	hes	50 25	150	
Collector-Emitter Saturation Voltage(1) (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>CE(sut)</sub>	12-1	0.3	Vdc
Buse-Emitter Saturation Voltage(1) $(I_C = 50 \text{ mAde}, I_B = 5.0 \text{ mAde})$	V <sub>RE(sat)</sub>	-	0.95	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current-Gain – Bandwidth Product ( $I_C = 10 \text{ mAde}, V_{CH} = 20 \text{ Vde}, f = 100 \text{ MHz}$ )	f <sub>T</sub>	250		MHz
Output Capacitance (V <sub>CB</sub> = 5.0 Vdc, I <sub>E</sub> = 0, f = 100 MHz)	Cobo	-	4.0	pF
Input Capacitance $(V_{BB} = 0.5 \text{ Vdc}, I_C = 0, f = 100 \text{ kHz})$	Cipo	-	8.0	pΨ
Collector-Base Capacitance (I <sub>E</sub> = 0, V <sub>CB</sub> = 5.0 V, f = 100 kHz)	C <sub>cb</sub>	-	4.0	рF
Small-Signal Current Gain (I <sub>C</sub> = 2.0 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	hís	50	200	1 32
Current Gain – High Frequency ( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 20 \text{ Vdc}$ , $f = 100 \text{ MHz}$ ) ( $I_C = 2.0 \text{ mAdc}$ , $V_{CE} = 10 \text{ V}$ , $f = 1.0 \text{ kHz}$ )	h <sub>fe</sub>	2.5 50	200	-
Noise Figure (I <sub>C</sub> = 100 µAde, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 1.0 k ohm, f = 1.0 kHz)	NF	-	6.0	dB

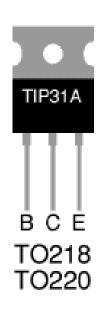
<sup>(1)</sup> Pulse Test: Pulse Width = 300  $\mu$ s. Duty Cycle = 2.0%

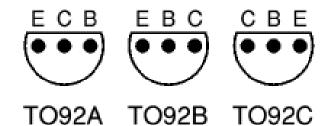
## BJTs - Practical Aspects



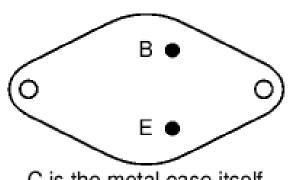






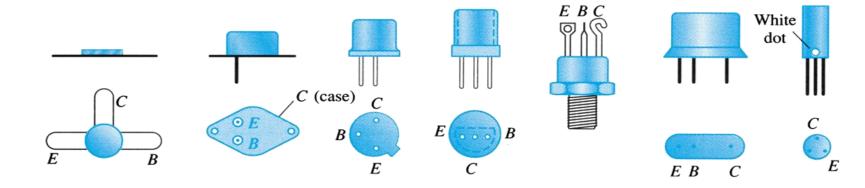


Views are from below with the leads towards you.



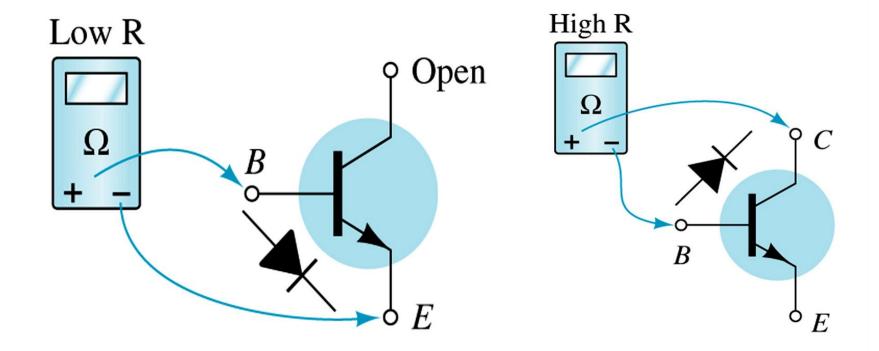
C is the metal case itself TO3

## Transistor Terminal Identification



## **Transistor Testing**

- Curve Tracer
   Provides a graph of the characteristic curves.
- 2. DMM Some DMM's will measure  $\beta$ DC or HFE.
- 3. Ohmmeter



## BJTs - Testing



## PNP Test Procedure

Connect the meter leads with the polarity as shown and verify that the base-to-emitter and base-to-collector junctions read as a forward biased diode: 0.5 to 0.8 VDC.

Reverse the meter connections to the transistor and verify that both PN junctions do not conduct. Meter should indicate an open circuit. (Display = OUCH or OL.)

Finally read the resistance from emitter to collector and verify an open circuit reading in both directions. (Note: A short can exist from emitter to collector even if the individual PN junctions test properly.)

## BJTs - Testing



### PNP Test Procedure

Connect the meter leads with the polarity as shown and verify that the base-to-emitter and base-to-collector junctions read as a forward biased diode: 0.5 to 0.8 VDC.

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# ...that's all folks... ...thanks for your time...

