А	A 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
	Address decoding
	Registers
	Coding clock* ALU
D.	ALU
2	is for high speed memory transfer instruction
	ESI*
	EAX
	EIP
D.	EFLAGS
3	is another name for Instruction Pointer.
	Memory Address Register
	Program Counter*
	Central Processing Unit
D.	Processor Status Word
<i>1</i> A DA	M has 4 Gigabytes worth of space. What is its address width
A.	
A. B. C. D.	9 32* 17 30 following storage devices are volatile except A. Magnetic Tape
A. B. C. D. 5. The	9 32* 17 30 following storage devices are volatile except A. Magnetic Tape B. EEPROM
A. B. C. D. 5. The t	9 32* 17 30 following storage devices are volatile except A. Magnetic Tape B. EEPROM C. EPROM*
A. B. C. D. 5. The t	9 32* 17 30 following storage devices are volatile except A. Magnetic Tape B. EEPROM
A. B. C. D. 5. The f	9 32* 17 30 following storage devices are volatile except A. Magnetic Tape B. EEPROM C. EPROM*
A. B. C. D. 5. The	9 32* 17 30 following storage devices are volatile except A. Magnetic Tape B. EEPROM C. EPROM* D. EAROM

7. MDR:
*Memory Data Register
8. SRAM:
*Static Random Access Memory
9. ACF:
*Auxiliary Carry Flag

- 10. Convert 29FC from hexadecimal to decimal
 - A. 8748
 - B. 9748
 - C. 10748*
 - D. 11748