

EE 272 - Video Assignment 1 (Please answer the following questions and submit your answers)

1. Implement an SR latch using NOR gates and explain its operation using a truth table.
2. Implement an SR Latch using NAND gates and explain its operation using a truth table.
3. Give one practical application of the NOR gates SR latch.
4. Give one practical application of the NAND gates SR latch.
5. What is a gated SR latch?
6. Implement a gated SR latch using NAND gates only.
7. Describe how a gated SR latch can be converted to a D latch and draw the circuit diagram for a gated D latch using only 4 NAND gates.
8. Give the truth table for a D Latch.
9. State the technical difference between a Latch and a Flip-Flop.
10. What do you understand by the term "Active high Latch"?