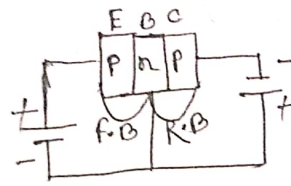
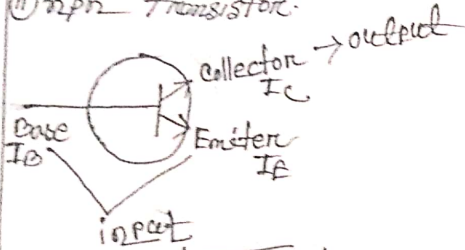


*29-01-19

* Transistor: Transistor consist of a Pn Junction formed by Sandwiching either p-type or n-type Semiconductor between a pair of opposite type

① pnp transistor.

② npn transistor.



Structure of Transistor:-

Symbol of Transistor:-

$$\therefore I_E = I_B + I_C$$

⇒ Transistor use to convert low resistance to high resistance. In 1948 J. Bardeen and W.H. Brattain invented Transistor.

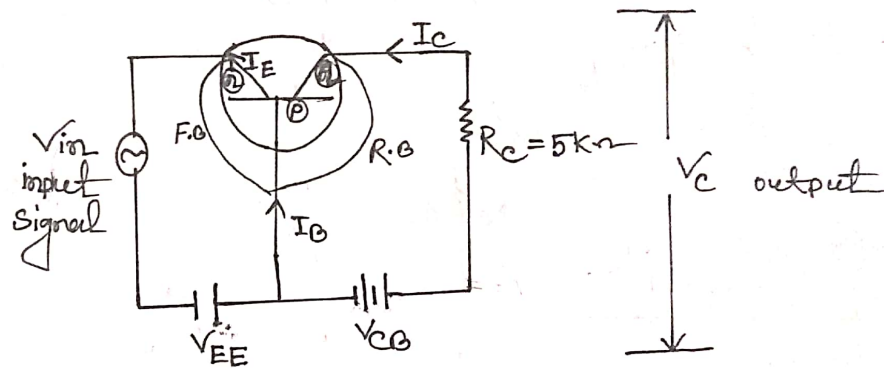
① Emitter: Emitter is highly doped and charge carrier is used to inject

② Base: Base is less doped.

③ Collector: Collector is moderately doped.

*05.02.19

Transistor as an amplifier: Transistor rises the strength of weak signal into higher signal and it acts as an amplifier.



Suppose,

$$V_{in} = 0.1V, R_C = 5k\Omega$$

$$I_C = 1mA$$

$$\begin{aligned} V_{C0} &= I_C R_C \\ &= 1mA \times 5k\Omega \\ &= 5V \end{aligned}$$

$$\begin{aligned} \therefore \text{Voltage amplification} &= \frac{V_o / V_c}{V_{in}} \\ &= \frac{5}{0.1} \\ &= 50 \checkmark \end{aligned}$$

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*Math problem:

A common base Transistor amplifier has an input resistance of 20Ω and output resistance of $100k\Omega$. The collector load is $1k\Omega$. If a signal of $500mV$ is applied between Emitter and Base. Find the voltage amplification. Assume α_{ac} to be nearly one.

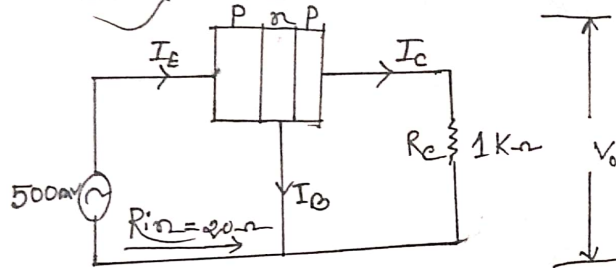


Fig-1:

Soln:

$$V_{in} = 500mV$$

$$R_{in} = 20\Omega$$

$$V_{in} = I_E R_{in}$$

$$\begin{aligned} \therefore I_E &= \frac{V_{in}}{R_{in}} \\ &= \frac{500mV}{20\Omega} \\ &= 25mA \end{aligned}$$

$$\therefore \text{current amplification, } \alpha = \frac{I_C (\text{output})}{I_E (\text{input})}$$

$$1 = \frac{I_C}{25mA}$$

$$\therefore I_C = 25mA$$

$$\text{Voltage amplification, } A_v = \frac{V_o}{V_{in}}$$

$$= \frac{I_C R_c}{V_{in}}$$

$$\begin{aligned} &= \frac{25mA \times 1k\Omega}{500mV} \\ &= \frac{25 \times 10^{-3} \times 10^3}{500 \times 10^{-3}} \\ &= 50 \end{aligned}$$

Date: 05.03.19

From here, After mid-term exam

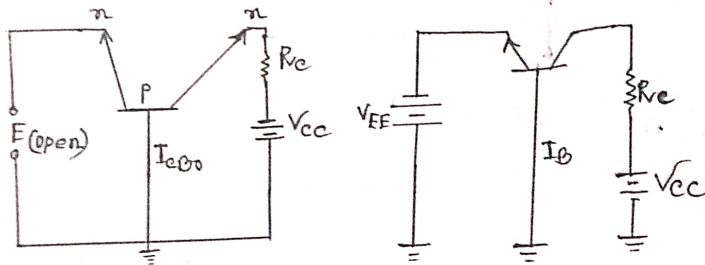
Ahmed

*Expression for collector current: The total collector ^{current} consist of —

(i) That part of emitter current which reaches the collector terminal.
i.e. αI_E .

(ii) The leakage current $I_{leakage}$: This current is due to the movement of minority carriers across base-collector junction on account of it being reverse biased. This is generally much smaller than αI_E .

\therefore Total collector current, $I_C = \alpha I_E + I_{leakage}$.



$$\begin{aligned} I_E &= I_C + I_B \\ I_C &= \alpha I_E + I_{C00} \\ I_C &= \alpha (I_C + I_B) + I_{C00} \\ I_C &= \alpha I_C + \alpha I_B + I_{C00} \\ I_C (1 - \alpha) &= \alpha I_B + I_{C00} \\ I_C &= \frac{\alpha I_B + I_{C00}}{1 - \alpha} \quad \checkmark \end{aligned}$$

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Date: 08-03-19

Ques: In a common base connection, the emitter current is 1mA. If the emitter current is 50 nA. Find the total collector current.
Given that, $\alpha = 0.92$

Solution:

$$I_E = 1 \text{ mA}$$

When Emitter circuit is open for collector current,

$$I_{CBO} = 50 \text{ nA}$$

$$\alpha = 0.92$$

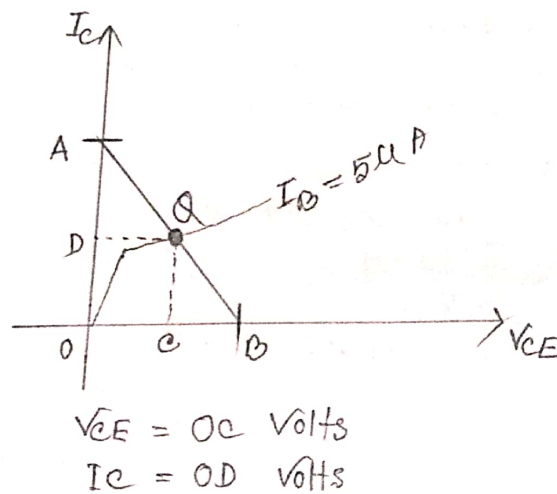
\therefore Total collector current, $I_C = ?$

$$\begin{aligned} \therefore I_C &= \alpha I_E + I_{CBO} \\ &= 0.92 \times 1 \times 10^{-3} + 50 \times 10^{-6} \\ &= 9.7 \times 10^{-4} \text{ A} \end{aligned}$$

Ans

Operating point/silent point The Zero Signal Values of I_C and V_{CE} are known as operating point. It is called operating point because, the variations of I_C and V_{CE} take place about this point when signal is applied. It is also called quiescent (Silent) point or Q-point because it is the point on I_C and V_{CE} .

Characteristics when the transistor is silent. i.e.: an absence of the signal



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Ex 6.8 In a transistor circuit collector load is $4\text{K}\Omega$ whereas quiescent current (zero signal collector current) is 1mA

- ① what is the operating point if $V_{CC} = 10\text{V}$? $V_{CE} = ?$
② what will be the operating point if $R_C = 5\text{K}\Omega$?

① Solve

When collector load, $R_C = 4\text{K}\Omega$
 $V_{CC} = 10\text{V}$
 $I_C = 1\text{mA}$

For $R_C = 4\text{K}\Omega$ and $V_{CC} = 10\text{V}$ then

$$\begin{aligned} V_{CE} &= V_{CC} - I_C R_C \\ &= 10 - 1 \times 10^{-3} \times 4 \times 10^3 \\ &= 10 - 4 \\ &= 6\text{V} \end{aligned}$$

\therefore operating point $(V_{CE}, I_C) = (6\text{V}, 1\text{mA})$ 

② When, $R_C = 5\text{K}\Omega$
 $I_C = 1\text{mA}$
 $V_{CC} = 10\text{V}$

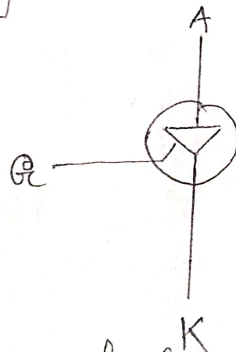
$$\begin{aligned} V_{CE} &= V_{CC} - I_C R_C \\ &= 10 - 5 \\ &= 5\text{V} \end{aligned}$$



Date: 12-03-19

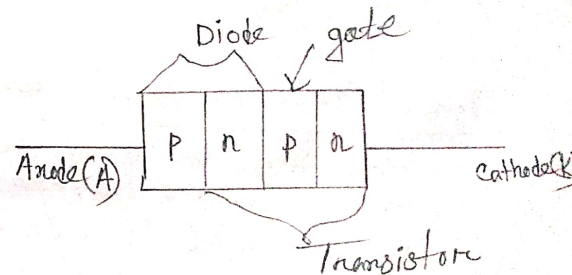
* SCR (Silicon Controlled Rectifier): The silicon control rectifier is a three terminal semiconductor switching device which is probably the most important circuit element after the diode and the transistor. The SCR has assumed paramount importance in electronics because it can be produced in versions to handle current upto several thousand amperes and voltage upto more than.

* Circuit diagram:



Symbol of (SCR)
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Levofloxacin INN

Construction of SCR:



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* Working of SCR:

① when gate is open:

Fig-1: Shows the scr circuit with gate open i.e. no voltage applied to the gate. Under this condition, Junction J_2 is reverse biased while Junction J_1 and J_3 are forward biased. Hence the situation in the Junction J_1 and J_3 is just as in a open npn transistor with base open. Consequently, no current flows through the load, R_L and the SCR is cut off.

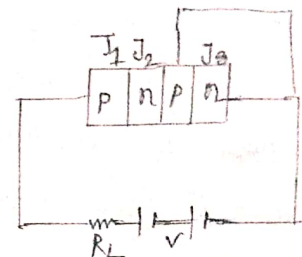


Fig-1a

② when gate is positive w.r. to cathode:

The SCR can be made to conduct heavily by applying a small positive potential to gate as shown in Fig-1b. Now, Junction J_3 is forward biased and Junction J_2 is reverse biased. The electron in n type material starts moving across Junction J_3 towards left whereas hole from p type towards right.

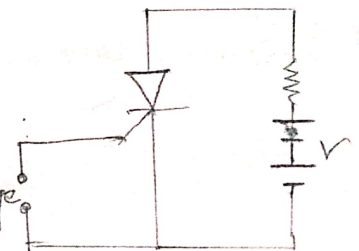
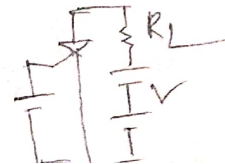
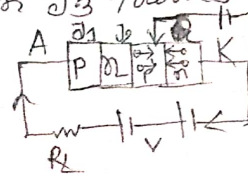


Fig-1b



* peak reverse voltage (prv): peak reverse voltage is the maximum reverse voltage (cathode positive w.r. to anode) that can be applied to an SCR without conducting in the reverse direction.

* circuit fusing rating: it's the products of square of forward surge current and time of duration of surge
i.e. $\text{circuit fusing rating} = I^2 t$
maximum value of $I^2 t = 90 \text{ A}^2 \text{ s}$ (circuit fusing rating).

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21.09.19

*~~maths~~ An SCR has a circuit fusing of $50A^2s$. The device is being used in circuit where it could be subjected to a $100A$ surge. Determine the maximum allowable duration of such a surge.

Solve

Fusing rating, $i^2t = 50A^2s$

Value of surge current, $I_s = 100A$

maximum allowable duration, $t_{max} = ?$

We know, From the fusing rating, $I_s^2 t_{max} = i^2 t$

$$\Rightarrow t_{max} = \frac{i^2 t}{I_s^2}$$

$$\Rightarrow t_{max} = \frac{50A^2s}{(100)^2A}$$

$$= 0.005s$$

(Ans ✓)

3 being
100 A
such

** Advantage of SCR:

- i) It has no moving parts. Consequently, it gives noise less operation at high frequency.
- ii) The switching speed is very high upto 10^9 operation per-second.
- iii) It permits control over large current (30-100A) in the load by means of small gate current (a few of mA).
- iv) It has small size and gives trouble free service.

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* Voltage gain of JFET Amplifier:

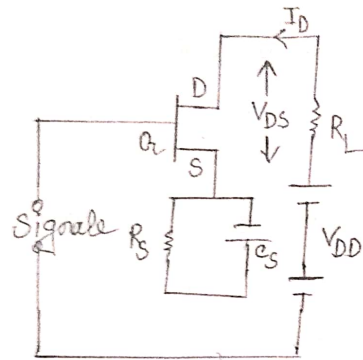


Fig:-1 JFET amplifier.

Fig-1 shows a typical circuit of JFET amplifier. The JFET is self-biased by using network R_S - C_S . The DC component of drain current flowing through the source biasing resistance R_S produces the desired bias voltage. The capacitor C_S passes the a.c component of drain current.

$$R_S = \frac{V_{GS}}{I_D}$$

where,

V_{GS} = voltage across R_S

I_D = current through R_S

$$\therefore \mu = r_{ds} g_m$$

$$\therefore \text{voltage gain } \Rightarrow A_v = \frac{\mu R_L}{r_{ds} + R_L}$$

$$A_v = \frac{r_{ds} g_m R_L}{r_{ds} + R_L}$$

g_m = Transconductance,
if $r_{ds} > R_L$ then

$$A_v = \frac{r_{ds} g_m R_L}{r_{ds}}$$

$$A_v = g_m R_L$$