Akshat Johar

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EDUCATION

BITS Pilani University

Bachelor of Engineering in Electrical and Electronics

• CGPA: 9.4

Hyderabad, India

Expected June 2024

SGGS Chandigarh, India

2019-20

• 94 Percent

EXPERIENCE

10+2

Tech Team Member

Phoenix Association, Bits Pilani, Hyderabad

Member
Automation and Robotics Club, Bits Pilani, Hyderabad

Member

Department of Photography, Bits Pilani, Hyderbad

July 2021 – Present Hyderabad, India

January 2022 – Present

Hyderabad, India July 2021 – Present

Hyderabad, India

PROJECTS

Portfolio Website | HTML, CSS, JS, Email JS

 A Responsive Portfolio Website which contains the detailed information about myself. Email JS is used for contact purpose. December 2021 – January 2022

January 2022 – February 2022

Chat Application | MERN Stack, SocketIO, Chakra UI

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A real-time responsive Chat Application on which users. Cloudinary
was used for users to upload their profile images can
send messages one on one as well as can initiate group chats.

Spherical Survelliance Bot | Arduino, Esp32, SPI

• A remote controller surveillance bot capable of traversing on different terrains and simultaneously providing live video as well as audio feed at the base station. September 2021 – December 2021

Water Quality Monitoring System | Raspberry-Pi, LoRA, ThingsSpeak IoT

• Real-Time water and air quality monitoring system capable of sensing and transmitting desired parameters to the base station where ML Techniques are applied to determine water quality January 2022 – Present

LEADERSHIP

Team Leader

September 2021 – December 2021

Hyderabad, India

Robothon Competition
Was head of the team which participated in the Inter College Robotics Competition which was held at MCEME Secunderbad.

• Responsible in motivating the team as well as directly being involved to construct a bot in a short span of time

TECHNICAL SKILLS

Languages: C, C++, Python, R , Verilog, MATLAB **Frontend**: HTML, CSS, JavaScript, React JS, Bootstrap

Backend: Node JS, Express JS, MongoDB

Others: Git, Arduino, ESP, AutoCAD, LTSpice, Xilinx, Assebly Language