Design Rules Verification Report

Filename : C:\Altium Projects\B100\[AA]\Design Files\B100AA\B100AA PCB R1.PcbDoc

Warnings

Warnings	
Total	0

Rule Violations

Rule Violations	
Clearance Constraint (Gap=0.1mm) (All),(All)	8
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.77mm) (Max=0.77mm) (Preferred=0.77mm) (InNetClass('R7_Nets'))	0
Width Constraint (Min=0.1mm) (Max=1.5mm) (Preferred=0.2mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.175mm) (Conductor Width=0.2mm)	0
Minimum Annular Ring (Minimum=0.125mm) (All)	0
Minimum Annular Ring (Minimum=0.125mm) (((ObjectKind = 'Pad') OR (ObjectKind = 'Via')) And	0
Minimum Annular Ring (Minimum=0.125mm) (((ObjectKind = 'Pad') OR (ObjectKind = 'Via')) And	0
Hole Size Constraint (Min=0.15mm) (Max=5mm) (All)	0
Hole To Hole Clearance (Gap=0.35mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.08mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.1mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.2mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25mm) (Prefered=12.5mm) (All)	0
Total	8

Clearance Constraint (Gap=0.1mm) (All),(All)

Clearance Constraint: (0.097mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 1 And Track (65.85mm,8.261mm)(67.074mm,9.485mm) on Mid Layer 1
Clearance Constraint: (0.097mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 1 And Track (67.074mm,9.485mm)(67.31mm,9.485mm) on Mid Layer 1
Clearance Constraint: (0.1mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 1 And Track (79.375mm,20.075mm)(79.825mm,19.625mm) on Mid Layer 1
Clearance Constraint: (0.1mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 1 And Track (79.825mm,8.675mm)(79.825mm,19.625mm) on Mid Layer 1
Clearance Constraint: (0.099mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 3 And Track (62.451mm,6.525mm)(63.85mm,7.924mm) on Mid Layer 3
Clearance Constraint: (0.099mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 3 And Track (63.85mm,7.924mm)(63.85mm,8.755mm) on Mid Layer 3
Clearance Constraint: (0.1mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 4 And Track (50.256mm,11.525mm)(50.75mm,11.031mm) on Mid Layer 4
Clearance Constraint: (0.1mm < 0.1mm) Between Region (0 hole(s)) Mid Layer 4 And Track (50.75mm,8.999mm)(50.75mm,11.031mm) on Mid Layer 4