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Roll No.

Fourth SEMESTER**B.Tech [IT]****MID SEMESTER EXAMINATION****March-2019****IT-206 COMPUTER ORGANIZATION AND ARCHITECTURE****Time: 1:30 Hours****Max. Marks : 25****Note : Answer all questions****Assume suitable missing data, if any.****Q.1 Draw and Describe:**

- a) Instruction Cycle flowchart
- b) Flowchart for LDA, ADD, ISZ, BSA

(4+6)**Q.2 a) Explain the microprogrammed control unit using a block diagram****b) Interpret the meaning of the 20-bit control word:**

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(4+2)**Q.3 Answer the following questions:**

- a) Differentiate between hardwired vs microprogrammed CU
- b) Draw the entire common bus arrangement and then highlight in the diagram: the datapath for the ADD microoperation
- c) Analyze the relation between micro-operations and symbolic microprogram using a suitable example for any one routine.

(3x3)