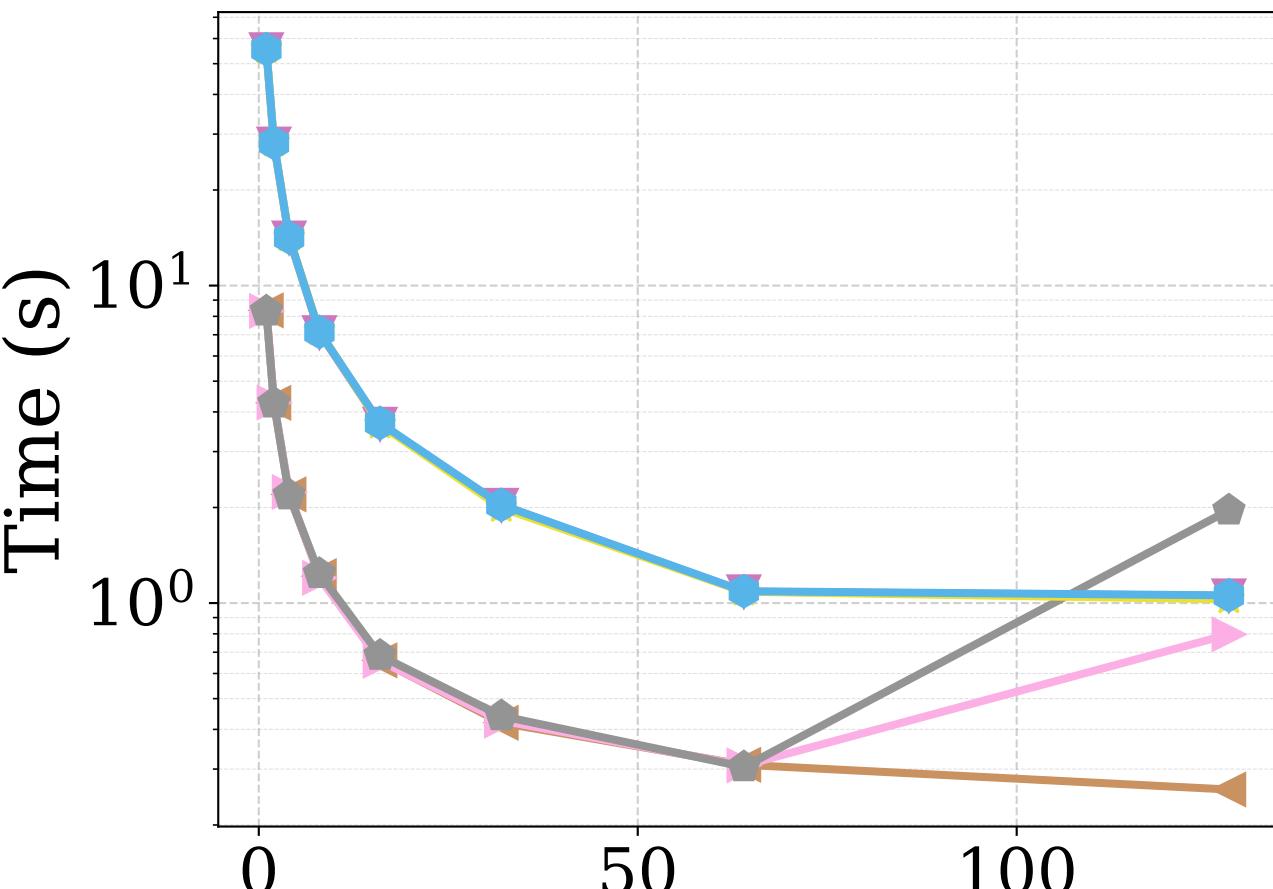
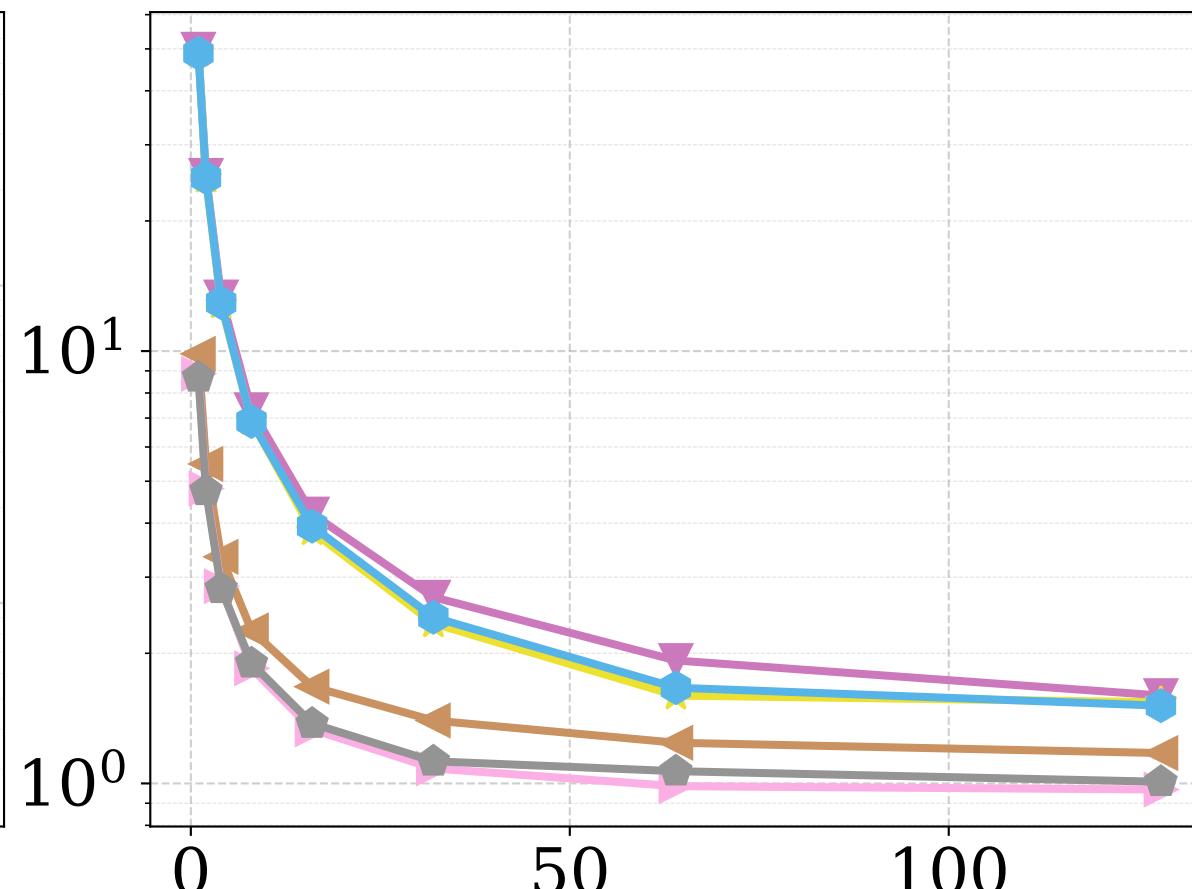


▼ CacheV2QPara ► SIMDQPara ★ CPara
—▲— CacheV2SIMDQPara —◆— SIMDQPara —●— QPara

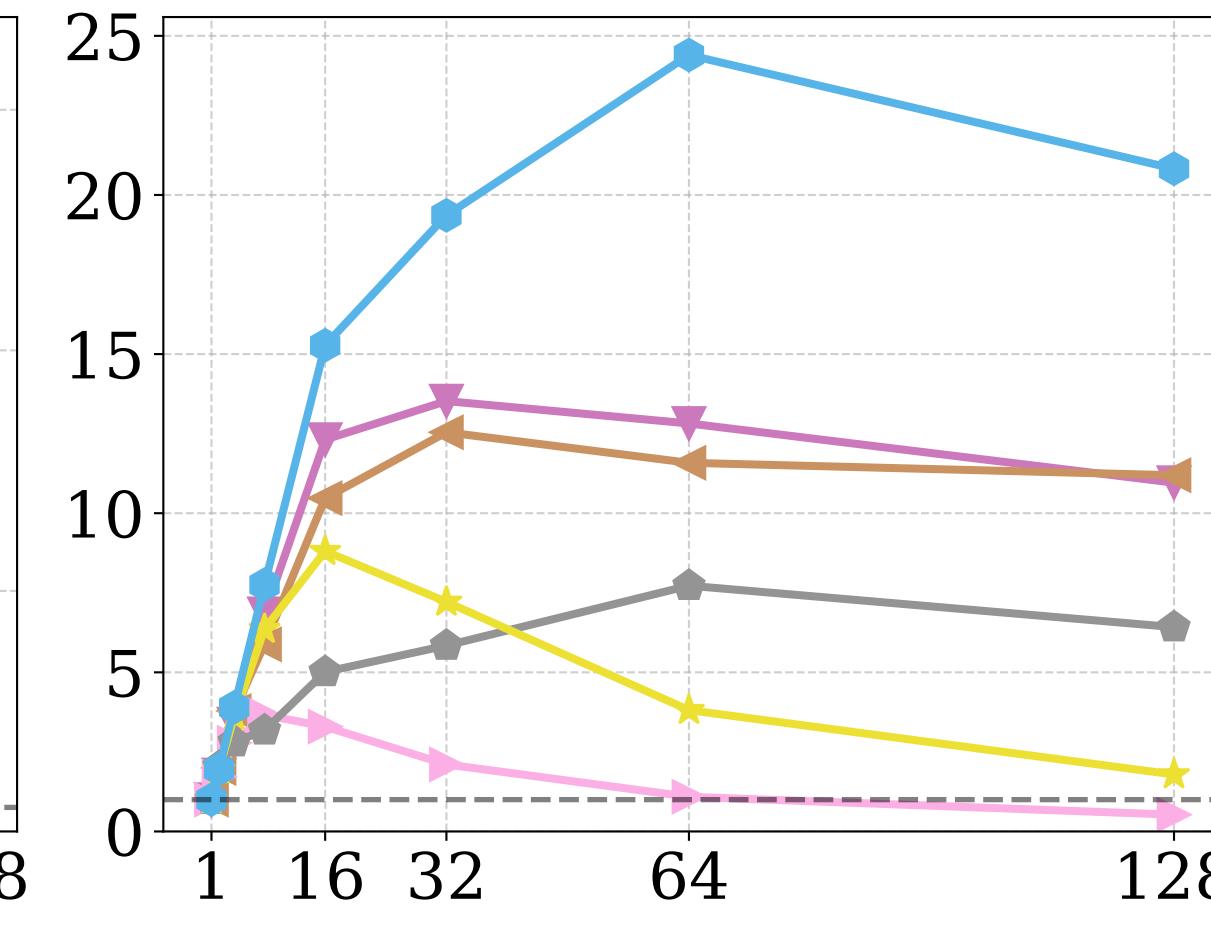
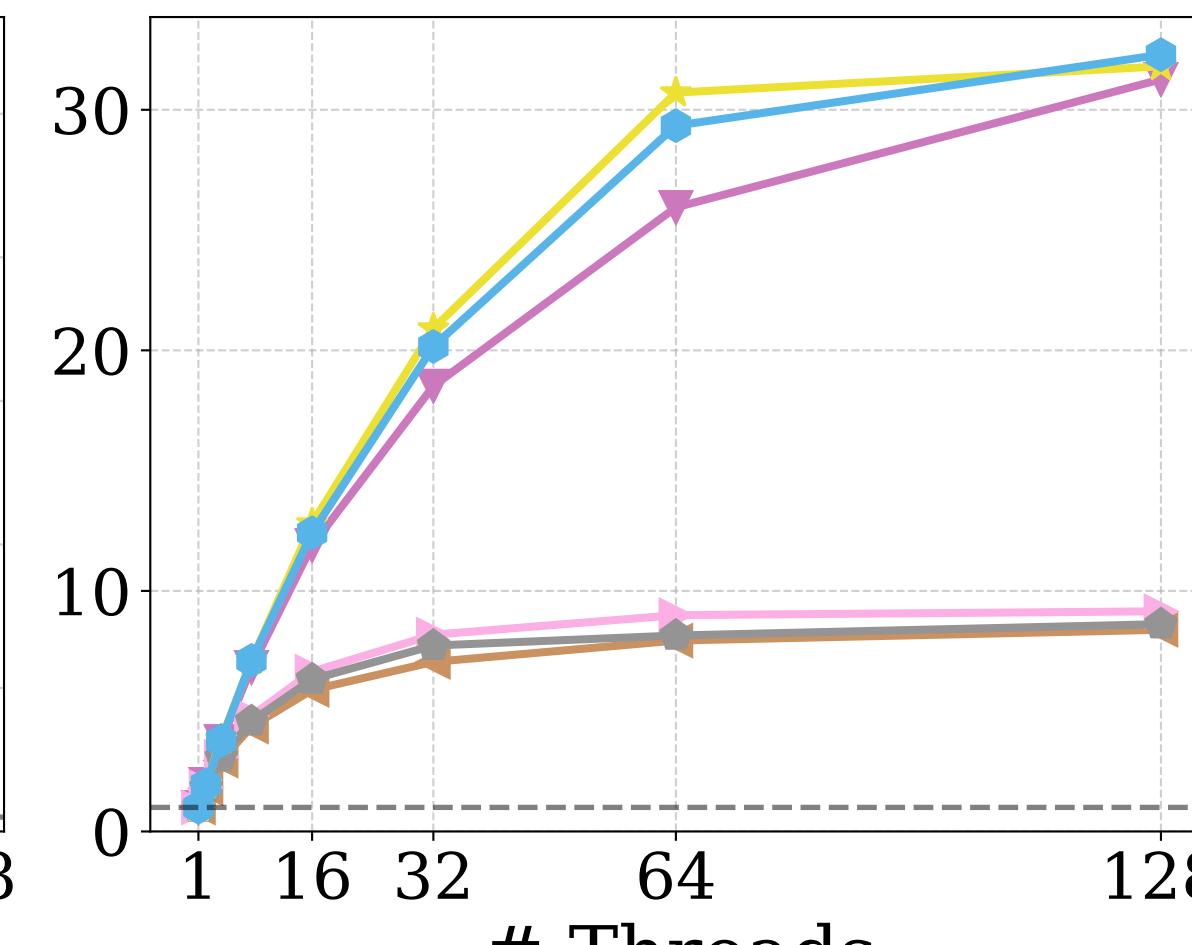
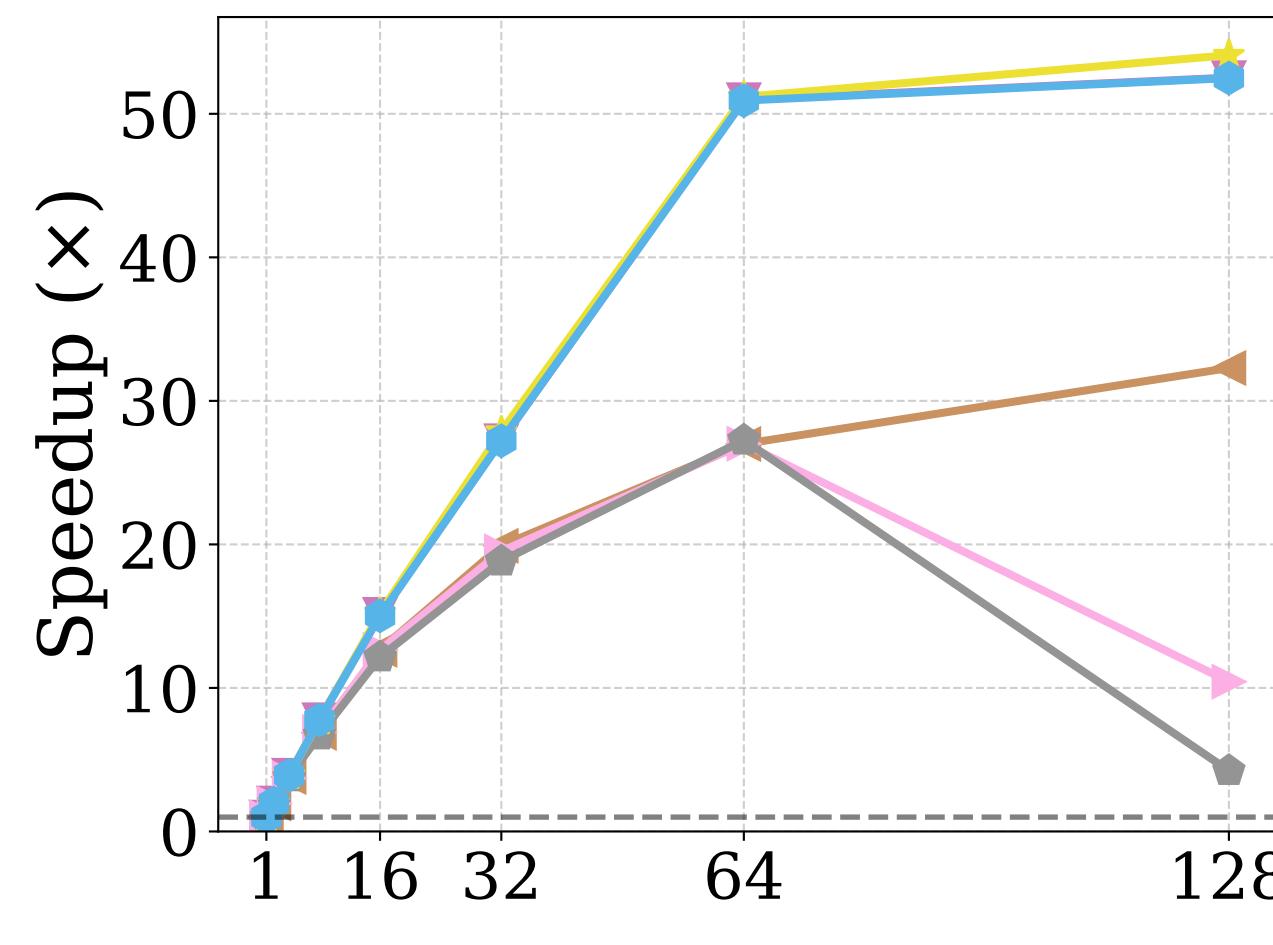
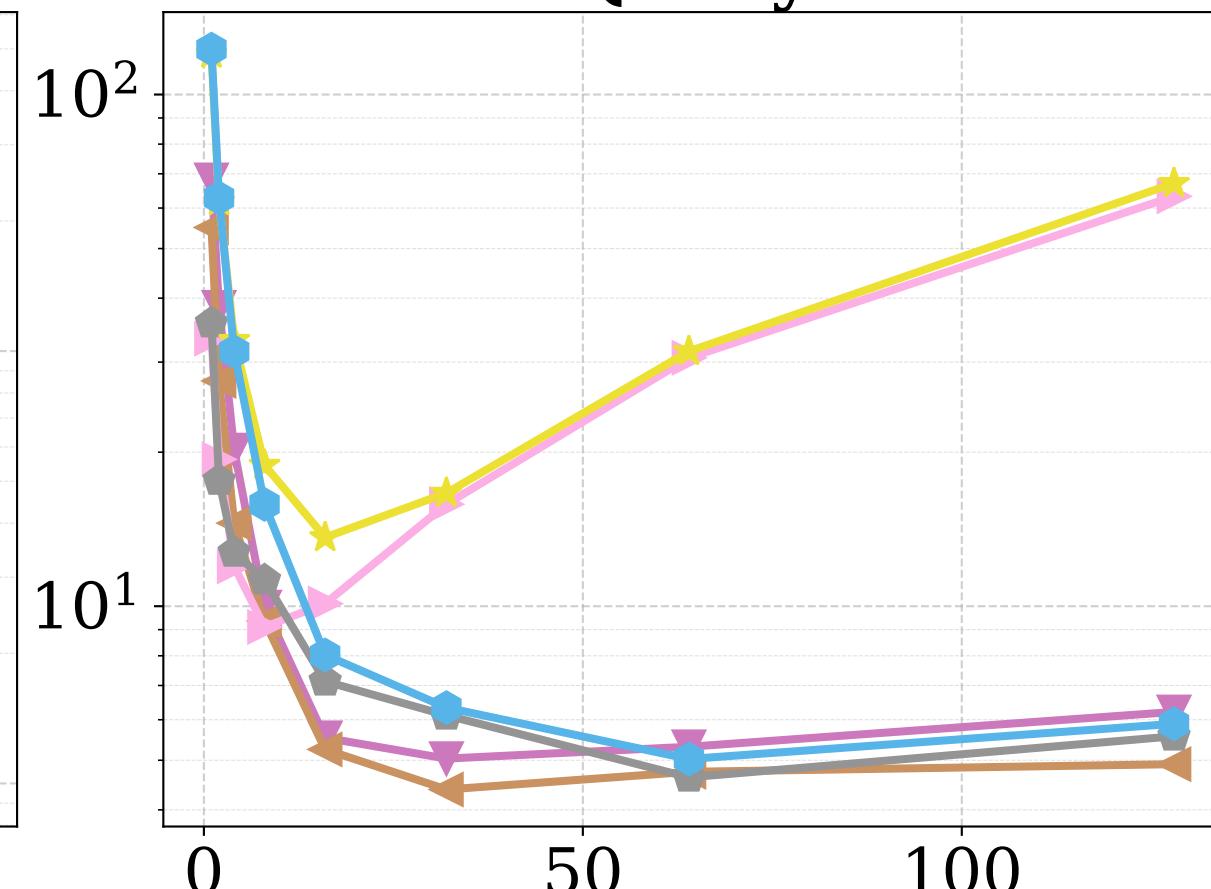
Train



Build



Query



Threads