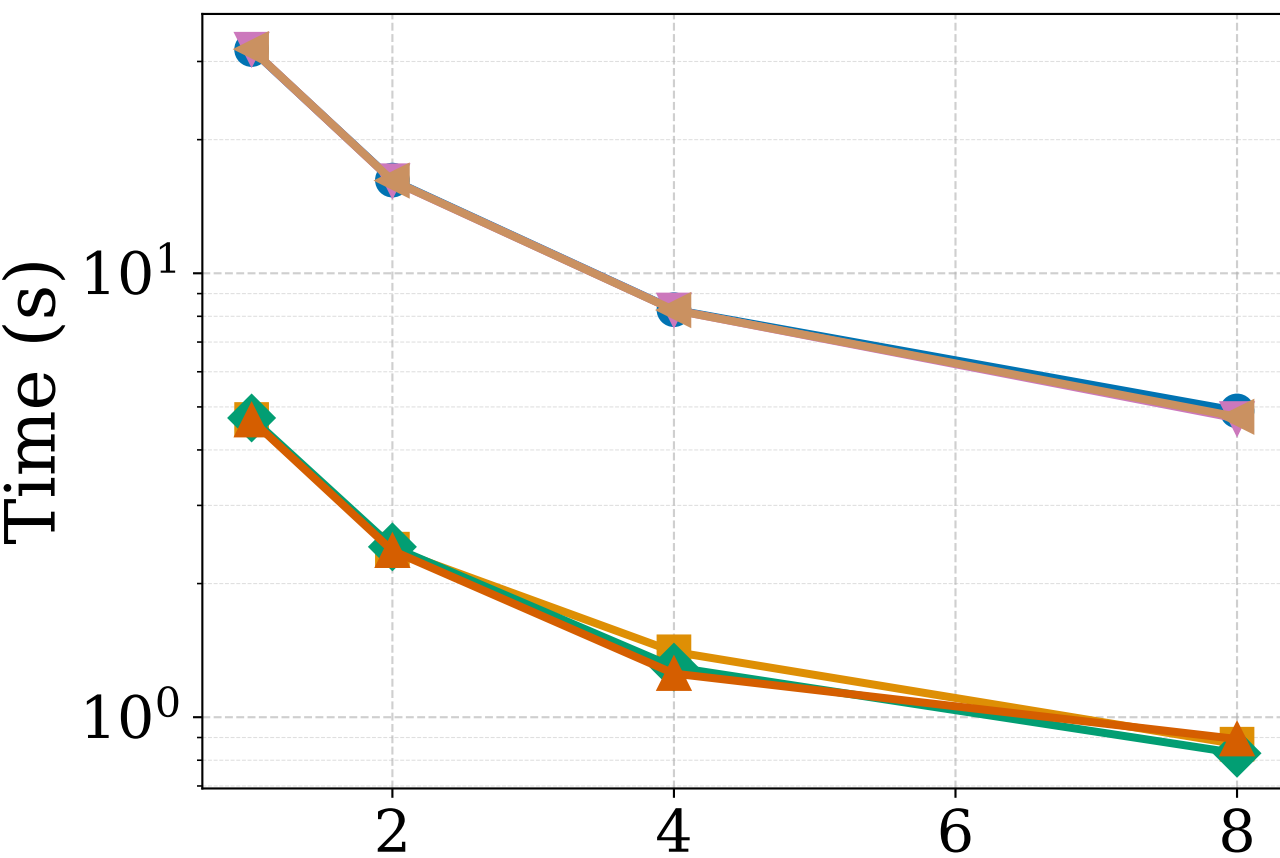
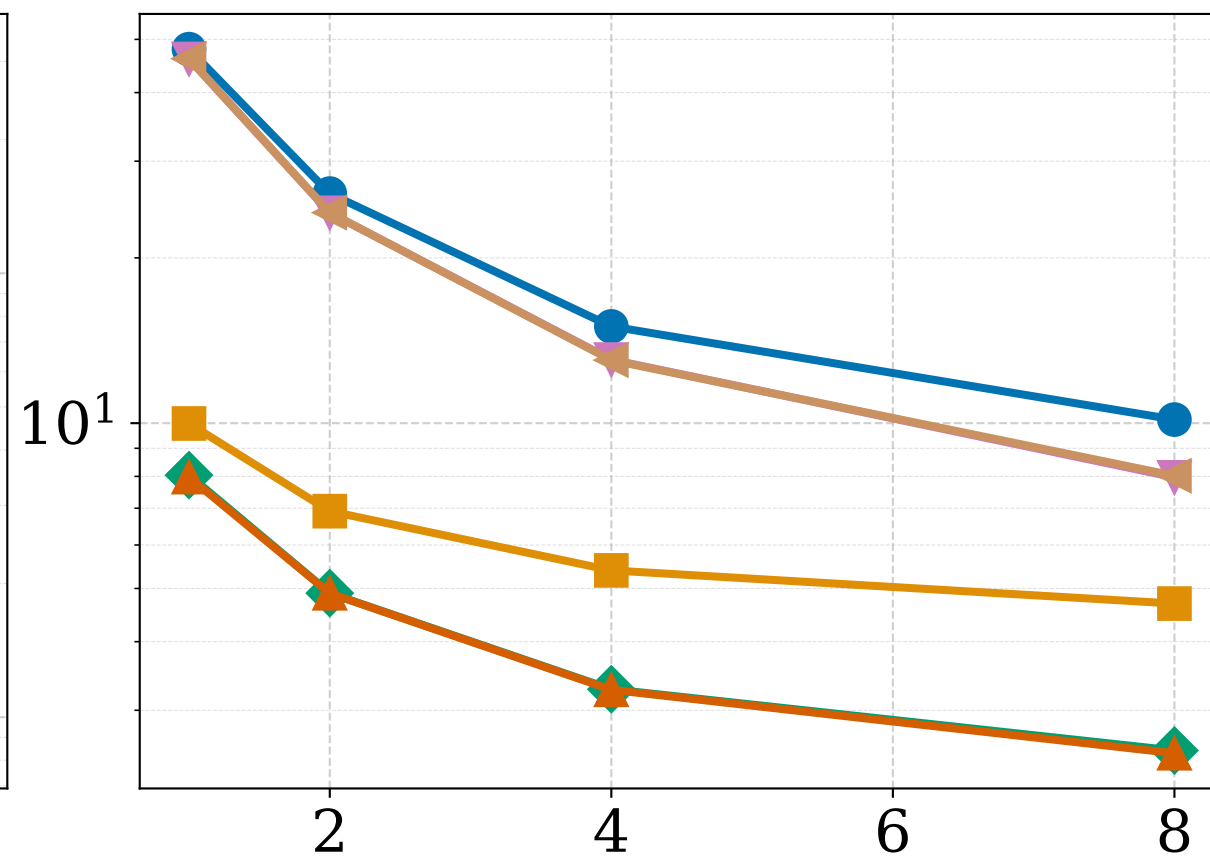


● CacheV2QPara      ◆ SIMDCPara      ▼ ScalarCPara  
 ■ CacheV2SIMDQPara      ▲ SIMDQPara      ▶ ScalarQPara

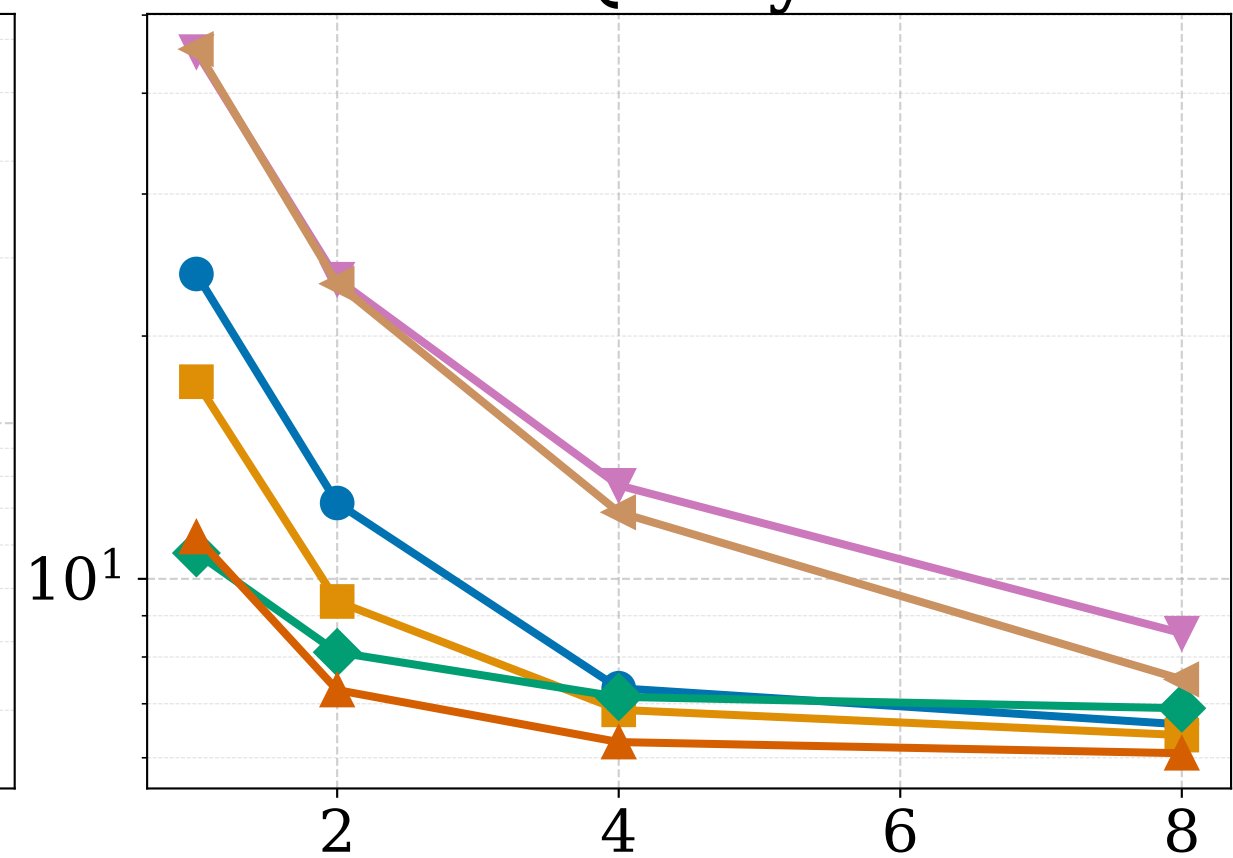
Train



Build

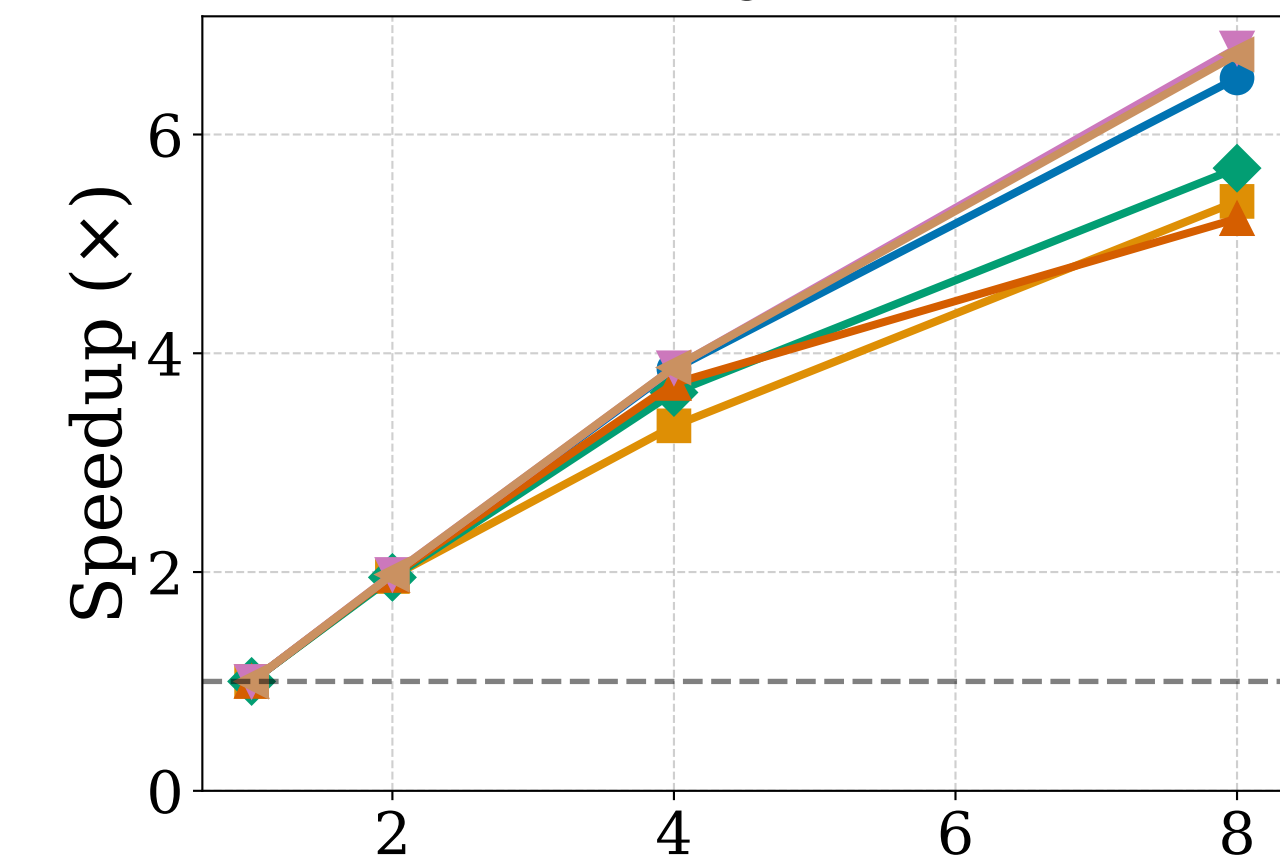


Query

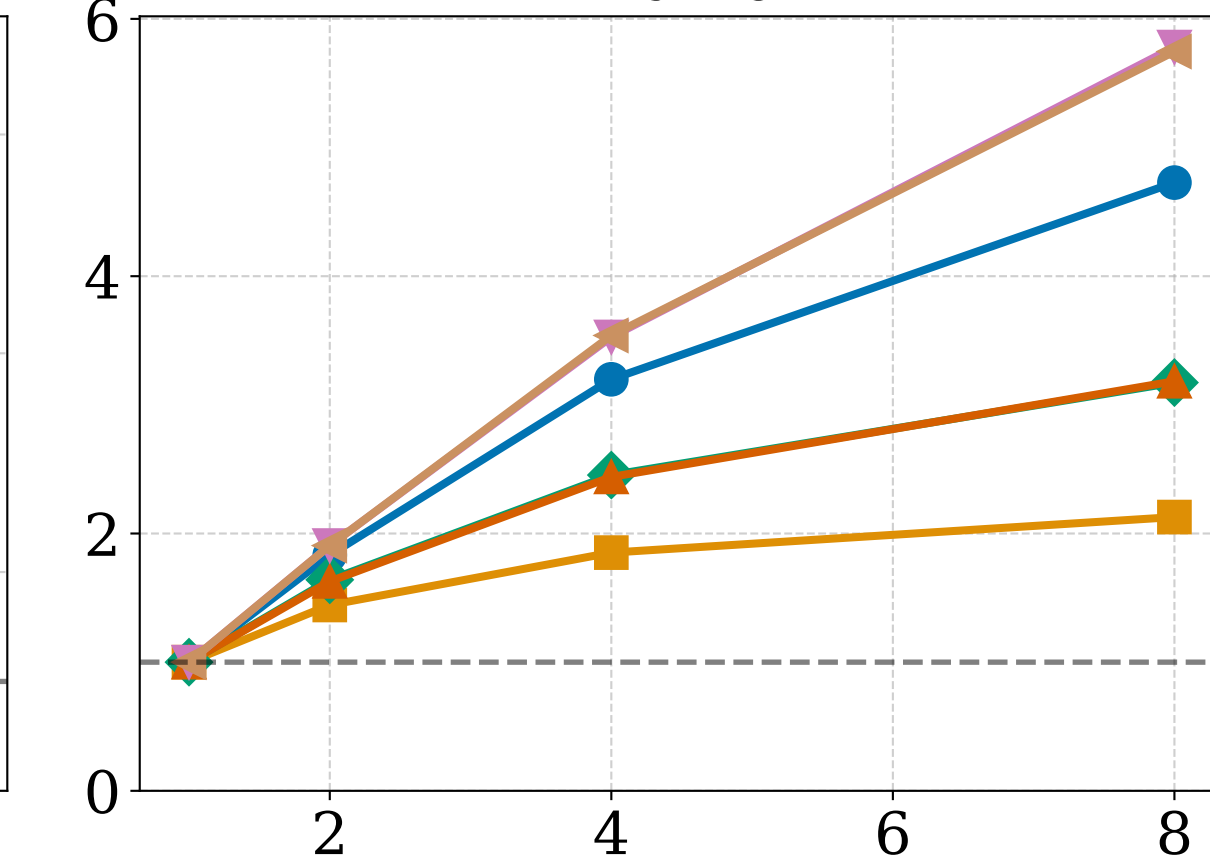


# Threads

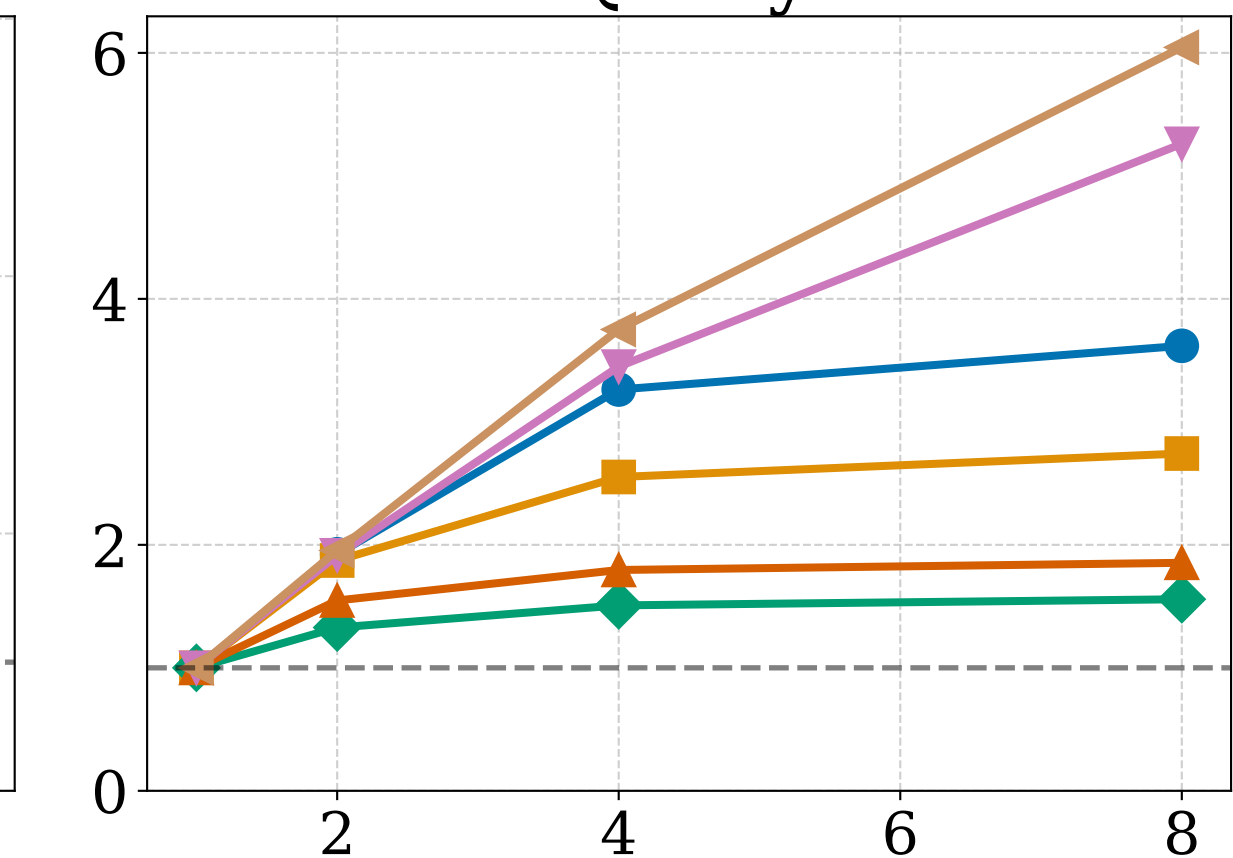
Train



Build



Query



# Threads