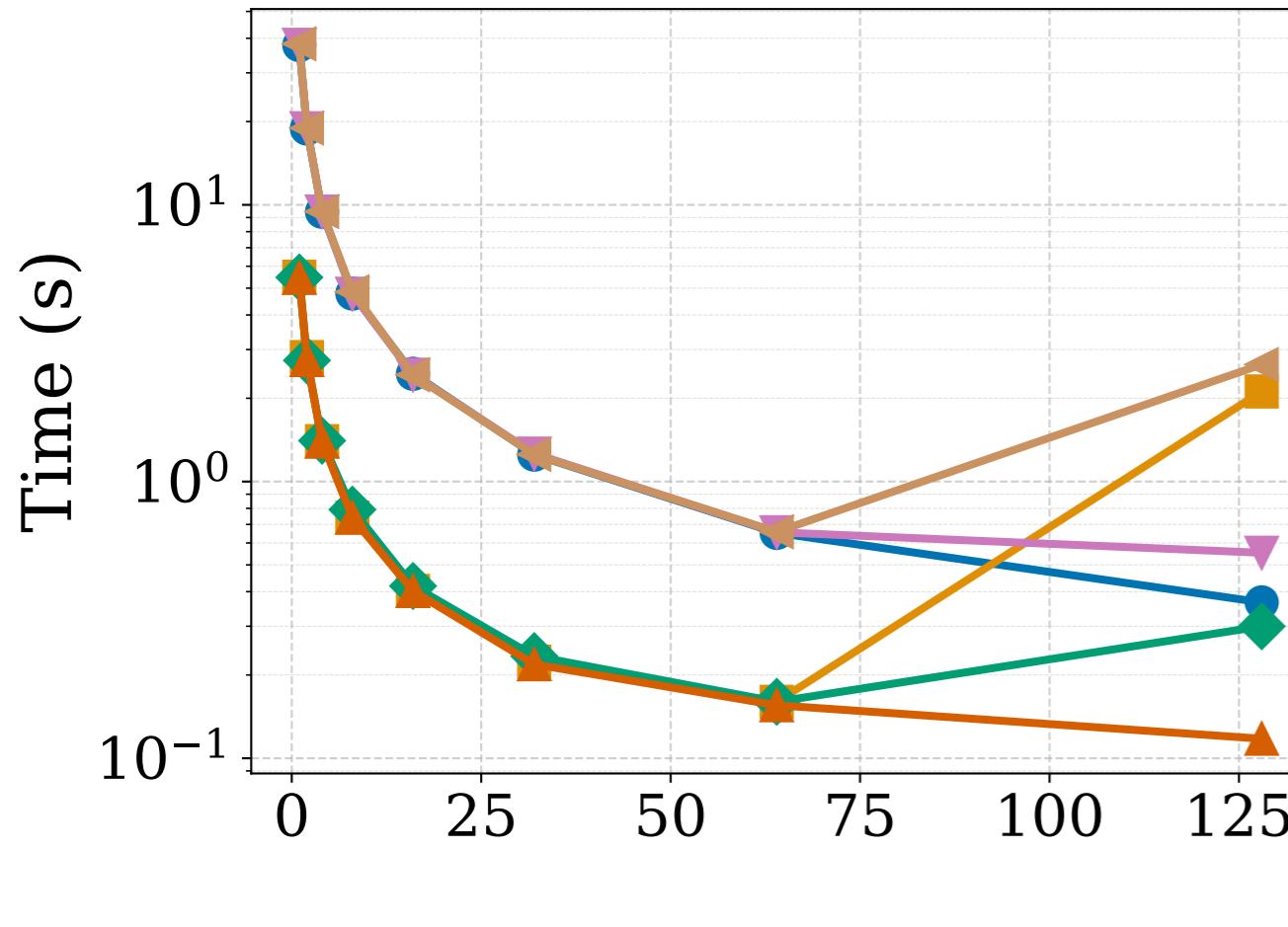
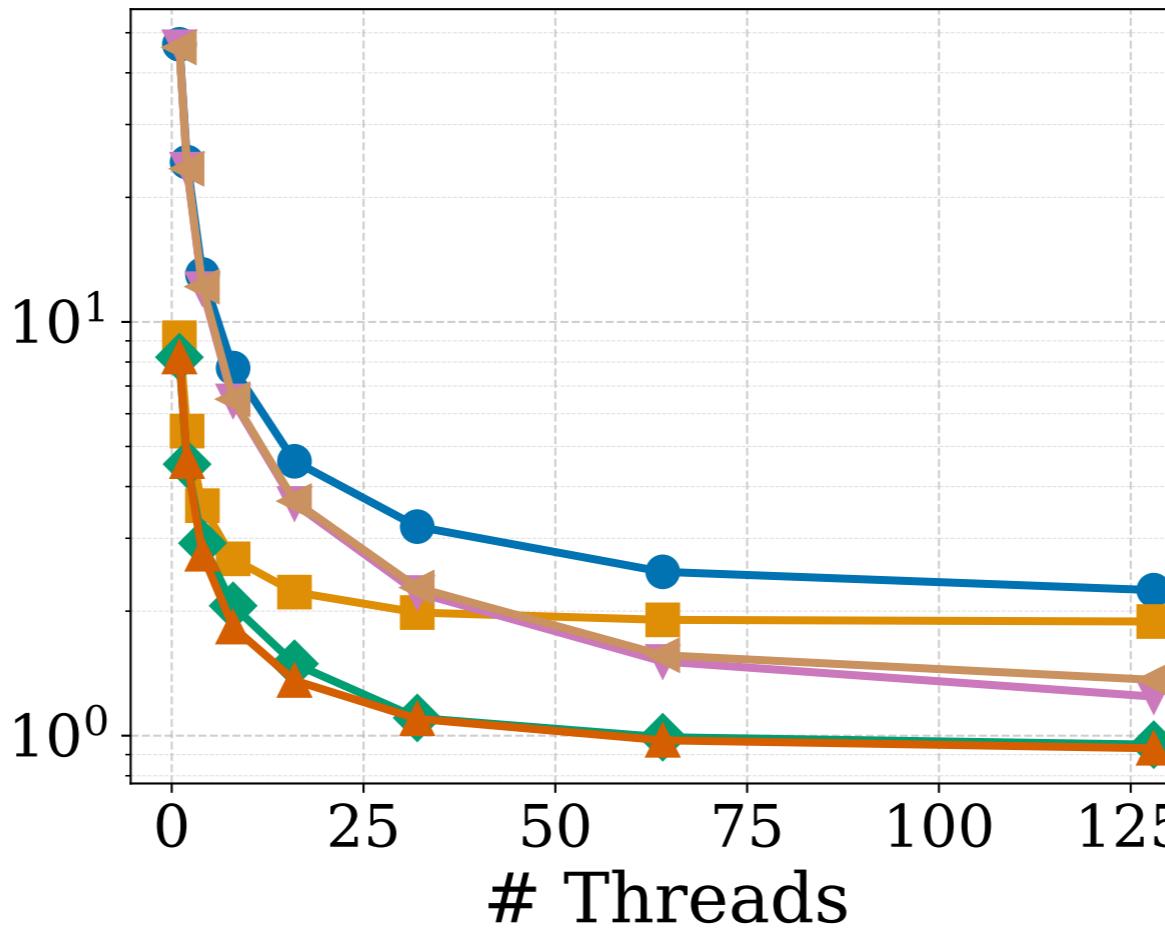


● CacheV2QPara      ◆ SIMDCTPara      ▼ ScalarCPara  
■ CacheV2SIMDQPara    ▲ SIMDQPara      ◀ ScalarQPara

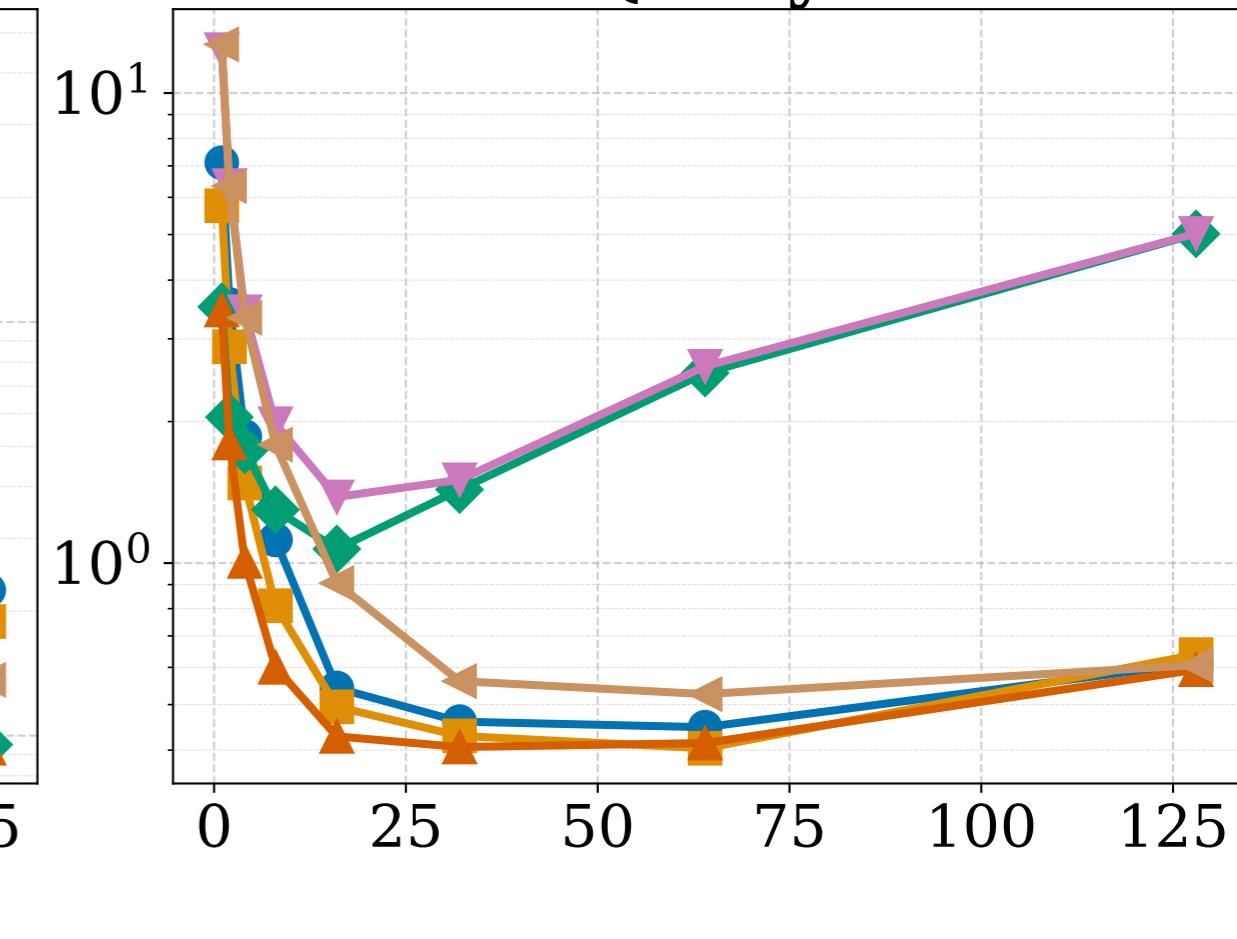
Train



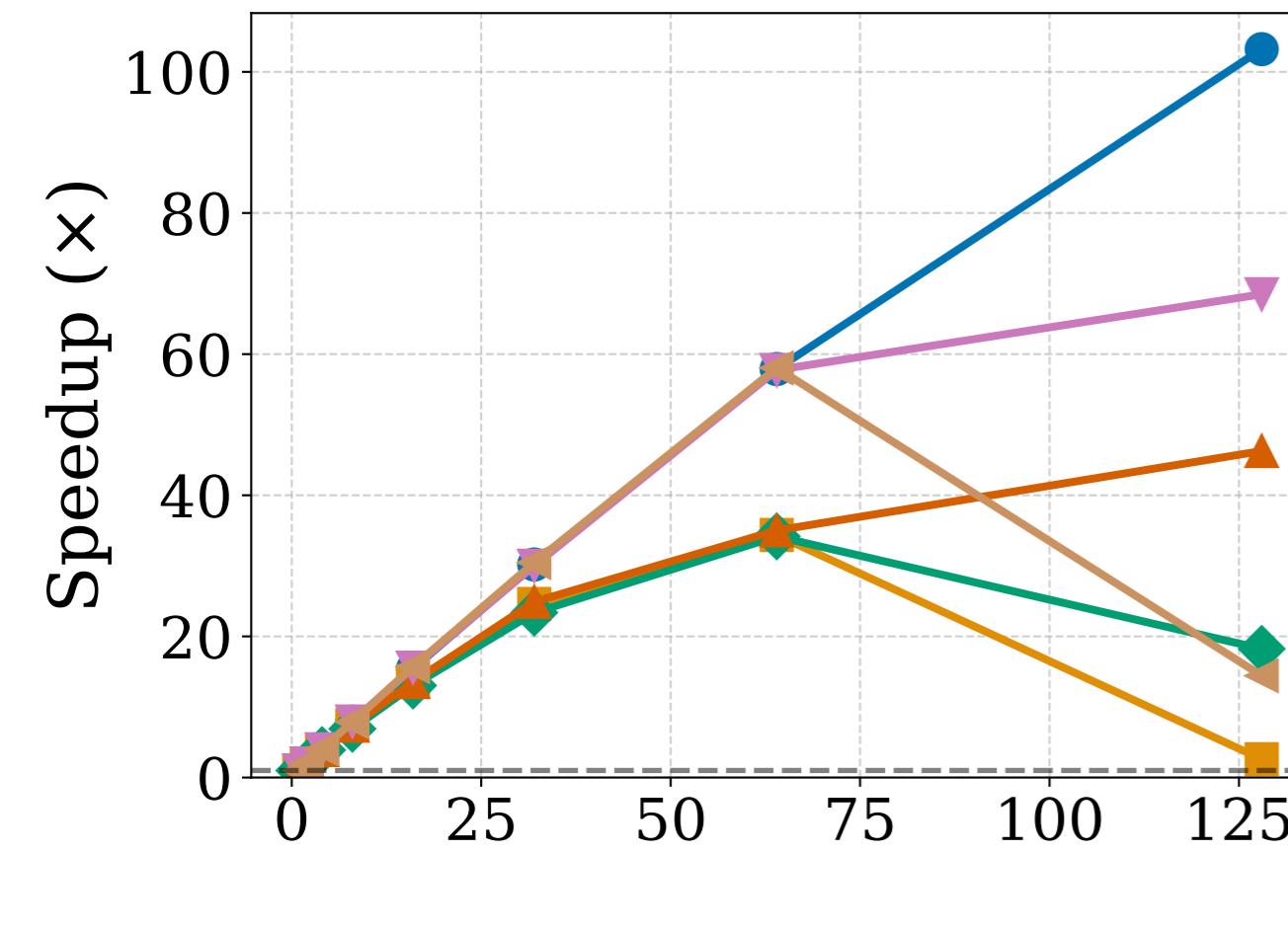
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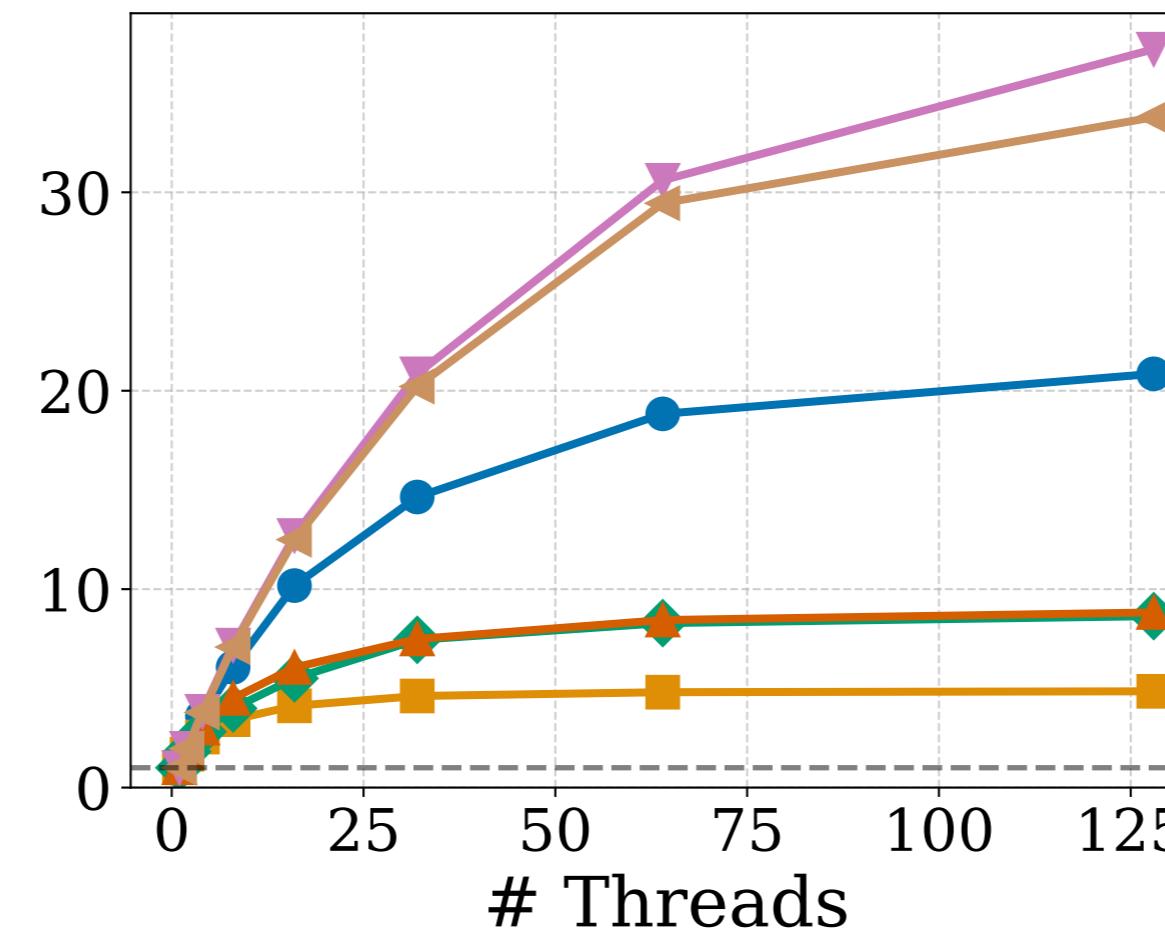
Query



Train



Build



Query

