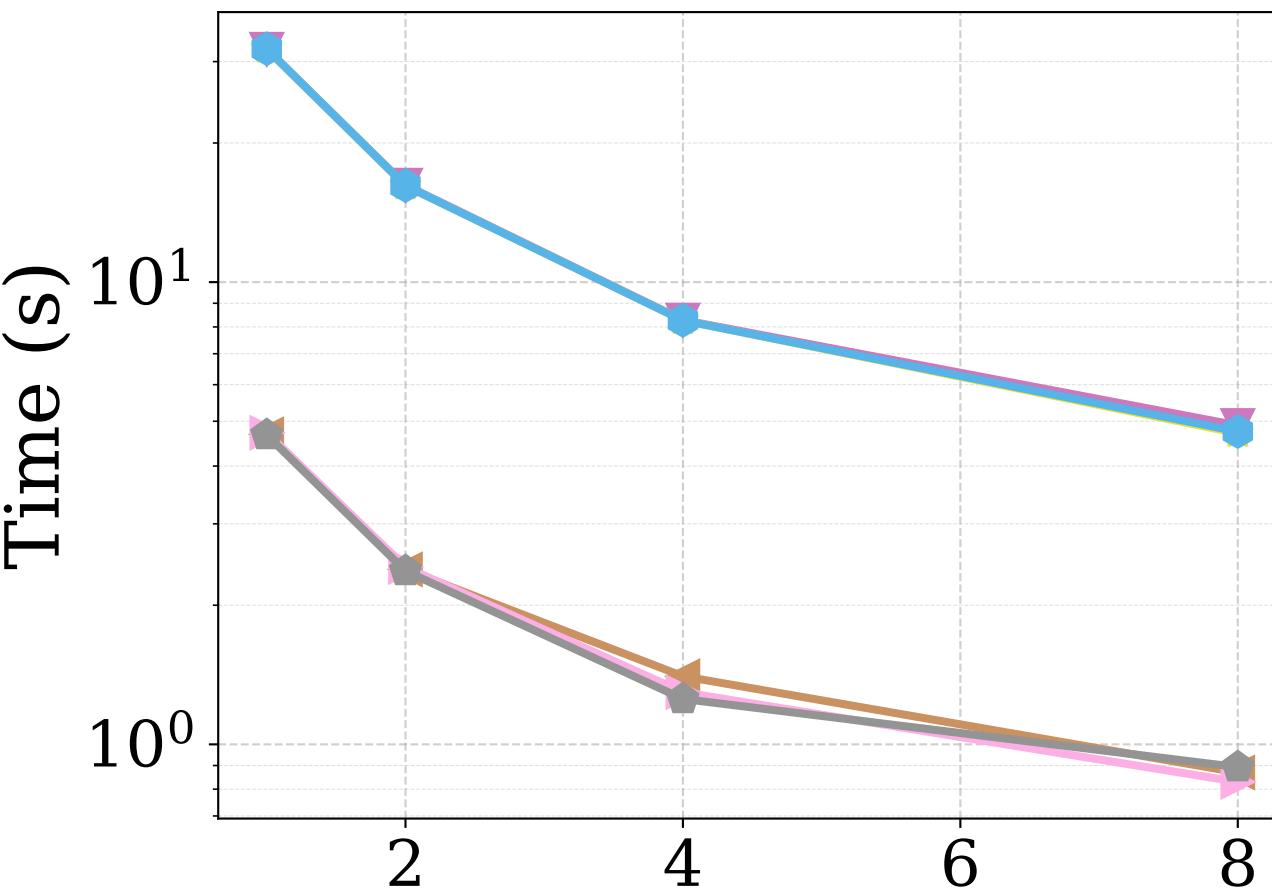
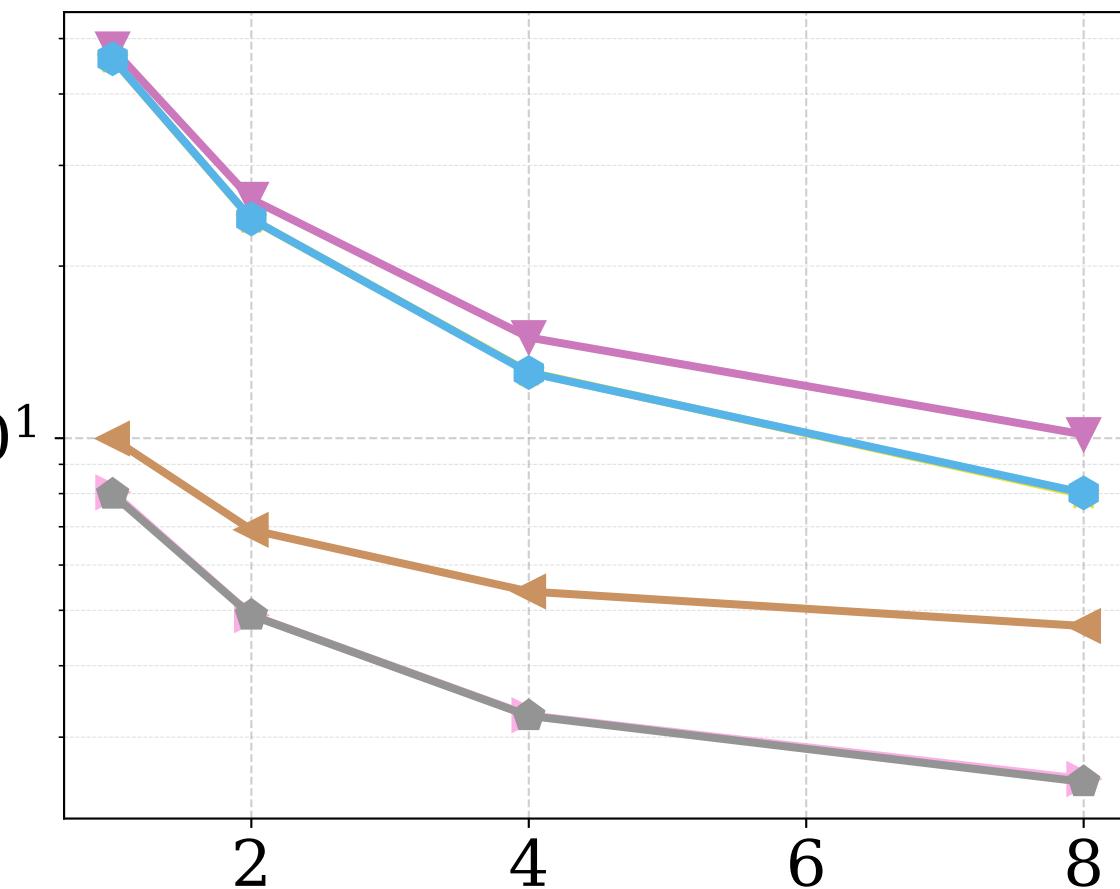


▼ CacheV2QPara  
 → SIMDPara  
 ★ CPara  
— CacheV2SIMDQPara  
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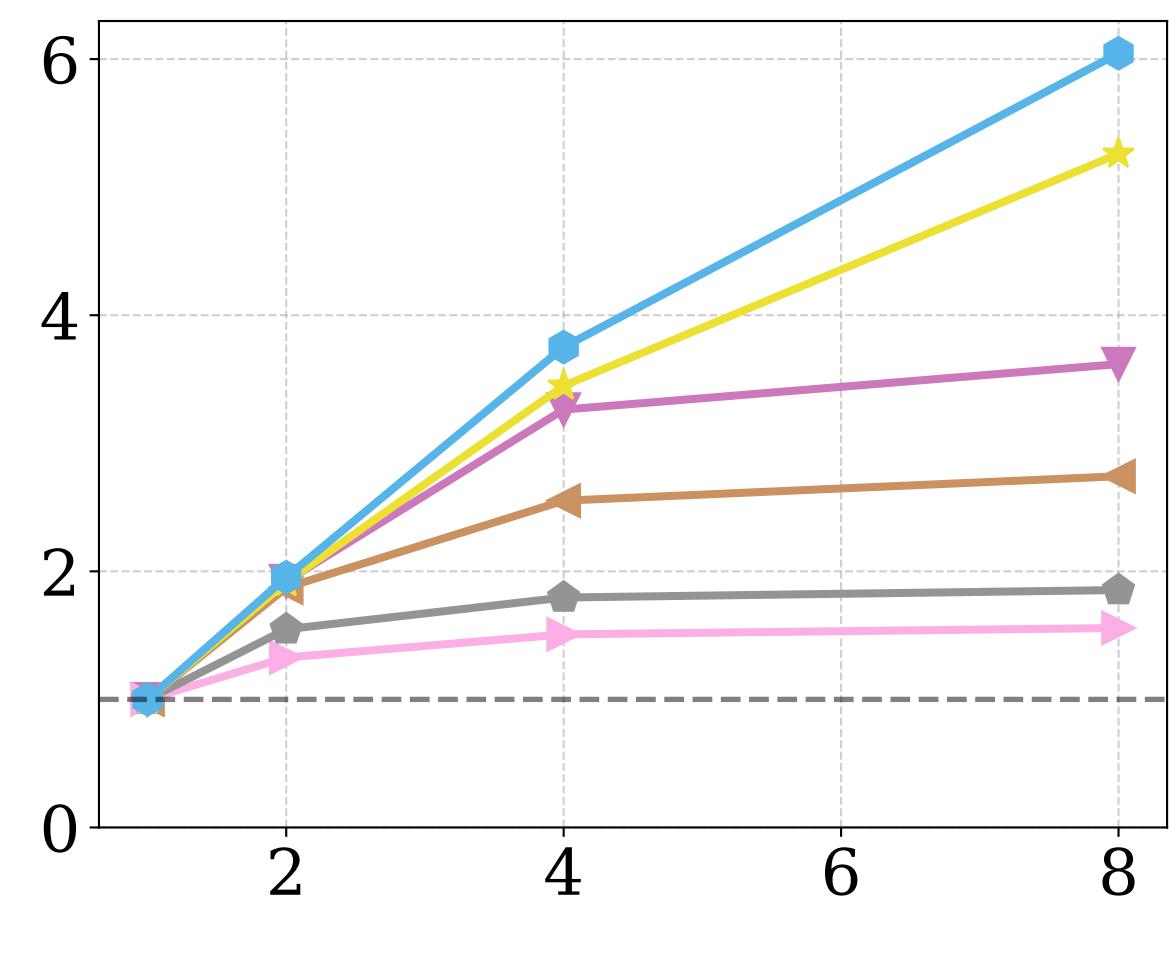
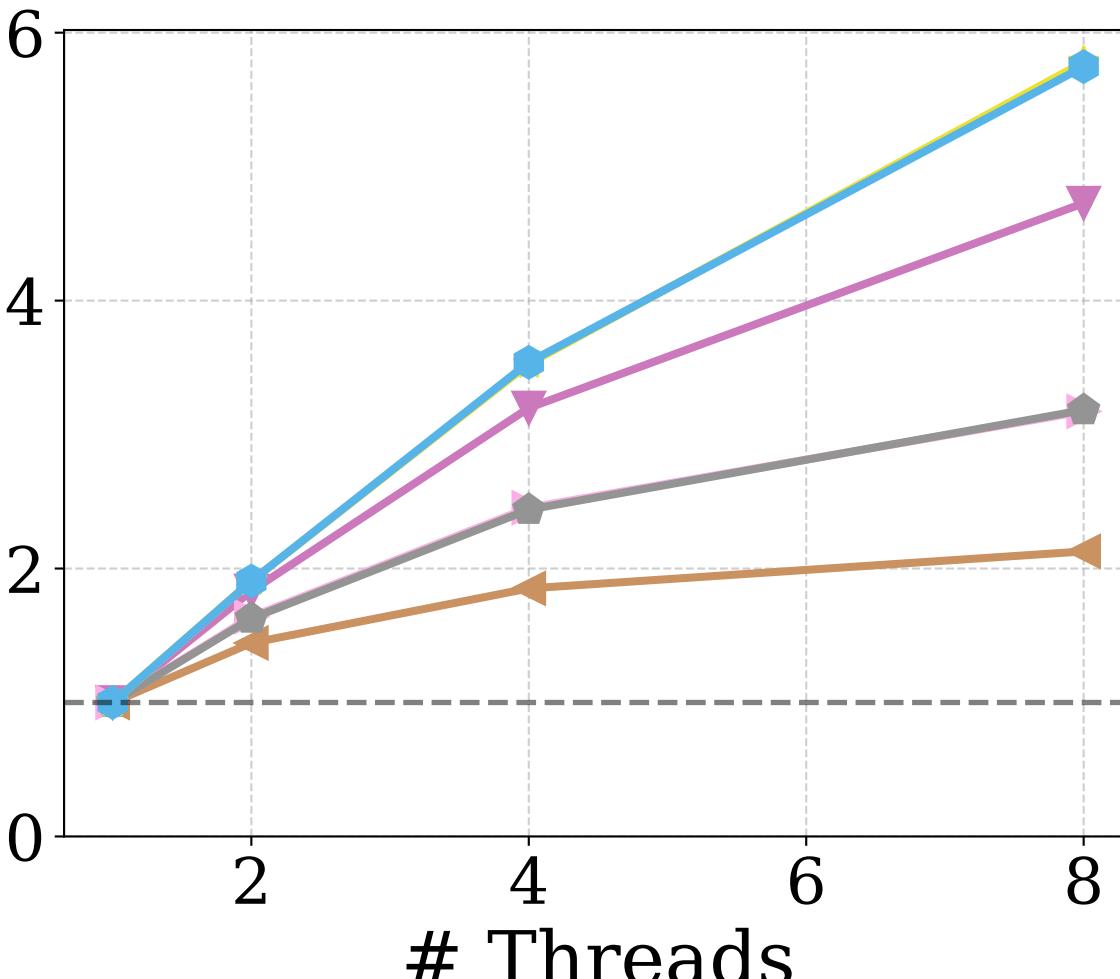
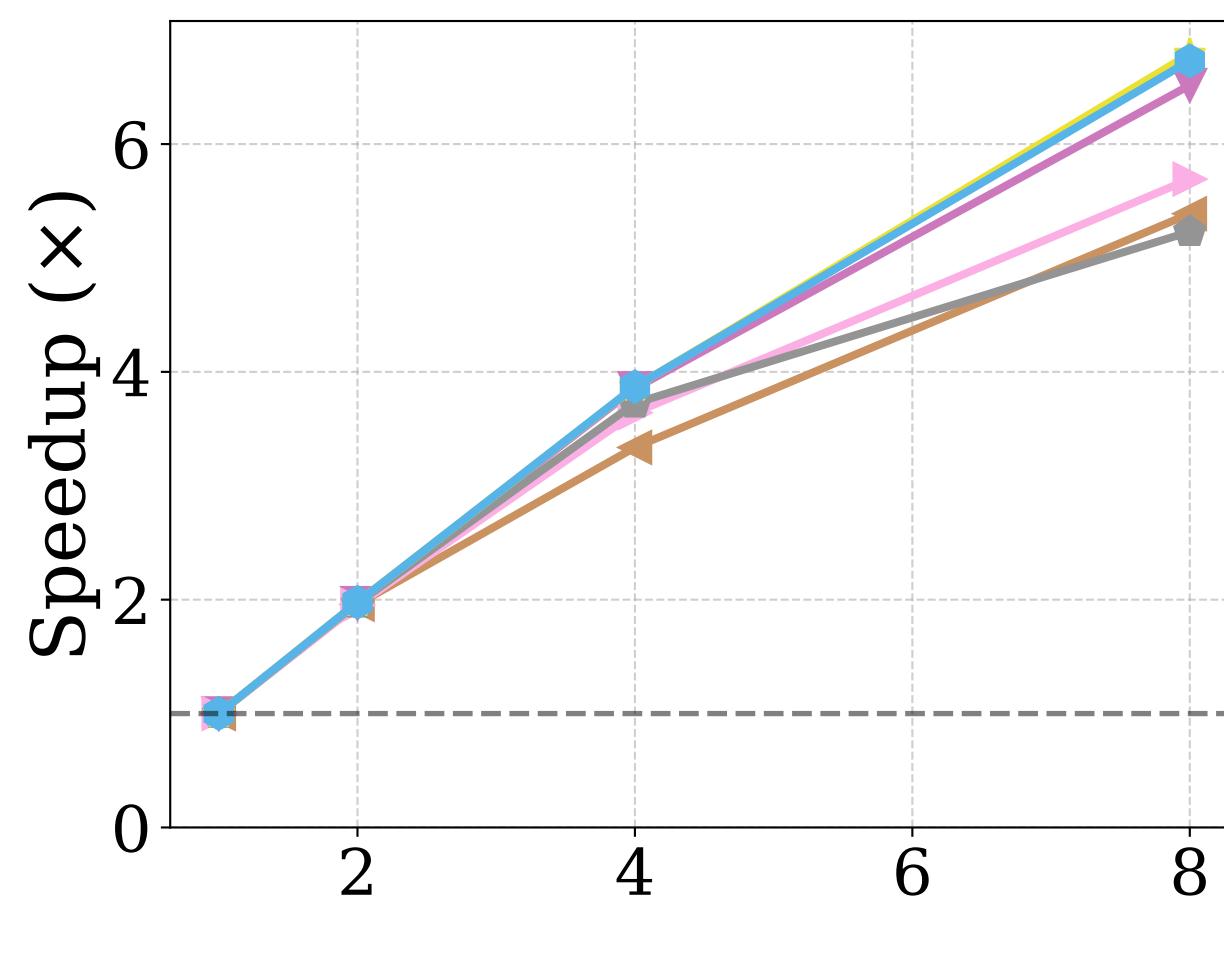
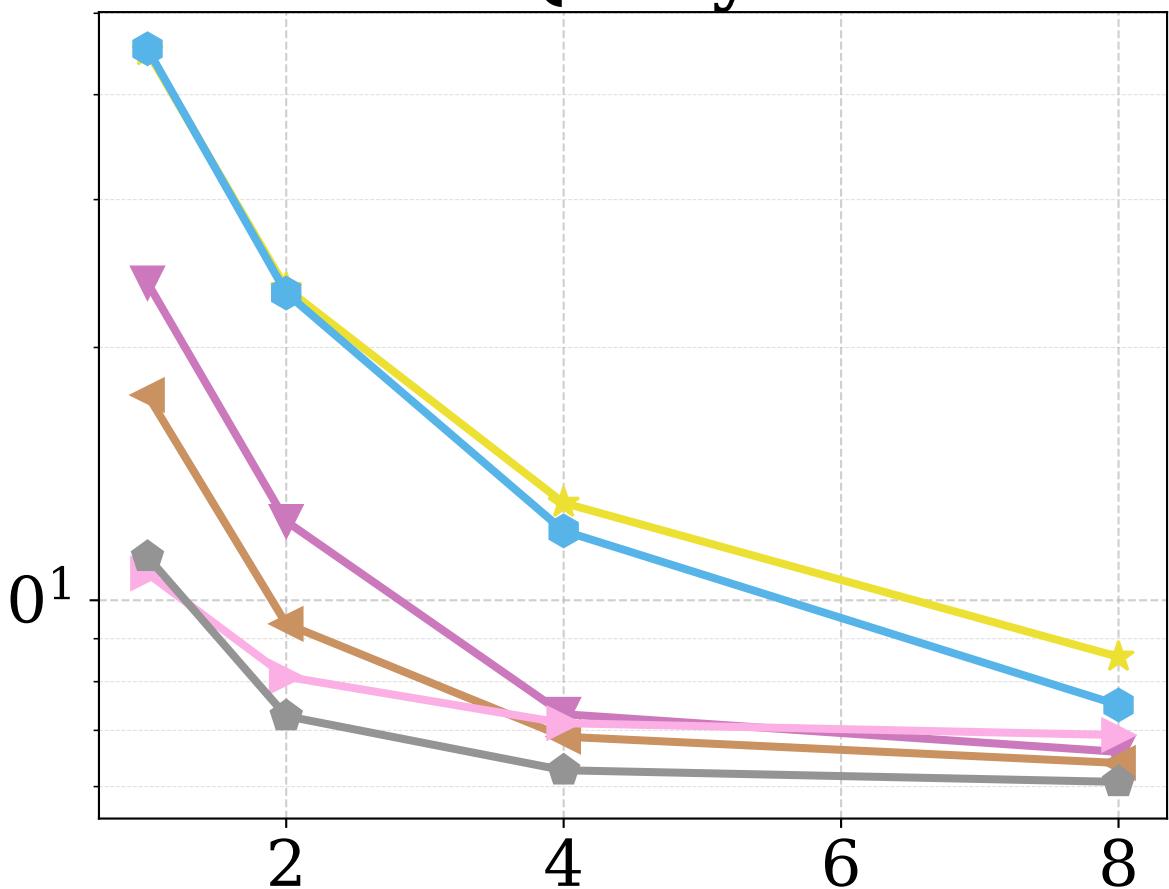
Train



Build



Query



# Threads