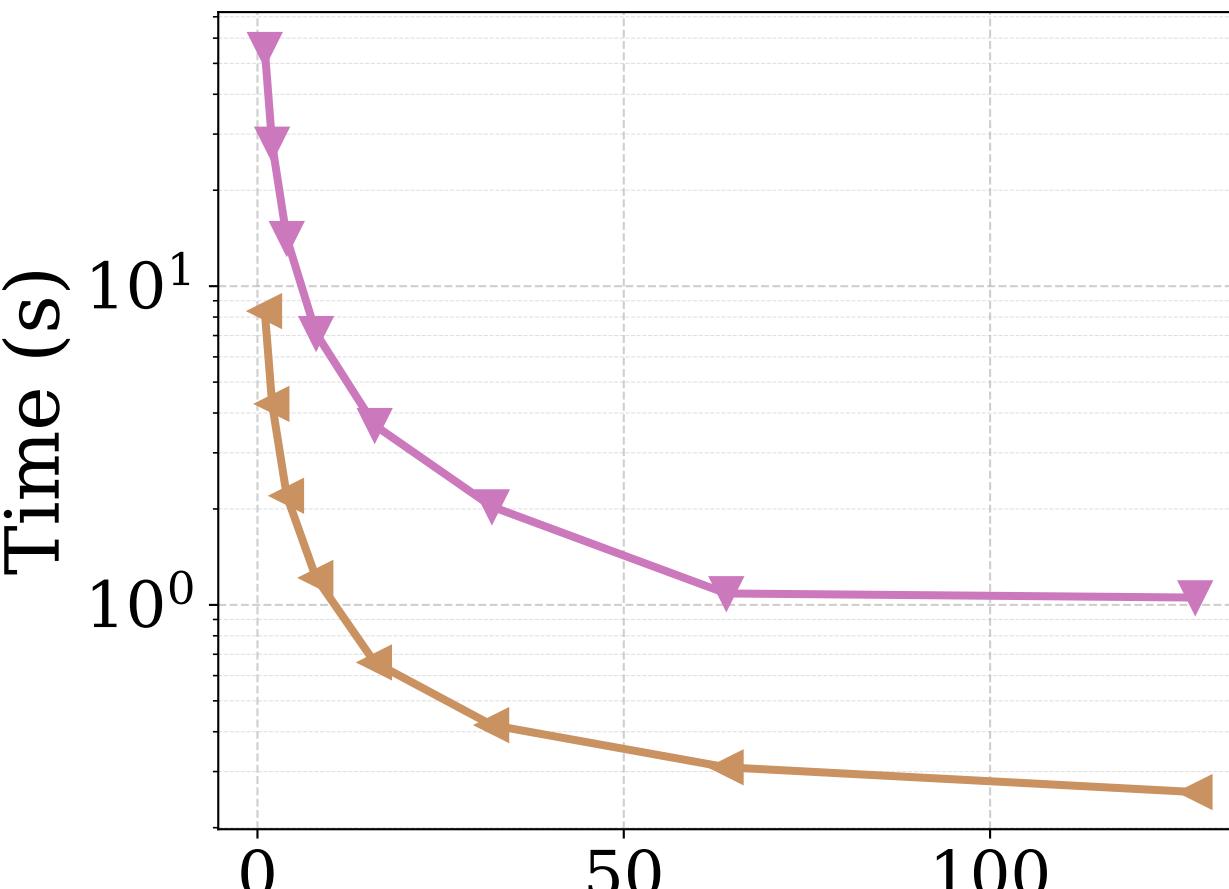
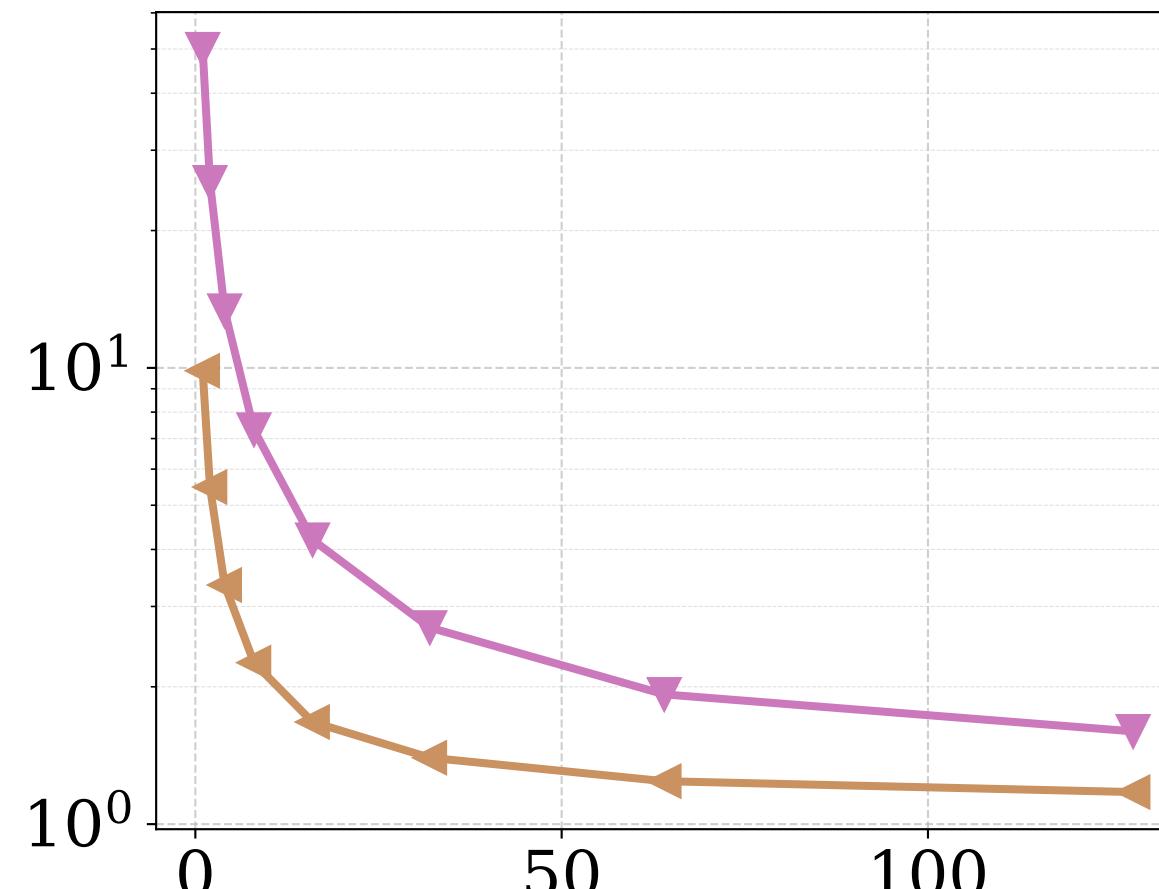


CacheV2QPara CacheV2SIMDQPara

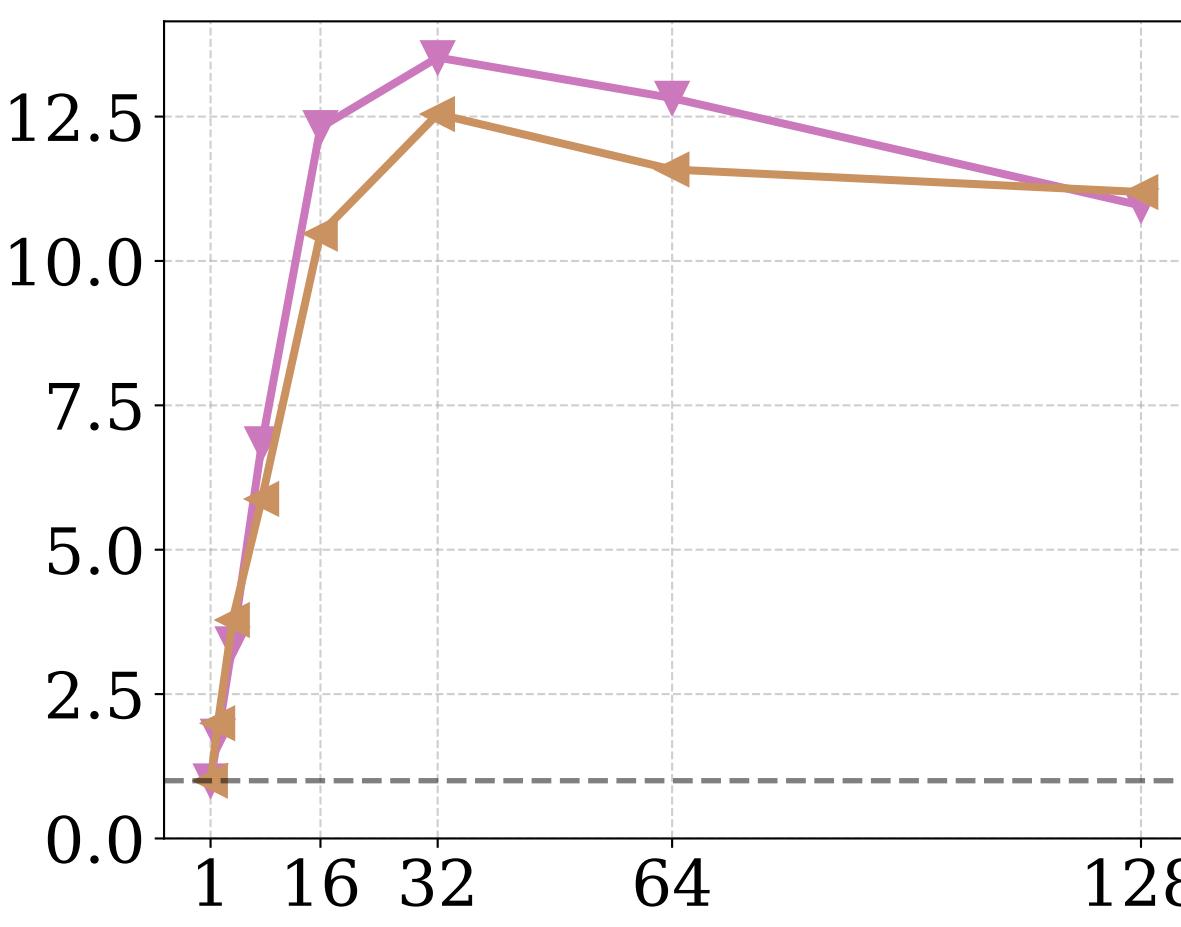
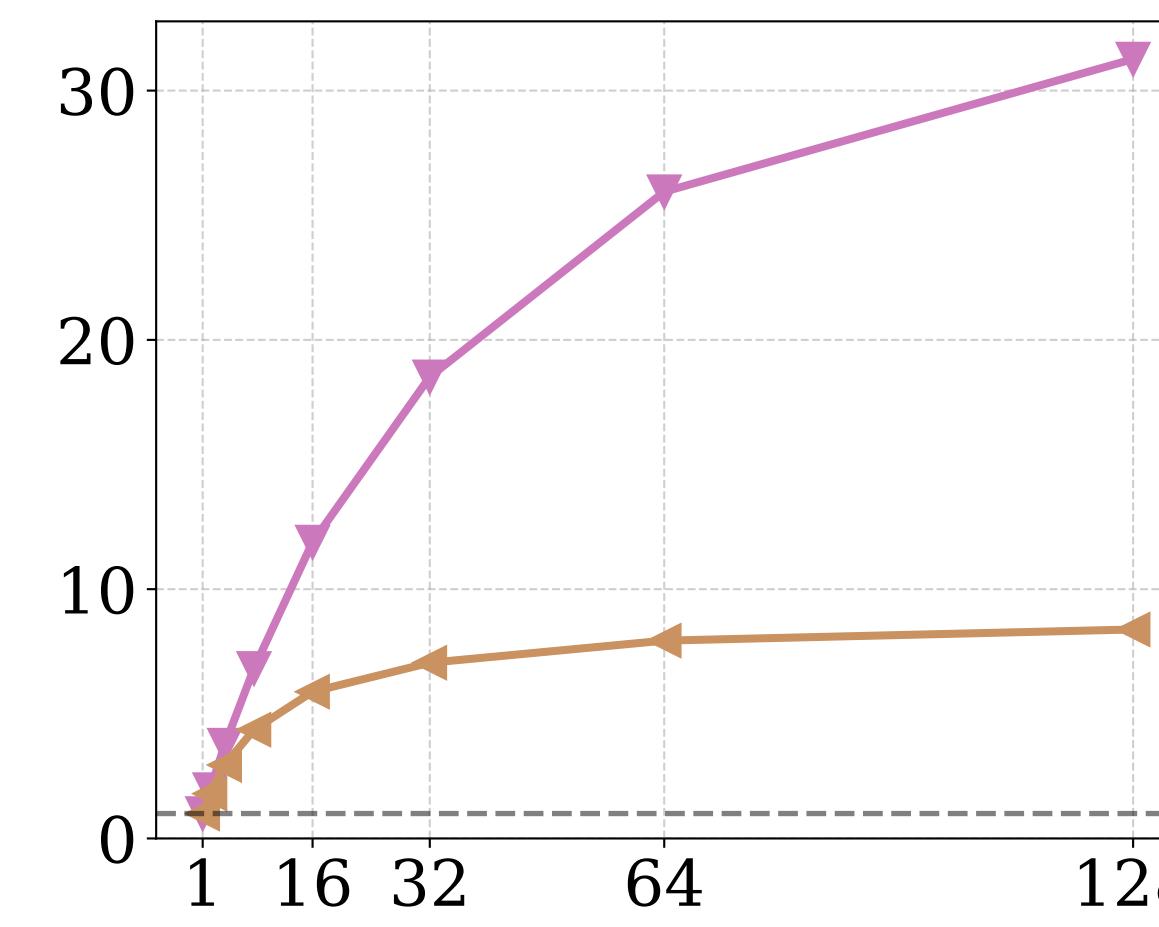
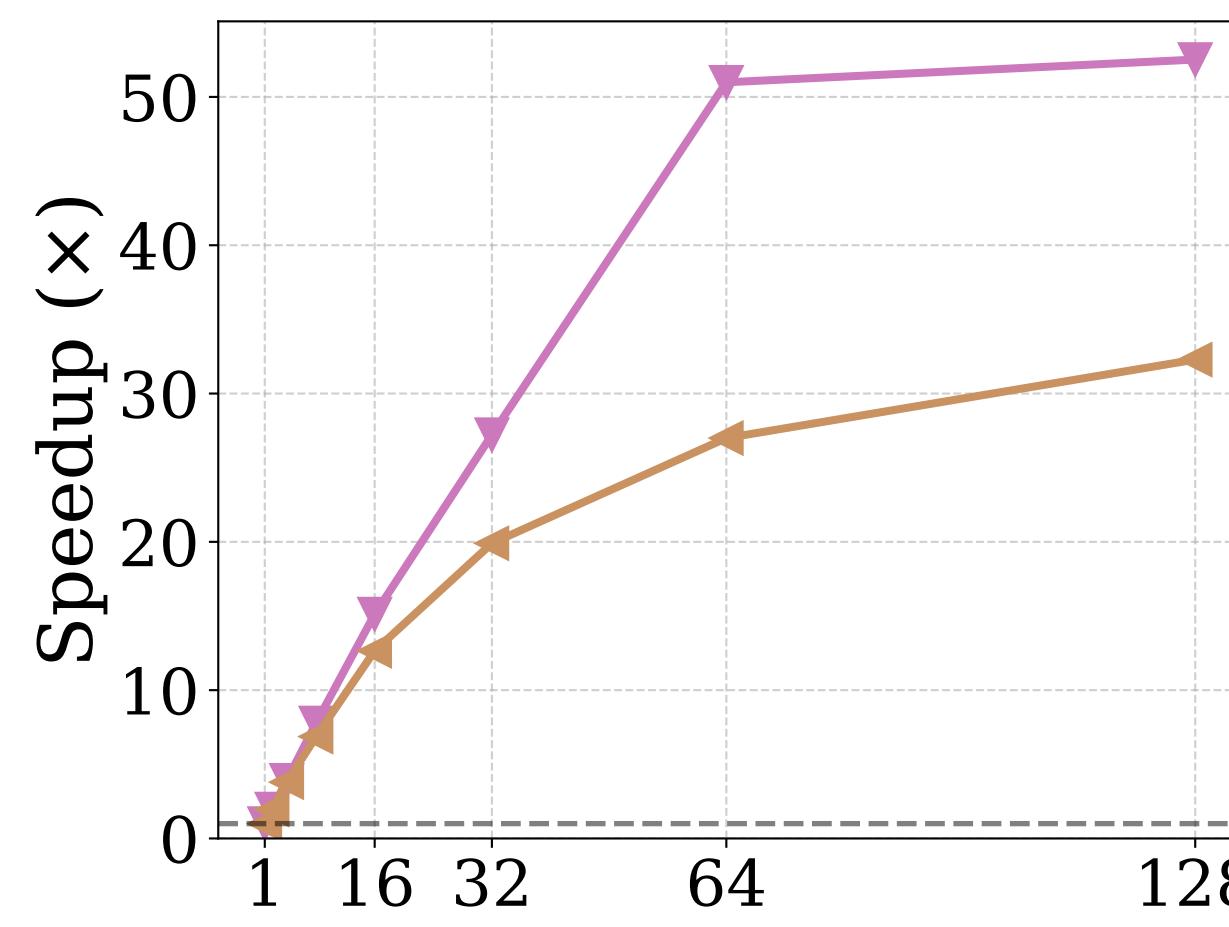
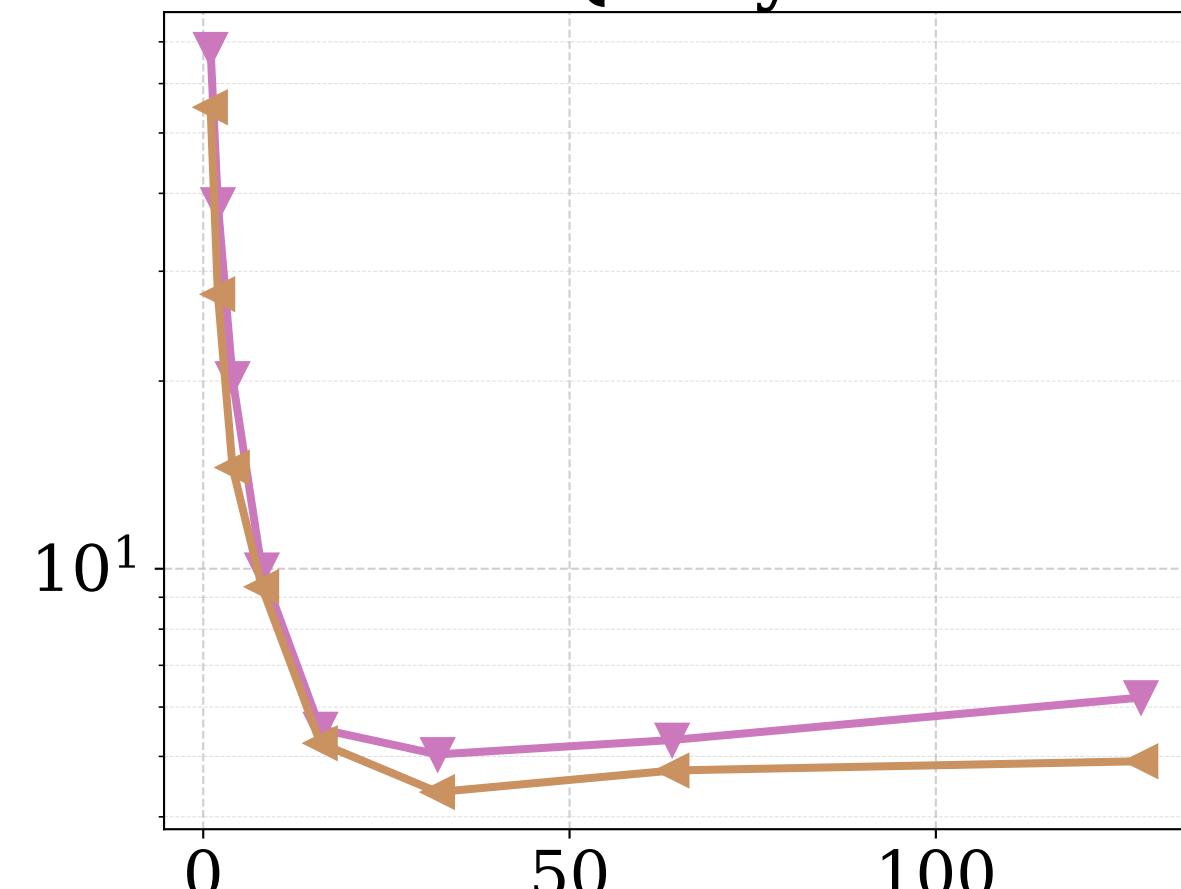
Train



Build



Query



Threads