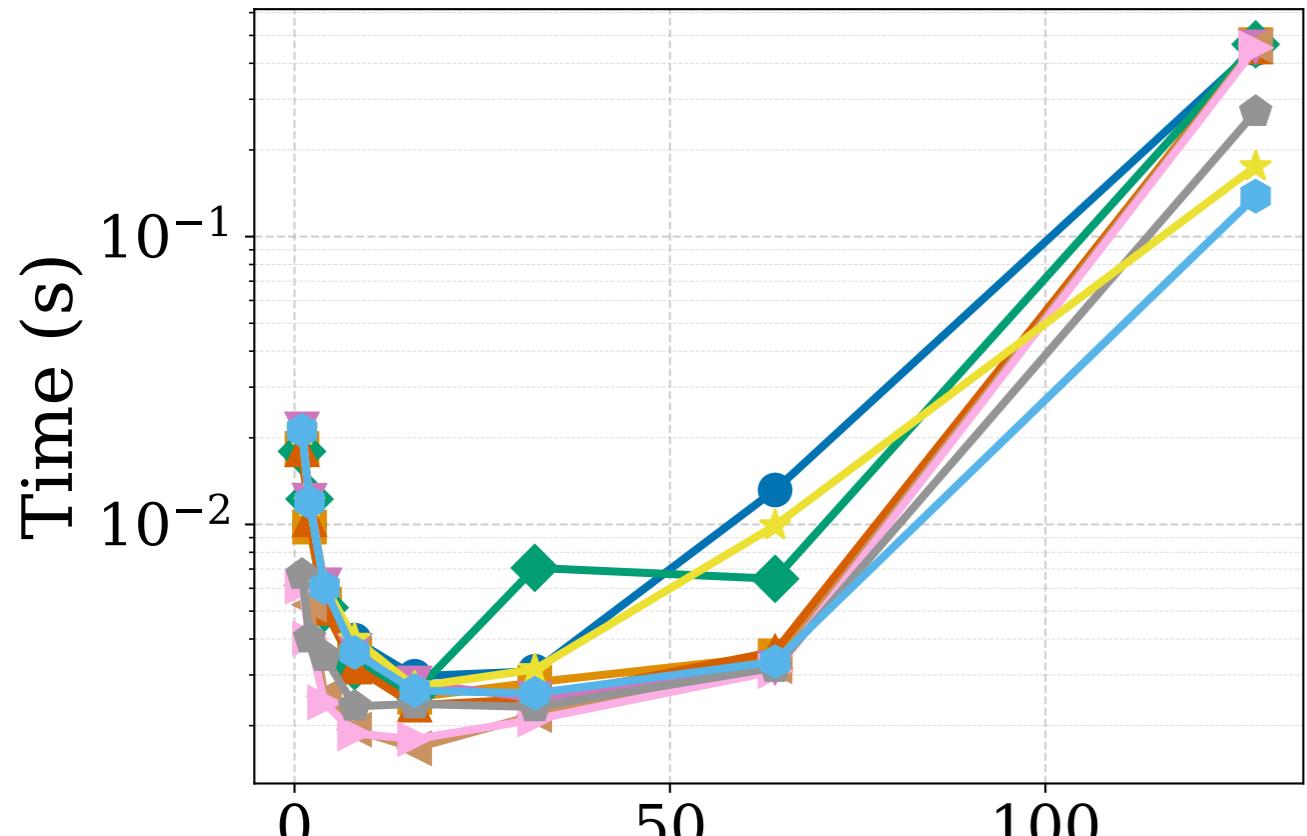
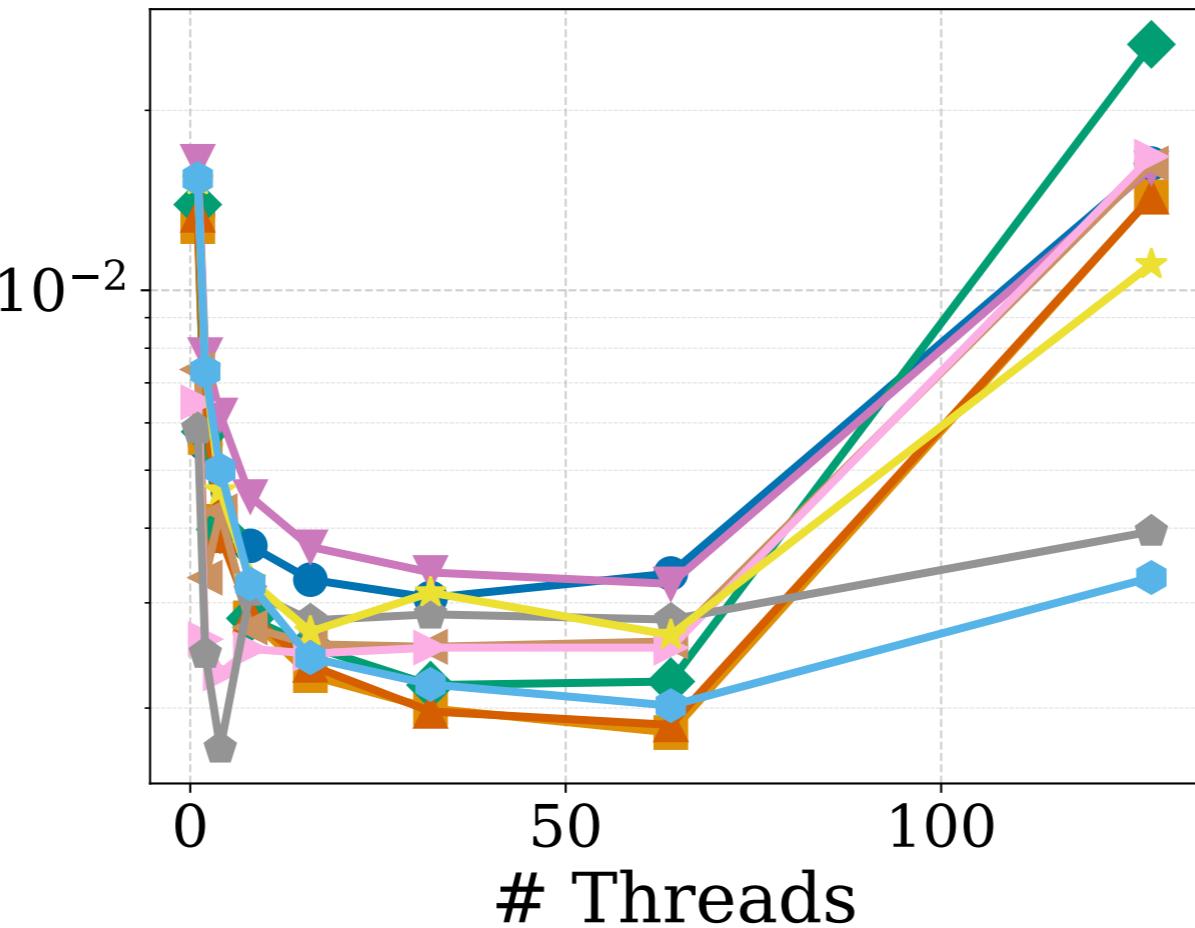


● CacheCPara	◆ CacheSIMDCPara	▼ CacheV2QPara	→ SIMDPara	★ ScalarCPara
■ CacheQPara	▲ CacheSIMDQPara	◀ CacheV2SIMDQPara	◆ SIMDQPara	○ ScalarQPara

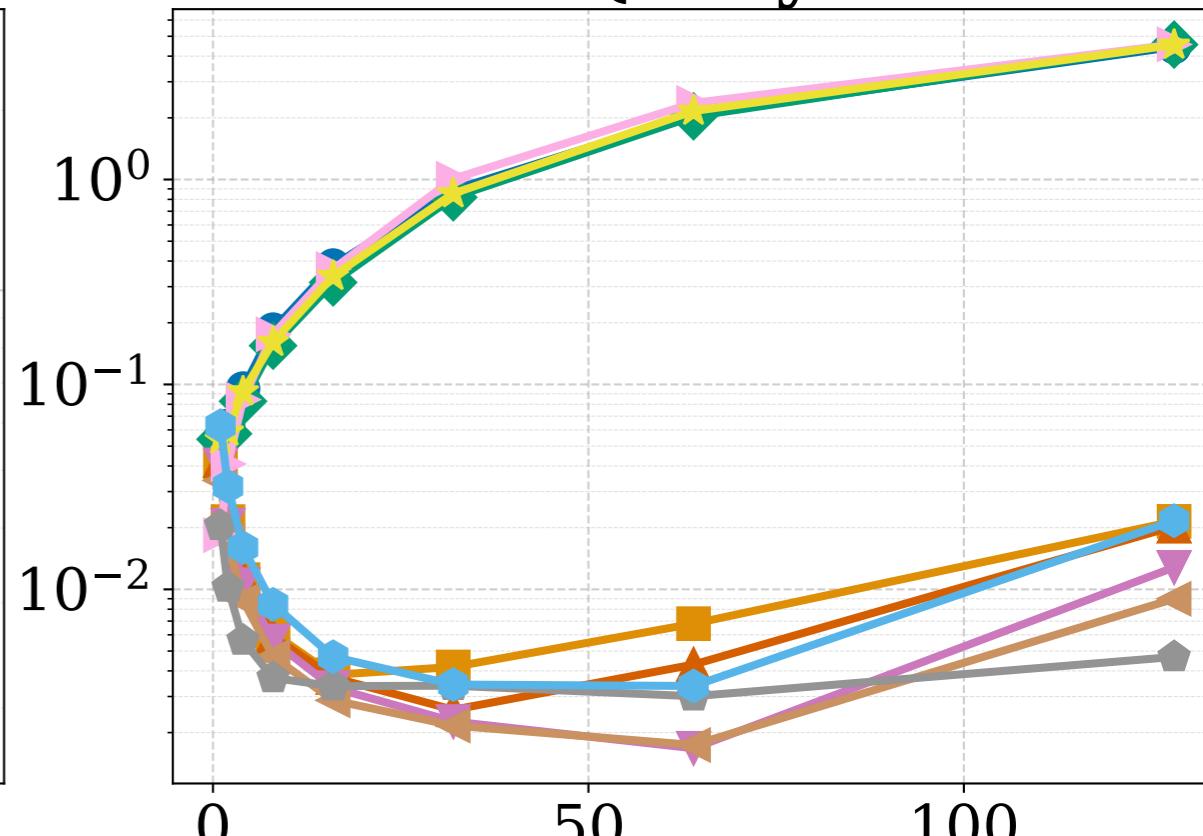
Train



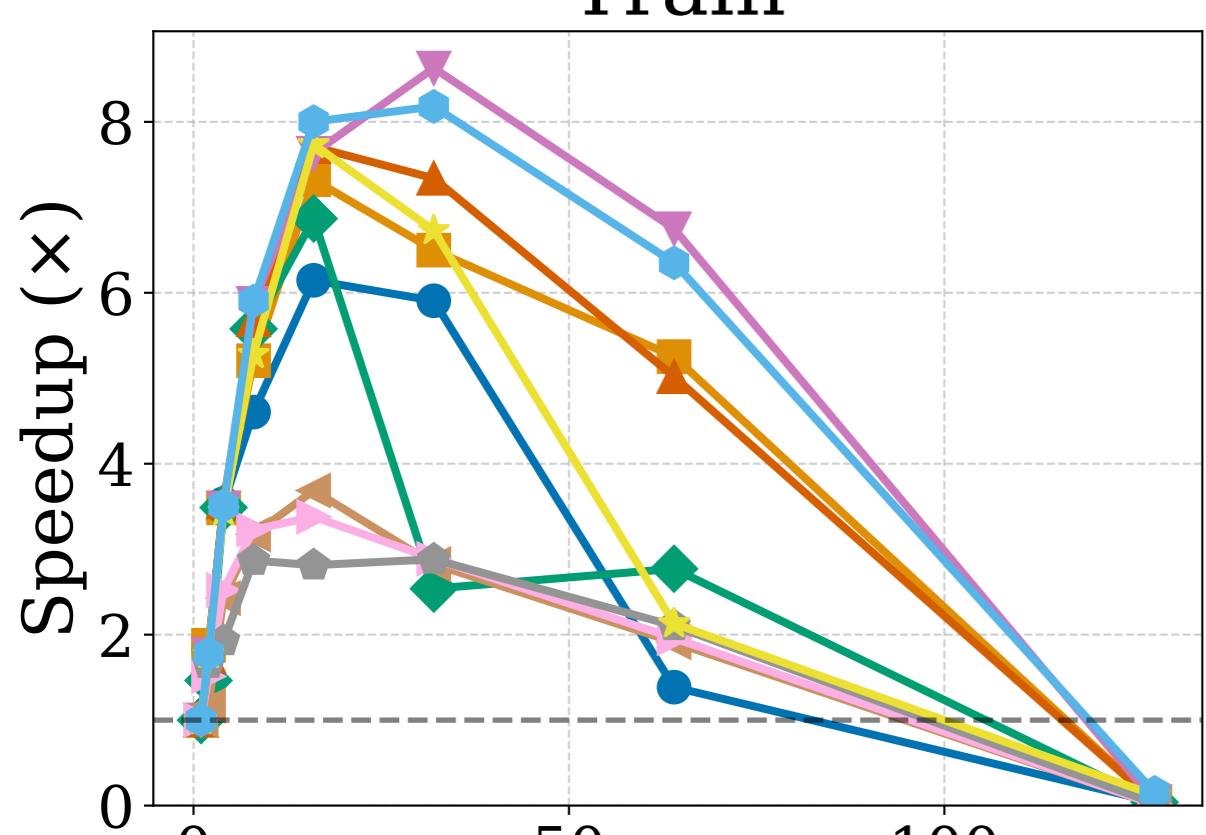
Build



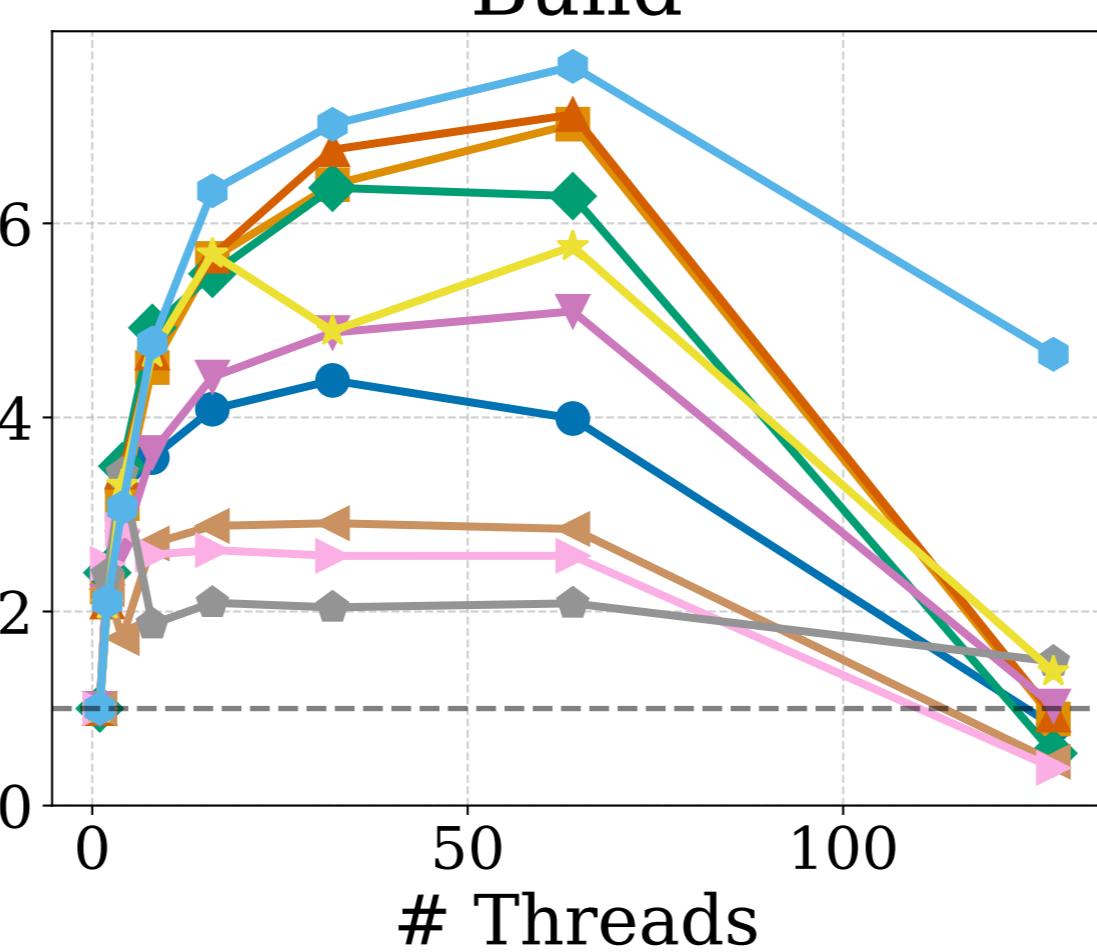
Query



Train



Build



Query

