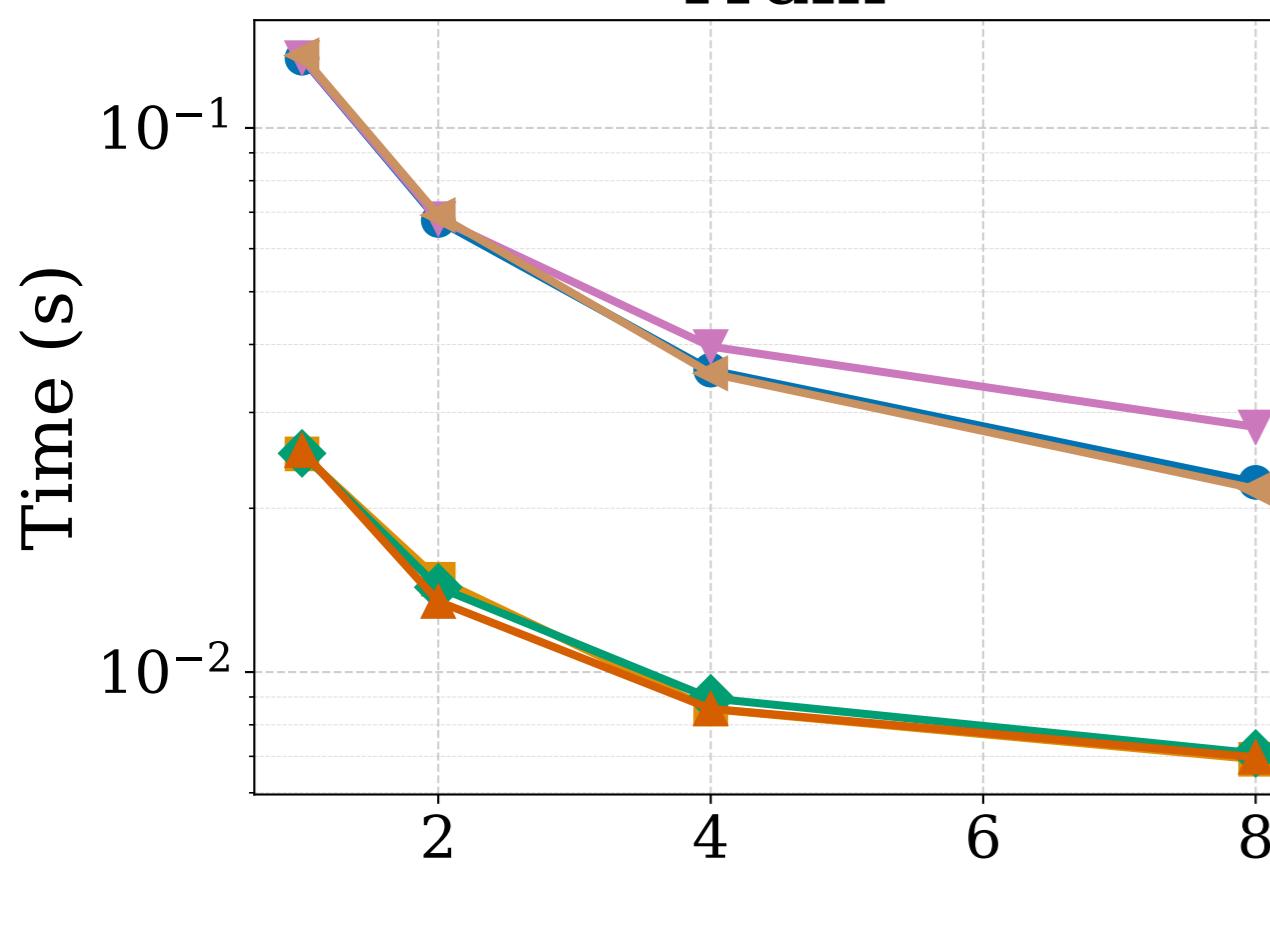


● CacheV2QPara
■ CacheV2SIMDQPara

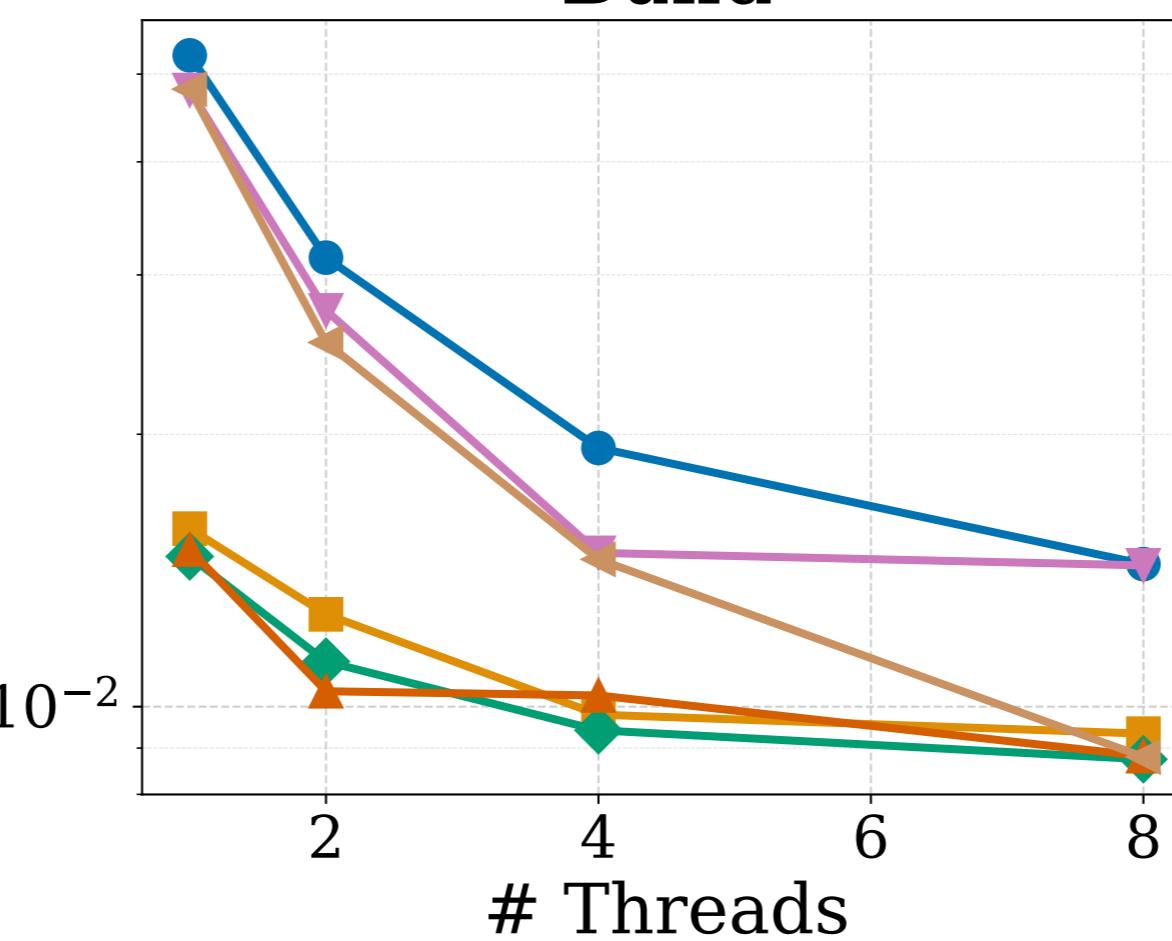
◆ SIMDCTPara
▲ SIMDQPara

▼ ScalarCPara
◀ ScalarQPara

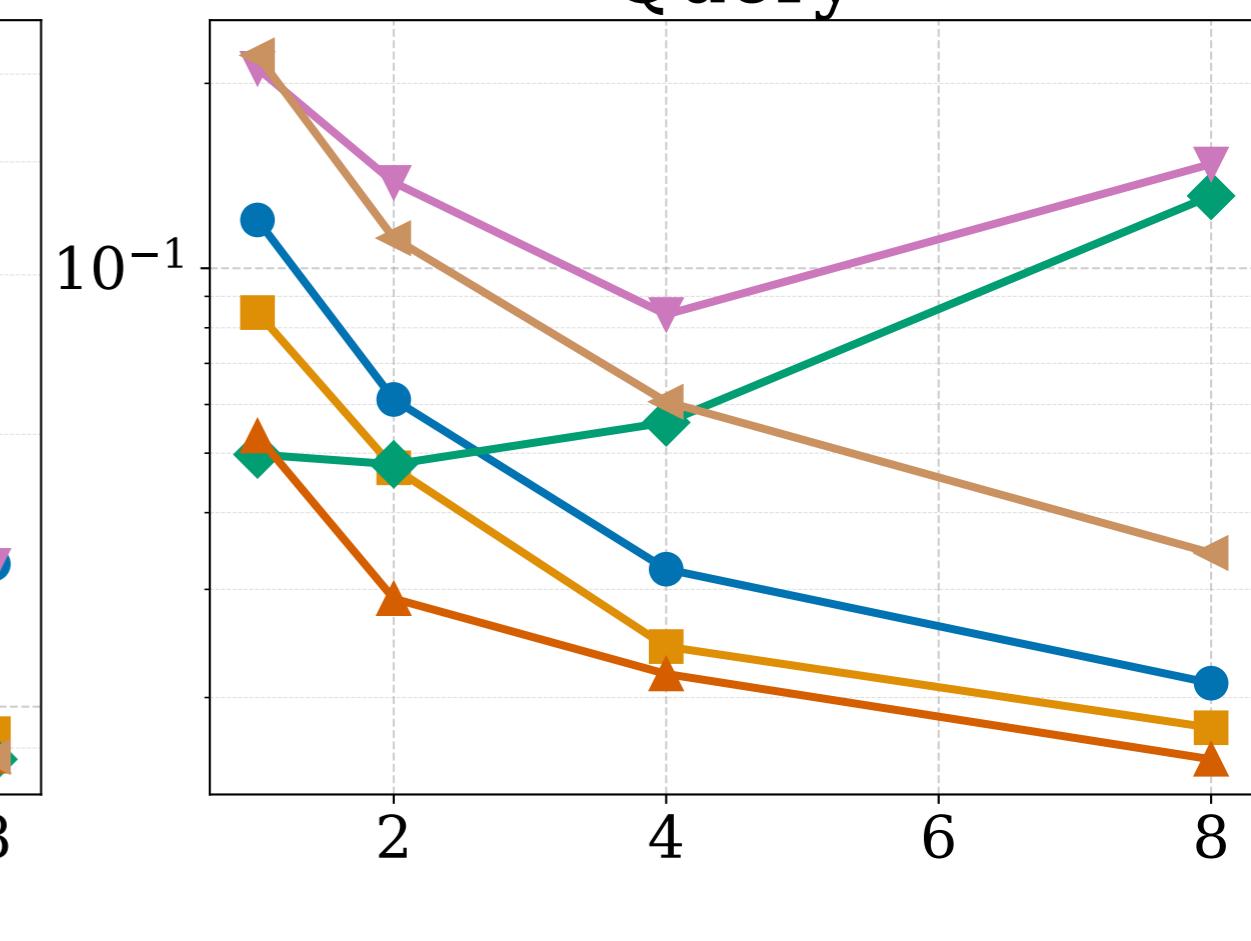
Train



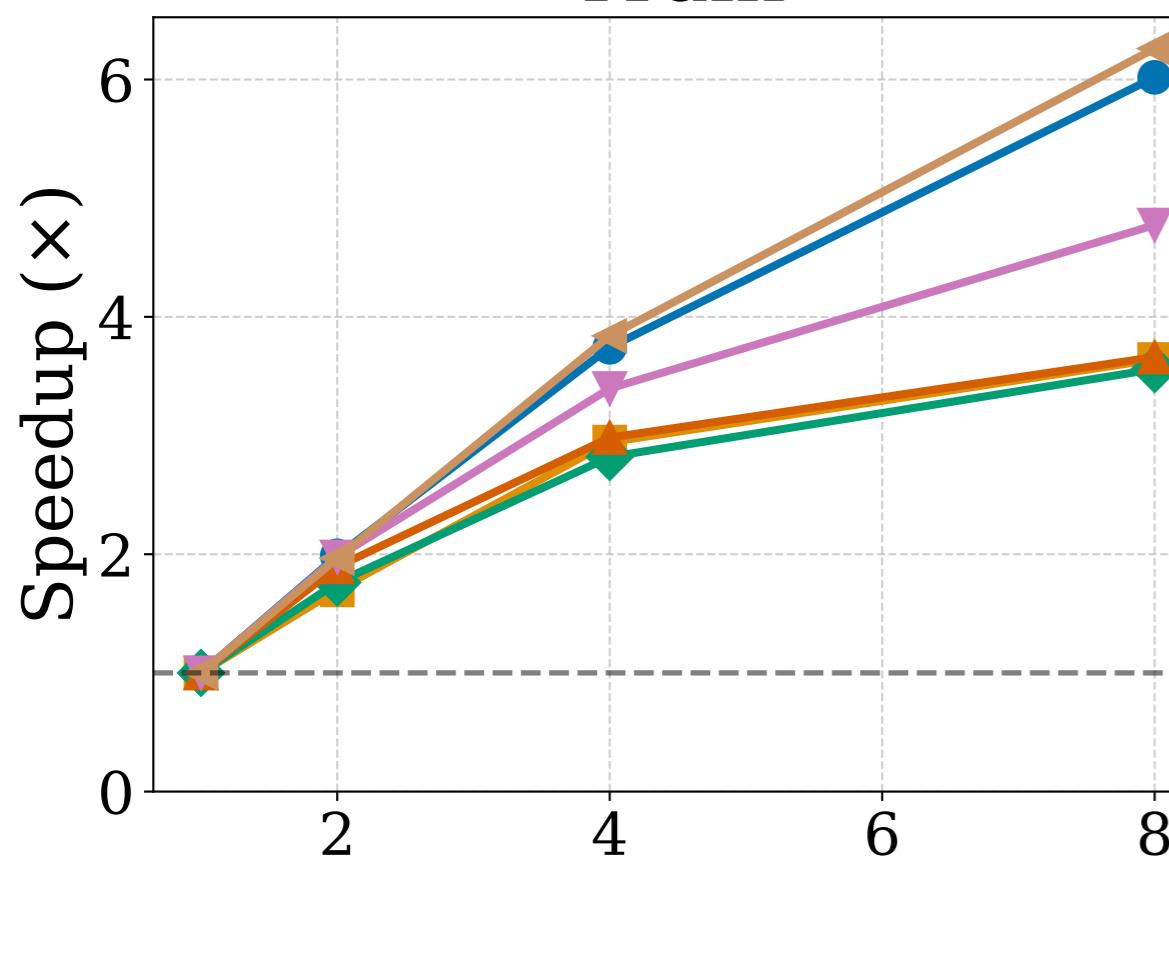
Build



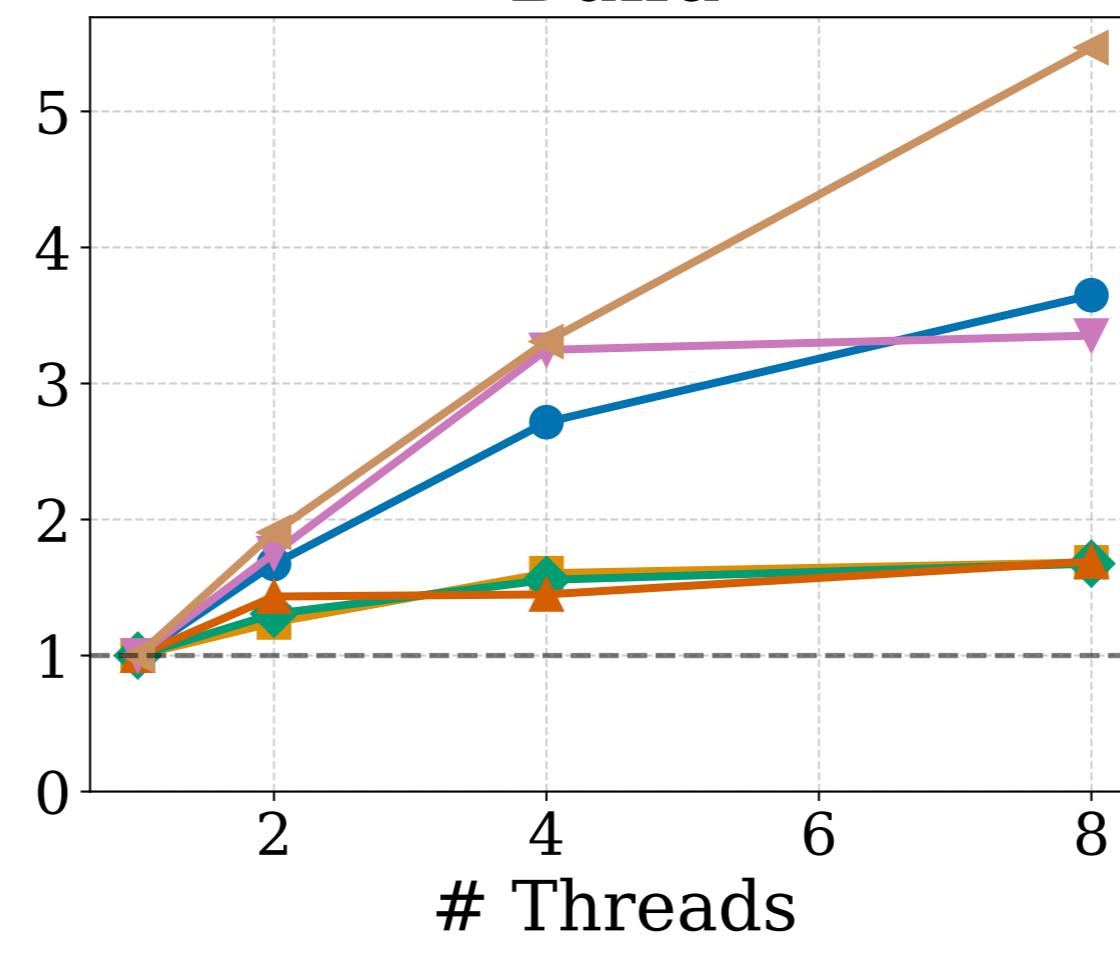
Query



Train



Build



Query

