JUNE 2010

CONSOLIDATED MEMORANDUM OF MARKS EXAMINATION: B. E. (INFORMATION TECHNOLOGY)[2006-07 TO 2009-10]

213

ROLL NO. : 02075002

NAME: ANUGUA KONGHADA

FATHER'S NAME : NACESUADA PAD KONCHADA

.	SUBJECTS	U. EXAM		HADA SESS.		SL.	OUR TOTAL	U. EXAM		SESS.	
Ď.		MAX MARKS S	MARKS	MAX MARKS S	MARKS	NO.	SUBJECTS	MAX MARKS	MARKS SECURED	MAX MARKS	MAI
	I YEAR						I YEAR				
4	ENGLISH	75	56	25	22	0	ENGINEERING GRAPHICS	100	77	50	1
-	MATHEMATICS - I	75	64	25	23		WORKSHOP PRACTICE	50	45	25	
								-		25	
	MATHEMATICS - II	75	72	25	23		ENGINEERING MECHANICS	75	57		
	ENGINEERING PHYSICS	75	68	25	24		PROGRAMMING IN C & C++	75	70	25	
	PHYSICS LAB	50	48	25	25		PROGRAMMING LAB	50	44	25	
	ENGINEERING CHEMISTRY	75	72	25	24	13	ENGLISH LANGUAGE LAB	201 100	===	25	
7	CHEMISTRY LAB	50	36	25	24						
-											
	II YEAR I SEMESTER						II YEAR II SEMESTER				
	DISCRETE MATHEMATICS	75	74	25	24	1	PROBABILITY &RANDOM PROCESSES	75	69	25	
2	MICRO ELECTRONICS	75	60	25	21	2	SIGNALS & SYSTEMS	75	74	25	
	DIGITAL ELECNS. & LOGIC DESIGN	75	63	25	20		ELECTRONIC COMMU. TECHNIQUES	75	69	25	
-	DATA STRUCTURES	75	60	25	24		COMP. ORGANI. &MICRO PROCESSORS		68	25	
	ELECTRICAL ENGINEERING	75	45	25	25		OOP using JAVA	75	75	25	
	ENVIRONMENTAL STUDIES	75	56	25	23		SOFTWARE ENGINEERING	75	66	25	
	BASIC ELECTRONICS LAB	50	48	25	23	1	MICROPROCESSOR LAB	50	45	25	
- 1	DATA STRUCTURES LABORATORY	50	43	25	22	(5.5)	JAVA PROGRAMMING LAB	50	48	25	
				10.00	-	8		50	***	25	
7	MINI PROJECT - I	2001 2000	sied stea	25	24	7	MINI PROJECT - II		100 100	25	
	III YEAR I SEMESTER						III YEAR II SEMESTER				
1	MANG. ECONOMICS & ACCOUNTANCY	75	66	25	23	1	COMPILER CONSTRUCTOR	75	67	25	
2	DATA COMMUNICATION	75	75	25	25	2	COMPUTER NETWORKS	75	62	25	
3	DIGITAL SIGNAL PROCESSING	75	69	25	23	3	REAL TIME SYSTEMS	75	75	25	
	DATABASE SYSTEMS	75	75	25	20		OBJECT ORIENTED SYSTEMS DEV.	75	64	25	
	OPERATING SYSTEMS	75	67	25	25		ARTIFICIAL INTELLIGENCE	75	66	25	
	THEORY OF AUTOMATA	75	65	25	24		DESIGN & ANALYSIS OF ALG.	75	68	25	
	OPERATING SYSTEMS LAB	50	43	25	24	1000	OOSD LAB	50	47	25	
	DATABASE LAB	50	43	25	21		NETWORK PROGRAMMING LAB	50	48	25	
	MINI PROJECT - I	30	73	25	25		MINI PROJECT - IV	==	70	25	
	TIAITA TINUVENT - 1				Time and	,	TIATA I NOVEDI - LV			23	
	IV YEAR I SEMESTER						IV YEAR II SEMESTER				
	VLSI DESIGN	75	61	25	23	1	EMBEDDED SYSTEMS	75	48	25	
	WIRELESS & MOB. COMMUNICATIONS	75	67	25	22		INFORMATION RETRIEVAL SYSTEMS		69	25	
									69	25	
	MIDDLEWARE TECHNOLOGIES	75	60	25	25		HUMAN COMPUTER INTERACTION	75		100	
	DATA WAREHOUSING AND DATA MIN		69	25	23		EMBEDDED SYSTEMS LAB	50	42	25	
	DIGITAL IMAGE PROCESSING	75	61	25	22		PROJECT	GRD	EX	50	
	VLSI DESIGN LAB	50	46	25	24	6	SEMINAR	200 1001	1007 5000	25	
7	MIDDLEWARE TECH. LAB	50	48	25	24						
				area, area	2000 Mg						
	PROJECT SEMINAR	100	200	25	24						

Note: Bridge course I&II Sems/Environmental studies subjects marks are not included in the Aggregate DIVISION FIRST WITH DISTINCTION AGGREGATE: 4770 AGGREGATE IN WORDS: ** FOUR SEVEN SEVEN ZERO **

G. Total : 5300