

#### CONSOLIDATED MEMORANDUM OF MARKS

JUNE 2010

EXAMINATION: B. E. (INFORMATION TECHNOLOGY)[2006-07 TO 2009-10]

SL.	OUR IFOTO	U. EX	AM	SES	SS.	SL.	CURIFOTO	U. E.	XAM	SE	88.
Ю.	SUBJECTS	MAX MARKS S	MARKS ECURED	MAX MARKS	MARKS	NO.	SUBJECTS	MAX MARKS	MARKS SECURED	MAX MARKS	MARKS
	I YEAR						I YEAR				
23456	ENGLISH MATHEMATICS - I MATHEMATICS - II ENGINEERING PHYSICS PHYSICS LAB ENGINEERING CHEMISTRY	75 75 75 75 50 75	56 64 72 68 48 72	25 25 25 25 25 25 25	22 23 23 24 25 24	9 10 11 12	ENGINEERING GRAPHICS WORKSHOP PRACTICE ENGINEERING MECHANICS PROGRAMMING IN C & C++ PROGRAMMING LAB ENGLISH LANGUAGE LAB	100 50 75 75 50		50 25 25 25 25 25	24 24 25 25 21
7	CHEMISTRY LAB	50	36	25	24		TI VENE III GENERALE				
	II YEAR I SEMESTER						II YEAR II SEMESTER				
2345678	DISCRETE MATHEMATICS MICRO ELECTRONICS DIGITAL ELECNS. & LOGIC DESIGN DATA STRUCTURES ELECTRICAL ENGINEERING ENVIRONMENTAL STUDIES BASIC ELECTRONICS LAB DATA STRUCTURES LABORATORY MINI PROJECT - I	75 75 75 75 75 75 75 50	74 60 63 60 45 56 48 43	25 25 25 25 25 25	24 21 24 25 23 22 24 25 22 24	2 3 4 5 6 7 8	PROBABILITY &RANDOM PROCESSES SIGNALS & SYSTEMS ELECTRONIC COMMU. TECHNIQUES COMP. ORGANI. &MICRO PROCESSORS OOP using JAVA SOFTWARE ENGINEERING MICROPROCESSOR LAB JAVA PROGRAMMING LAB MINI PROJECT — II	75 75		25 25 25 25 25 25 25 25 25 25 25 25 25 2	24 24 24 24 24 24 24 24
	III YEAR I SEMESTER						III YEAR II SEMESTER				
2345678	MANG. ECONOMICS & ACCOUNTANCY DATA COMMUNICATION DIGITAL SIGNAL PROCESSING DATABASE SYSTEMS OPERATING SYSTEMS THEORY OF AUTOMATA OPERATING SYSTEMS LAB DATABASE LAB MINI PROJECT - I	75 75 75 75 75 75 50	66 75 69 75 67 65 43 43	25 25 25 25 25 25 25 25 25 25 25 25 25 2	23 25 20 25 24 21 25 21 25	2 3 4 5 6 7 8	COMPILER CONSTRUCTOR COMPUTER NETWORKS REAL TIME SYSTEMS OBJECT ORIENTED SYSTEMS DEV. ARTIFICIAL INTELLIGENCE DESIGN & ANALYSIS OF ALG. OOSD LAB NETWORK PROGRAMMING LAB MINI PROJECT - IV	75 75 75 75 75 75 50 50	62 75	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	20 20 20 20 20 20 20 20 20 20 20 20 20 2
	IV YEAR I SEMESTER						IV YEAR II SEMESTER				
234567	VLSI DESIGN WIRELESS & MOB. COMMUNICATIONS MIDDLEWARE TECHNOLOGIES DATA WAREHOUSING AND DATA MIN DIGITAL IMAGE PROCESSING VLSI DESIGN LAB MIDDLEWARE TECH. LAB PROJECT SEMINAR	75 75 75 75 75 50	61 67 60 69 61 46 48	25 25 25 25 25 25 25 25 25 25 25 25 25	22 25 22 24 24 24 24 24 24	3 4 5	EMBEDDED SYSTEMS INFORMATION RETRIEVAL SYSTEMS HUMAN COMPUTER INTERACTION EMBEDDED SYSTEMS LAB PROJECT SEMINAR	75 75 75 50 GRD	48 69 69 42 EX	25 25 25 25 50 25	24 49 25

Note: Bridge course I&II Sems/Environmental studies subjects marks are not included in the Aggregate DIVISION FIRST WITH DISTINCTION AGGREGATE: 4770 AGGREGATE IN WORDS: \*\* FOUR SEVEN SEVEN ZERO \*\*

G. Total : 5300

ou 242636

# Osmania University



Roll Ao. <u>02075002</u>

Medium: English



This is to certify that Anusha Konchada

son / daughter of Nageswara Rac Konchada

has been awarded the degree of

### Bachelor Of Engineering

Information Technology

of this University, having passed the qualifying Examination held in *June-2010*, in the *First* Division, Wilh Distinction

Given under the seal of the University

EN111000139



Myderabad Dated 25 Nov, 2010



T. Torretat lor

#### Degree Prepared, Verified & Compared by

Prepared by	Section Incharge	Controller of Examinations
Peuo	Course	Lamer

PF 207889



## Osmania University

**FACULTY OF ENGINEERING** 

Ref. No. 213

Provisional Certificate

Roll No. 02075002

	is to certify that _anusha	KONCHADA
	daughter of NAGESWAR, by the BACHELOR OF ENGIN	A RAO KONCHADA
	FORMATION TECHNOLOGY	_Examination of this University, held
ín	JUNE 2010	and that he/she was placed in
	FIRST WITH DISTINCTION	

Hyderabad A.P. Dated 13/07/2010



for Registrar

Section - incharge