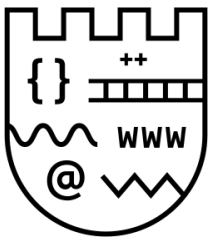

Aristotle University of Thessaloniki

Computer Science Department

Semester assignment in NNA-07-08

Digital Electronic Systems in VHDL

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