

| LED | COLOUR | STATUS |
|----------------|---------------|------------------------------|
| CPLD LED | RED | FPGA in reset |
| | GREEN (blink) | CPLD clock |
| | GREEN | FPGA configured |
| | BLUE | SPI PROM configuration mode |
| SYS LED TOP | RED | Golden Image Firmware loaded |
| | BLUE (blink) | IPBus 1Hz clock |
| SYS LED BOTTOM | RED | CDCE PLL not locked |
| | ORANGE | CDCE TTC clock out of phase |
| | GREEN | CDCE ok |
| MMC LED | GREEN | Loading firmware from SD |