

AD5933::increase_frequency

```
graph LR; A[AD5933::increase_frequency] --> B[AD5933::write_register]; B --> C[AD5933::read_register];
```

The diagram illustrates a three-step process for the AD5933 chip. It begins with a gray-shaded box labeled 'AD5933::increase_frequency'. A blue arrow points from this box to a white box labeled 'AD5933::write_register'. Another blue arrow points from the 'write_register' box to a final white box labeled 'AD5933::read_register'.

AD5933::write_register

AD5933::read_register