

Akrosh Vikram Shukla

B.Tech - Electronics and Communication Engineering

Indian Institute Of Information Technology, Una

EDUCATION

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Degree/Certificate	Institute/Board	CGPA/Percentage	Year
B.Tech	Indian Institute of Information Technology, Una	9.76	2025
Senior Secondary	GAIL DAV Public School/ CBSE Board	96.2%	2021
Secondary	GAIL DAV Public School/ CBSE Board	98.8%	2019

EXPERIENCE

- NTPC Gas Power Station, AURAIYA**
(Summer Intern)
– Training in Control and Instrumentation Department
June 2024
Auraiya, India
- Indian Institute of Technology, Jodhpur**
(Hardware Software Co-Design)
– Image Processing in MATLAB, HDL Code Generation using HDL Coder
March 2024 - May 2024
Jodhpur, India
- Gas Authority of India Limited (GAIL), Pata**
(Trainee)
– Training in Telecommunication Sector (GAILTEL) at GAIL, PATA
Jan 2024 - Feb 2024
Pata, India
- Semi Conductor Laboratory (SCL), Mohali**
(Optical Communication)
– Study of Single Photon Avalanche Diode (SPAD) and its Applications and Testing of various parameters of SPAD and Silicon Photomultiplier (SiPM) using Manual Prober and Parametric Analyzer
June 2023
Mohali, India
- Indian Institute of Technology, BHU**
(Project Intern)
– IoT Device for ECG Monitoring
Aug. 2023 - Sep. 2023
Varanasi, India
- Bariflolabs**
(Internet of Things)
– Arduino , Tinkercad , ROS Distributions on Ubuntu , NodeRed
Jan. 2023 - Feb. 2023
Bhubaneswar, India

PROJECTS

- Smart Traffic Light Controller using VHDL**
Xilinx Vivado
– Traffic light controller used as an intersection between highway and farmway, implemented on Basys 3 Board
Aug. 2023 - Oct. 2023
Github
- Air Quality Monitoring System**
Internet of Things
– Correctly measured **Air Quality** of the surrounding region with the help of MQ-135 sensor and displayed on the Oled Display and sent data on the Thingspeak Server for analytical study.
Feb 2023 - Apr . 2023
Github
- MATLAB to HDL Conversion**
HDL Coder
– An Image Processing Code written in MATLAB is converted to VHDL and Verilog Code using the HDL Coder tool in MATLAB with fixed point conversion.
March 2024 - May . 2024
Github
- Fire Alarm System**
Tinkercad
– Designed a **Fire Alarm System** using Arduino, Temperature Sensor, Gas Sensor, LED and Piezo Buzzer
Feb 2023
Tinkercad

TECHNICAL SKILLS

- Hardware Description Language:** VHDL, Verilog
- Programming:** Python, C, MATLAB
- Tools:** Vivado, CST Software Studio, Cadence, Keil uVision, Proteus 8 Professional, OrCAD PSPICE, MATLAB, Cisco Packet Tracer, Overleaf, Anaconda, Arduino, Microsoft Office

KEY COURSES TAKEN

- VLSI SoC Design using Verilog HDL at Maven Silicon:** Basics of Verilog, Compiler Directive and System Tasks, implementation of combinational and sequential circuits on Quartus Prime and Modelsim
- Udemy- Verilog for a FPGA Engineer with Xilinx Vivado Design Suite :** Fundamentals of Verilog, Modelling Styles, Writing Testbenches, Finite State Machines, Hardware Debugging in Vivado

POSITIONS OF RESPONSIBILITY

- Member:** EPMOC Club(Event Management Club), IIIT Una (Jan 2022 - May 2025)
- Member:** EUNOIA Club (Literary Club), IIIT Una (Jan 2022 - July 2022)

ACHIEVEMENTS

- Gold Medallist:** Academic Records, First and Second Year at IIIT UNA in ECE Branch
- Workshop:** Participated in One Week Short Term Training Program on Recent Advancements in VLSI Design and its Applications at IIIT UNA