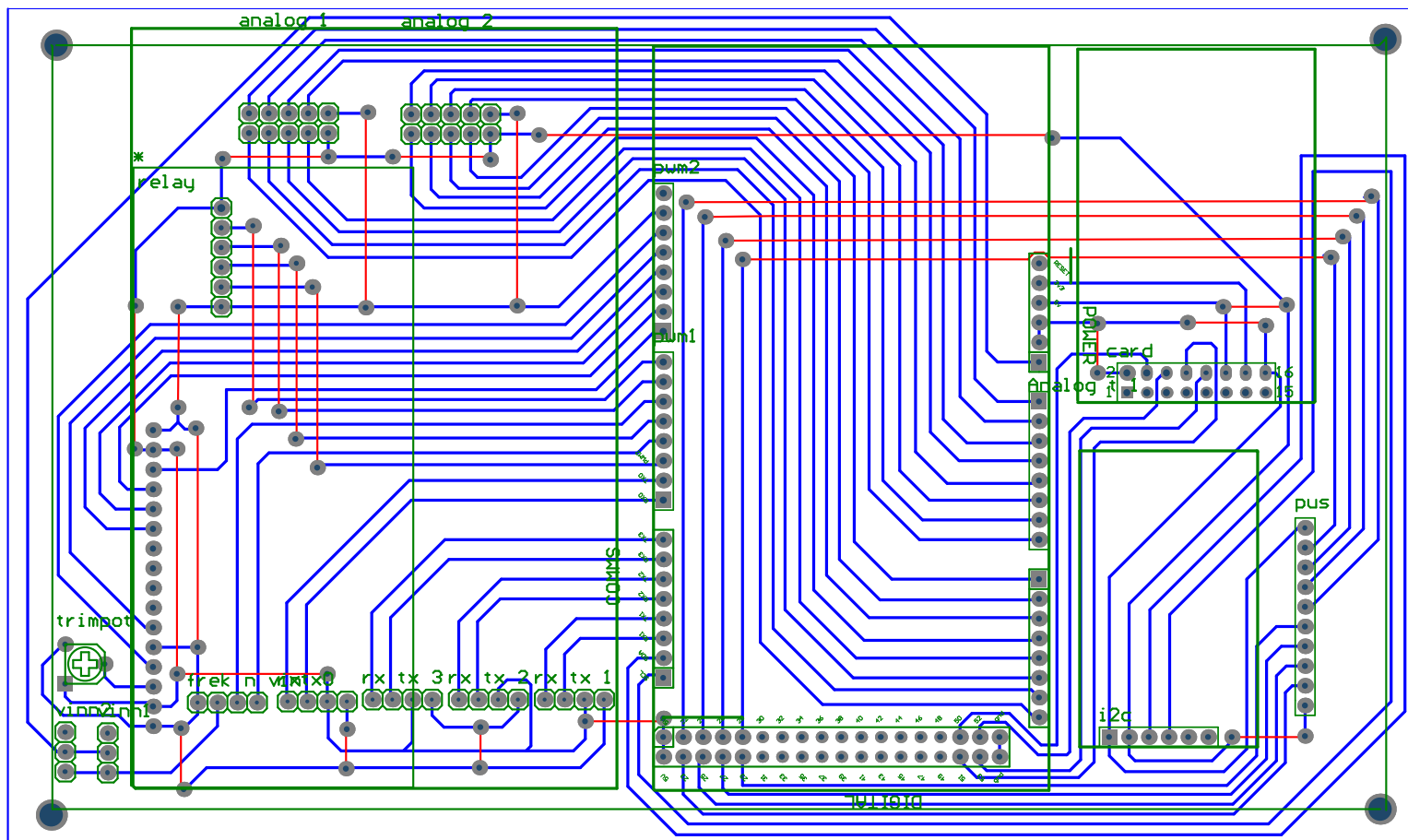


Title			
Size A	Number		Revision
Date:	8/28/2016	Sheet of	
File:	C:\Users\mega module.SchDoc	Drawn By:	



Comment	Description	Designator	Footprint	LibRef	Quantity
LCD			LCD16X2	LCD	2
AD_H		AD_H	Header 8x1 8.5mm	AD_H	2
HEADER 2x5		analog 1, analog 1, analog 2, analog 2	HEADER 2X5	HEADER 2x5	4
Header 8	Header, 8-Pin	Analog t 1, Analog t 1, pwm1, pwm1, pwm2, pwm2	HDR1X8	Header 8	6
	Header, 8-Pin, Dual row	card	HDR2X8	Header 8X2	1
Header 8X2	Header, 8-Pin, Dual row	card	HDR2X8	Header 8X2	1
Communication		COMMS	Header 8x1 8.5mm	Communication	2
IO_36		DIGITAL	HEADER 36X 8MM	IO_36	2
HEADER 1X4L		frek n vin, frek n vin, rxtx0, rxtx0, rx tx 1, rx tx 1, rx tx 2, rx tx 2, rx tx 3, rx tx 3	HEADER 1X4L	HEADER 1X4L	10
Header 6	Header, 6-Pin	i2c	HDR1X6	Header 6	2
POWER		POWER	Header 6x1 8.5mm	POWER	2
Header 10	Header, 10-Pin	pus	HDR1X10	Header 10	2
HEADER 1X6L		relay	HEADER 1X6 L	HEADER 1X6L	2
TRIMPOT		trimpot	TRIMPOT	TRIMPOT	2
header 3x1		vinn1, vinn1, vinn2, vinn	header 3x1	header 3x1	4