

Design Rules Verification Report

Filename : D:\Harit\Harit\#Projects\PIC0_D4_SD_card\PICOD4_T3_LDO\PIC0_D4_SD

Warnings 0
Rule Violations 303

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.102mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.127mm) (Max=12.7mm) (Preferred=0.254mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.127mm)	0
Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	3
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	192
Silk To Solder Mask (Clearance=0.102mm) (IsPad),(All)	72
Silk to Silk (Clearance=0.102mm) (All),(All)	36
Net Antennae (Tolerance=0mm) (All)	0
Matched Lengths(Tolerance=25.4mm) (InDifferentialPairClass("DIFF90"))	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	303

Hole To Hole Clearance (Gap=0.254mm) (All),(All)	
Hole To Hole Clearance Constraint: (0.221mm < 0.254mm) Between Via (12.471mm,7.707mm) from Top Layer to Bottom Layer And Via	
Hole To Hole Clearance Constraint: (0.234mm < 0.254mm) Between Via (4.765mm,12.301mm) from Top Layer to Bottom Layer And Via	
Hole To Hole Clearance Constraint: (0.187mm < 0.254mm) Between Via (4.778mm,12.738mm) from Top Layer to Bottom Layer And Via	

Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C10-1(8.695mm,24.06mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C1-1(15.461mm,28.776mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C11-1(4.915mm,10.033mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.15mm < 0.254mm) Between Pad C11-1(4.915mm,10.033mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.141mm < 0.254mm) Between Pad C11-1(4.915mm,10.033mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.141mm < 0.254mm) Between Pad C11-2(4.077mm,10.033mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C16-1(2.597mm,1.95mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C2-1(18.626mm,28.776mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C3-1(1.212mm,25.082mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C4-1(0.777mm,12.88mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C7-1(13.048mm,29.594mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.232mm < 0.254mm) Between Pad C7-1(13.048mm,29.594mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.244mm < 0.254mm) Between Pad C7-1(13.048mm,29.594mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.251mm < 0.254mm) Between Pad C7-2(13.048mm,28.756mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.232mm < 0.254mm) Between Pad C7-2(13.048mm,28.756mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C8-1(6.872mm,20.356mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad C9-1(8.623mm,28.702mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.144mm < 0.254mm) Between Pad D1-1(1.225mm,3.143mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.238mm < 0.254mm) Between Pad D2-1(2.469mm,19.997mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.238mm < 0.254mm) Between Pad D2-1(2.469mm,19.997mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.238mm < 0.254mm) Between Pad D2-2(2.469mm,21.471mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-1(12.3mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-2(11.2mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-3(10.1mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-4(9mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-5(7.9mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad J2-6(2.3mm,15.501mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-6(6.8mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-7(5.7mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J2-8(4.6mm,14.001mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad Q3-1(18.768mm,2.501mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad Q3-2(17.93mm,2.501mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad Q4-1(17.968mm,5.34mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.209mm < 0.254mm) Between Pad Q4-1(17.968mm,5.34mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad Q4-2(18.768mm,5.34mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.209mm < 0.254mm) Between Pad Q4-2(18.768mm,5.34mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R10-1(0.978mm,5.794mm) on Bottom Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R1-1(14.013mm,29.604mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R11-1(18.857mm,4.454mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.252mm < 0.254mm) Between Pad R11-1(18.857mm,4.454mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.252mm < 0.254mm) Between Pad R11-2(18.857mm,3.616mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R12-1(17.872mm,3.616mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R13-1(8.863mm,13.839mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R14-1(2.654mm,4.293mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.223mm < 0.254mm) Between Pad R14-1(2.654mm,4.293mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.223mm < 0.254mm) Between Pad R14-1(2.654mm,4.293mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.223mm < 0.254mm) Between Pad R14-2(1.816mm,4.293mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.223mm < 0.254mm) Between Pad R14-2(1.816mm,4.293mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.229mm < 0.254mm) Between Pad R14-2(1.816mm,4.293mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R15-1(1.968mm,16.891mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.116mm < 0.254mm) Between Pad R15-1(1.968mm,16.891mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.08mm < 0.254mm) Between Pad R15-1(1.968mm,16.891mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.173mm < 0.254mm) Between Pad R15-1(1.968mm,16.891mm) on Top Layer And Via (2.22mm,16.059mm)
Minimum Solder Mask Sliver Constraint: (0.08mm < 0.254mm) Between Pad R15-2(1.13mm,16.891mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.116mm < 0.254mm) Between Pad R15-2(1.13mm,16.891mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.085mm < 0.254mm) Between Pad R16-1(2.527mm,10.617mm) on Top Layer And Pad

Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U5-2(1.874mm,5.409mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U5-2(1.874mm,5.409mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U5-3(2.524mm,5.409mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U5-3(2.524mm,5.409mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U5-4(3.174mm,5.409mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U5-5(3.174mm,8.359mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U5-5(3.174mm,8.359mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U5-6(2.524mm,8.359mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U5-6(2.524mm,8.359mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.097mm < 0.254mm) Between Pad U5-7(1.874mm,8.359mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U5-7(1.874mm,8.359mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.022mm < 0.254mm) Between Pad U5-8(1.224mm,8.359mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.13mm < 0.254mm) Between Pad U6-1(0.521mm,12.748mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.105mm < 0.254mm) Between Pad U6-10(0.521mm,14.989mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.13mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.13mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.13mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.13mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.105mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.105mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.105mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.105mm < 0.254mm) Between Pad U6-11(1.321mm,13.881mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-1(3.927mm,12.899mm) on Top Layer And Pad
Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U7-2(4.877mm,12.899mm) on Top Layer And Pad

Silk To Solder Mask (Clearance=0.102mm) (IsPad),(All)

Silk To Solder Mask Clearance Constraint: (Collision < 0.102mm) Between Arc (13.5mm,6.25mm) on Top Overlay And Pad D3-1(13.589mm,6.485mm)
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 10-1(8.695mm,24.06mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 10-2(8.695mm,23.222mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 10-2(8.695mm,23.222mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 1-1(15.461mm,28.776mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 11-1(4.915mm,10.033mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 11-2(4.077mm,10.033mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 11-2(4.077mm,10.033mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 1-2(15.461mm,29.614mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 1-2(15.461mm,29.614mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.101mm < 0.102mm) Between Pad C 13-1(4.007mm,11.024mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.101mm < 0.102mm) Between Pad C 13-2(5.341mm,11.024mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.094mm < 0.102mm) Between Pad C 13-2(5.341mm,11.024mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.101mm < 0.102mm) Between Pad C 14-1(4.108mm,18.016mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.101mm < 0.102mm) Between Pad C 14-2(5.442mm,18.016mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.094mm < 0.102mm) Between Pad C 14-2(5.442mm,18.016mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 16-1(2.597mm,1.95mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 16-2(2.597mm,2.788mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 16-2(2.597mm,2.788mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 2-1(18.626mm,28.776mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 2-2(18.626mm,29.614mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 2-2(18.626mm,29.614mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 3-1(1.212mm,25.082mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 3-2(1.212mm,24.244mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 3-2(1.212mm,24.244mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 4-1(0.777mm,12.88mm) on Bottom Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 4-2(1.615mm,12.88mm) on Bottom Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 4-2(1.615mm,12.88mm) on Bottom Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 7-1(13.048mm,29.594mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 7-2(13.048mm,28.756mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 7-2(13.048mm,28.756mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 8-1(6.872mm,20.356mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 8-2(6.872mm,21.194mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 8-2(6.872mm,21.194mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 9-1(8.623mm,28.702mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.098mm < 0.102mm) Between Pad C 9-2(7.785mm,28.702mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad C 9-2(7.785mm,28.702mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.102mm) Between Pad D1-1(1.225mm,3.143mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.026mm < 0.102mm) Between Pad D1-2(1.225mm,0.819mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.001mm < 0.102mm) Between Pad D2-1(2.469mm,19.997mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.014mm < 0.102mm) Between Pad D2-1(2.469mm,19.997mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.011mm < 0.102mm) Between Pad D2-1(2.469mm,19.997mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.001mm < 0.102mm) Between Pad D2-2(2.469mm,21.471mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (Collision < 0.102mm) Between Pad D2-2(2.469mm,21.471mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.011mm < 0.102mm) Between Pad D2-2(2.469mm,21.471mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.032mm < 0.102mm) Between Pad D4-1(9.144mm,6.485mm) on Top Layer And Text "K"
Silk To Solder Mask Clearance Constraint: (0.05mm < 0.102mm) Between Pad D5-2(10.846mm,7.485mm) on Top Layer And Text "D5"
Silk To Solder Mask Clearance Constraint: (0.097mm < 0.102mm) Between Pad H1-6(18.996mm,17.688mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.089mm < 0.102mm) Between Pad J1-1(4.991mm,7.054mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.095mm < 0.102mm) Between Pad J2-6(16.1mm,15.048mm) on Bottom Layer And Text "RX"
Silk To Solder Mask Clearance Constraint: (0.051mm < 0.102mm) Between Pad J3-6(15.7mm,2.85mm) on Multi-Layer And Text "R12"
Silk To Solder Mask Clearance Constraint: (0.063mm < 0.102mm) Between Pad Q3-1(18.768mm,2.501mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.101mm < 0.102mm) Between Pad Q3-2(17.93mm,2.501mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.071mm < 0.102mm) Between Pad Q3-3(18.368mm,1.701mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.071mm < 0.102mm) Between Pad Q3-3(18.368mm,1.701mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.063mm < 0.102mm) Between Pad Q4-1(17.968mm,5.34mm) on Top Layer And Track

Silk To Solder Mask (Clearance=0.102mm) (IsPad),(All)

Silk To Solder Mask Clearance Constraint: (0.063mm < 0.102mm) Between Pad Q4-2(18.768mm,5.34mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.071mm < 0.102mm) Between Pad Q4-3(18.368mm,6.14mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.071mm < 0.102mm) Between Pad Q4-3(18.368mm,6.14mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.09mm < 0.102mm) Between Pad R15-1(1.968mm,16.891mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.09mm < 0.102mm) Between Pad R15-2(1.13mm,16.891mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.09mm < 0.102mm) Between Pad R18-1(1.13mm,16.078mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.09mm < 0.102mm) Between Pad R18-2(1.968mm,16.078mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.102mm) Between Pad SW1-2(13.157mm,17.018mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.095mm < 0.102mm) Between Pad U5-1(1.224mm,5.409mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.095mm < 0.102mm) Between Pad U5-4(3.174mm,5.409mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.095mm < 0.102mm) Between Pad U5-5(3.174mm,8.359mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.095mm < 0.102mm) Between Pad U5-8(1.224mm,8.359mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.102mm) Between Pad U6-1(0.521mm,12.748mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.102mm) Between Pad U6-10(0.521mm,14.989mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.102mm) Between Pad U6-5(2.121mm,12.748mm) on Top Layer And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.102mm) Between Pad U6-6(2.121mm,14.989mm) on Top Layer And Track

Silk to Silk (Clearance=0.102mm) (All),(All)

Silk To Silk Clearance Constraint: (Collision < 0.102mm) Between Arc (0.584mm,12.095mm) on Top Overlay And Text "R17" (0.998mm,10.709mm) on
Silk To Silk Clearance Constraint: (0.047mm < 0.102mm) Between Text "." (1.234mm,4.572mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.054mm < 0.102mm) Between Text "." (1.234mm,4.572mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.102mm) Between Text "+" (4.545mm,7.92mm) on Top Overlay And Text "U5" (4.581mm,8.278mm) on
Silk To Silk Clearance Constraint: (0.079mm < 0.102mm) Between Text "+" (4.545mm,7.92mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.102mm) Between Text "3V3" (15.96mm,13.576mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.076mm < 0.102mm) Between Text "C11" (5.583mm,9.844mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.077mm < 0.102mm) Between Text "C11" (5.583mm,9.844mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.1mm < 0.102mm) Between Text "C2" (18.709mm,30.201mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.05mm < 0.102mm) Between Text "C3" (1.473mm,25.629mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.09mm < 0.102mm) Between Text "C8" (7.955mm,20.731mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.087mm < 0.102mm) Between Text "D1" (0.897mm,1.629mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.023mm < 0.102mm) Between Text "DTS" (15.926mm,7.722mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.065mm < 0.102mm) Between Text "K" (0.356mm,3.378mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.055mm < 0.102mm) Between Text "K" (11.252mm,5.893mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.094mm < 0.102mm) Between Text "K" (14.148mm,5.969mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.052mm < 0.102mm) Between Text "K" (8.585mm,5.817mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.052mm < 0.102mm) Between Text "K" (8.585mm,5.817mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.055mm < 0.102mm) Between Text "R1" (13.737mm,30.156mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.056mm < 0.102mm) Between Text "R1" (13.737mm,30.156mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.101mm < 0.102mm) Between Text "R11" (17.324mm,4.405mm) on Top Overlay And Text "R12"
Silk To Silk Clearance Constraint: (0.051mm < 0.102mm) Between Text "R11" (17.324mm,4.405mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.057mm < 0.102mm) Between Text "R11" (17.324mm,4.405mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.048mm < 0.102mm) Between Text "R13" (8.679mm,14.383mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.044mm < 0.102mm) Between Text "R14" (3.8mm,4.79mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.044mm < 0.102mm) Between Text "R14" (3.8mm,4.79mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.101mm < 0.102mm) Between Text "R19" (6.998mm,12.592mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.049mm < 0.102mm) Between Text "R2" (5.315mm,20.664mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.087mm < 0.102mm) Between Text "R2" (5.315mm,20.664mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.07mm < 0.102mm) Between Text "R3" (4.966mm,11.773mm) on Bottom Overlay And Track
Silk To Silk Clearance Constraint: (0.078mm < 0.102mm) Between Text "R4" (16.896mm,10.698mm) on Bottom Overlay And Track
Silk To Silk Clearance Constraint: (0.072mm < 0.102mm) Between Text "R7" (10.372mm,10.345mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.048mm < 0.102mm) Between Text "R9" (4.598mm,19.404mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.048mm < 0.102mm) Between Text "R9" (4.598mm,19.404mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.074mm < 0.102mm) Between Text "RTS" (15.875mm,9.804mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.074mm < 0.102mm) Between Text "U1" (18.758mm,26.443mm) on Top Overlay And Track