

2 pc:

Personal pc:

Dblab:

L1d cache: 32K

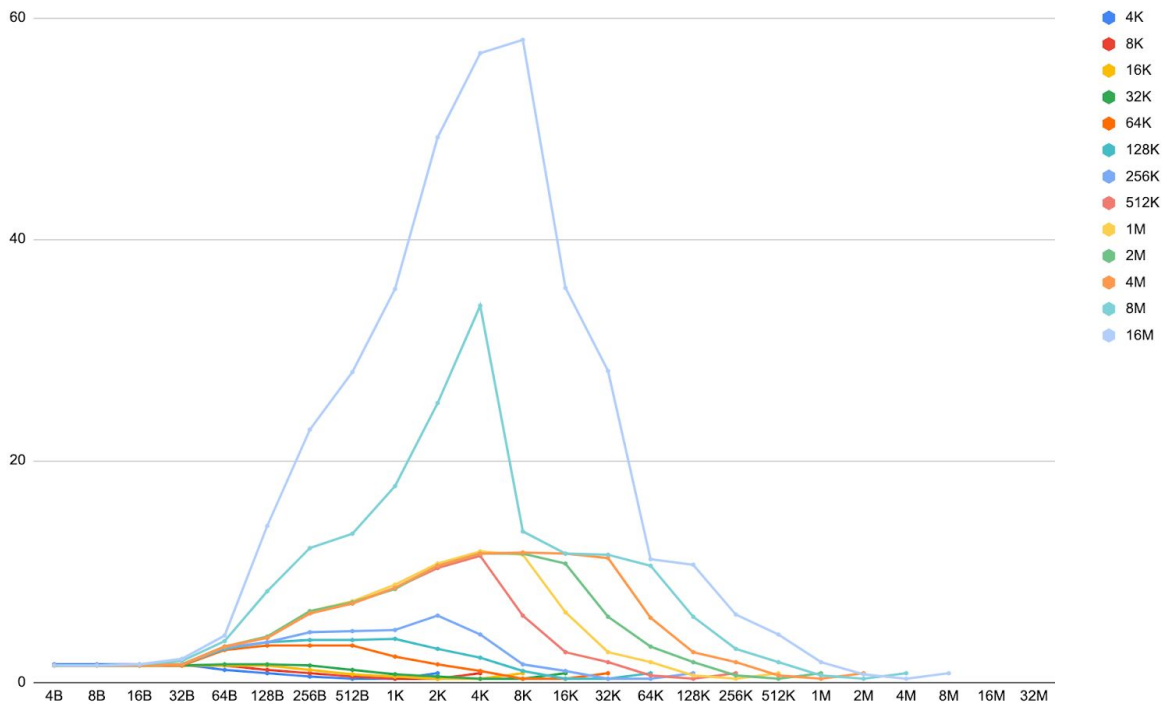
L2 cache: 256K

L3 cache: 8192K

cacheline : 64B

Page size: 4K

Tlb size: 64 entries



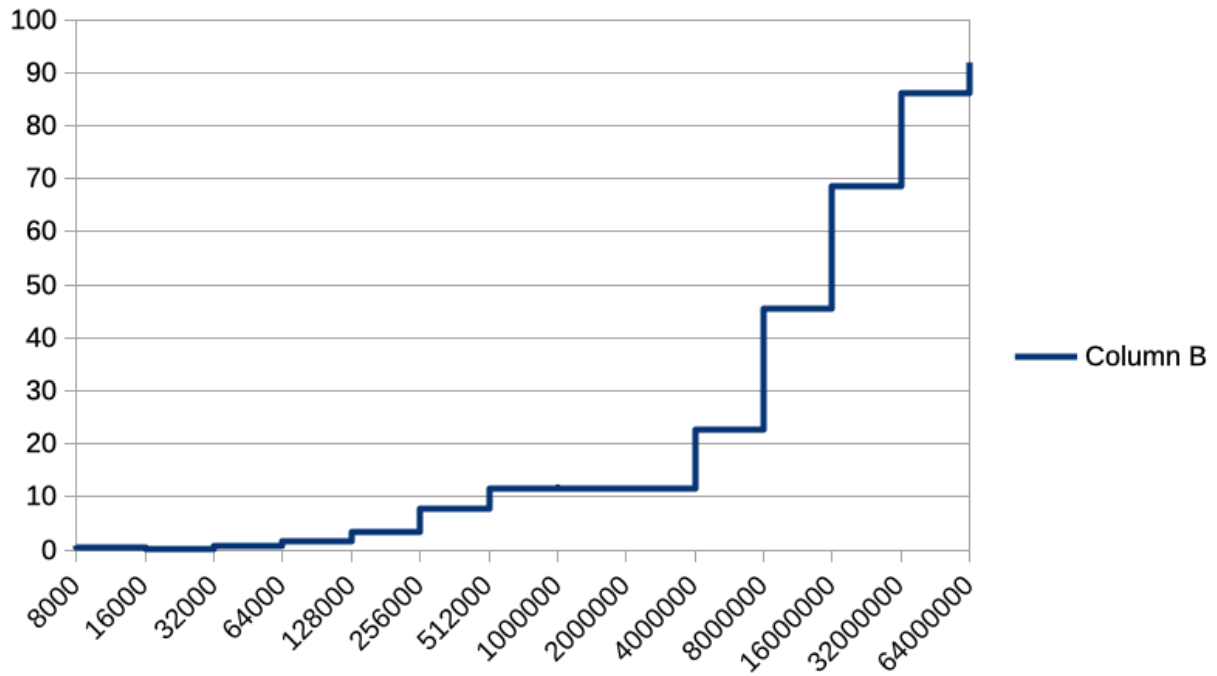
We can see that results diverge from 64B stride hence cache line is 64B

We see first major jump in time from 32K entries, then from 256K entries and then 8M

Hence cache sizes are 32K, 256K and then 8M

We see that till 4K stride we get huge increases in time for huge datasets, hence page size is 4K

For tlb:



We see that for 4k strides (page size), we get first major step at 256K.  
Hence tlb has 64 entries =  $256k/4k$ .