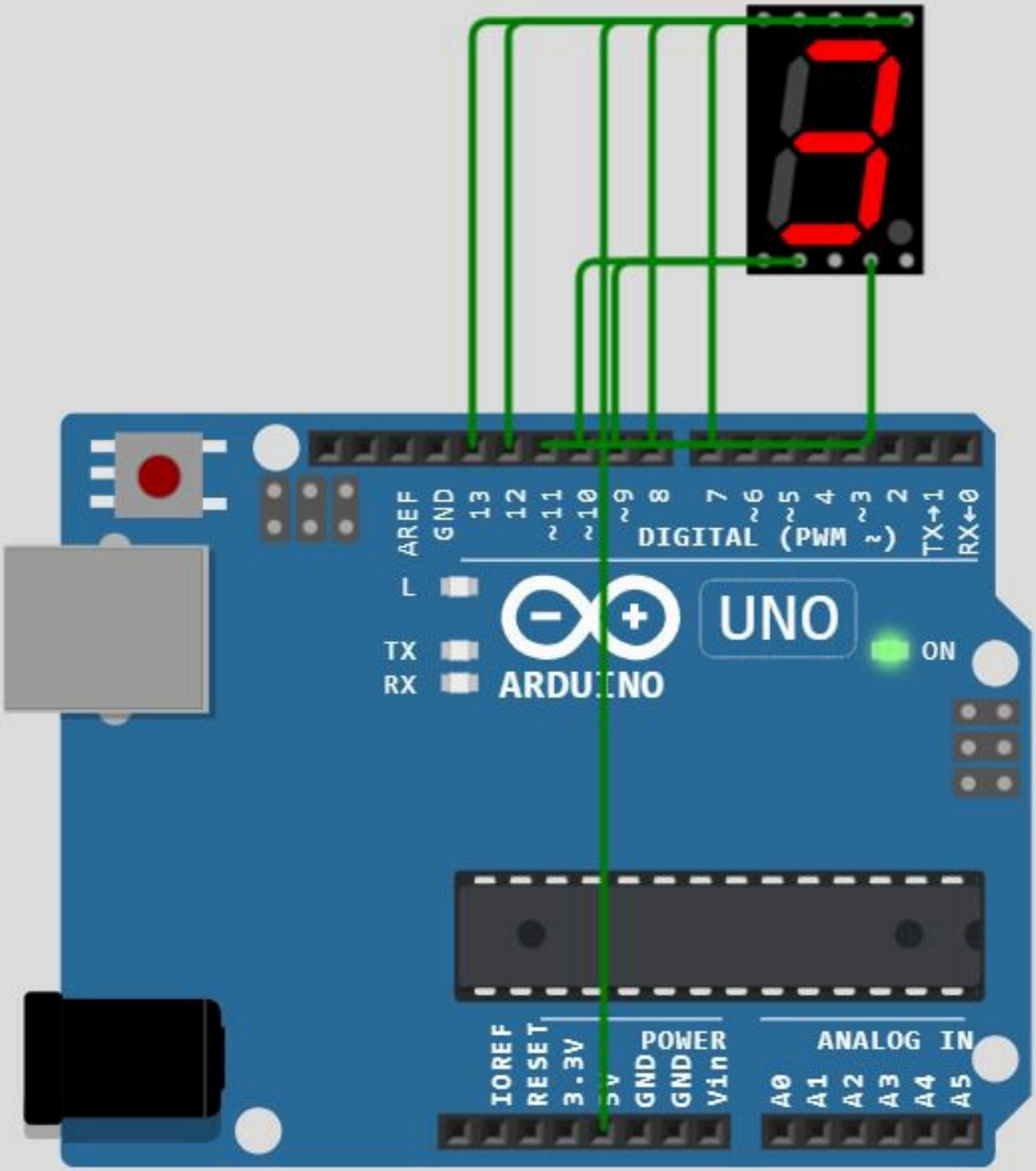
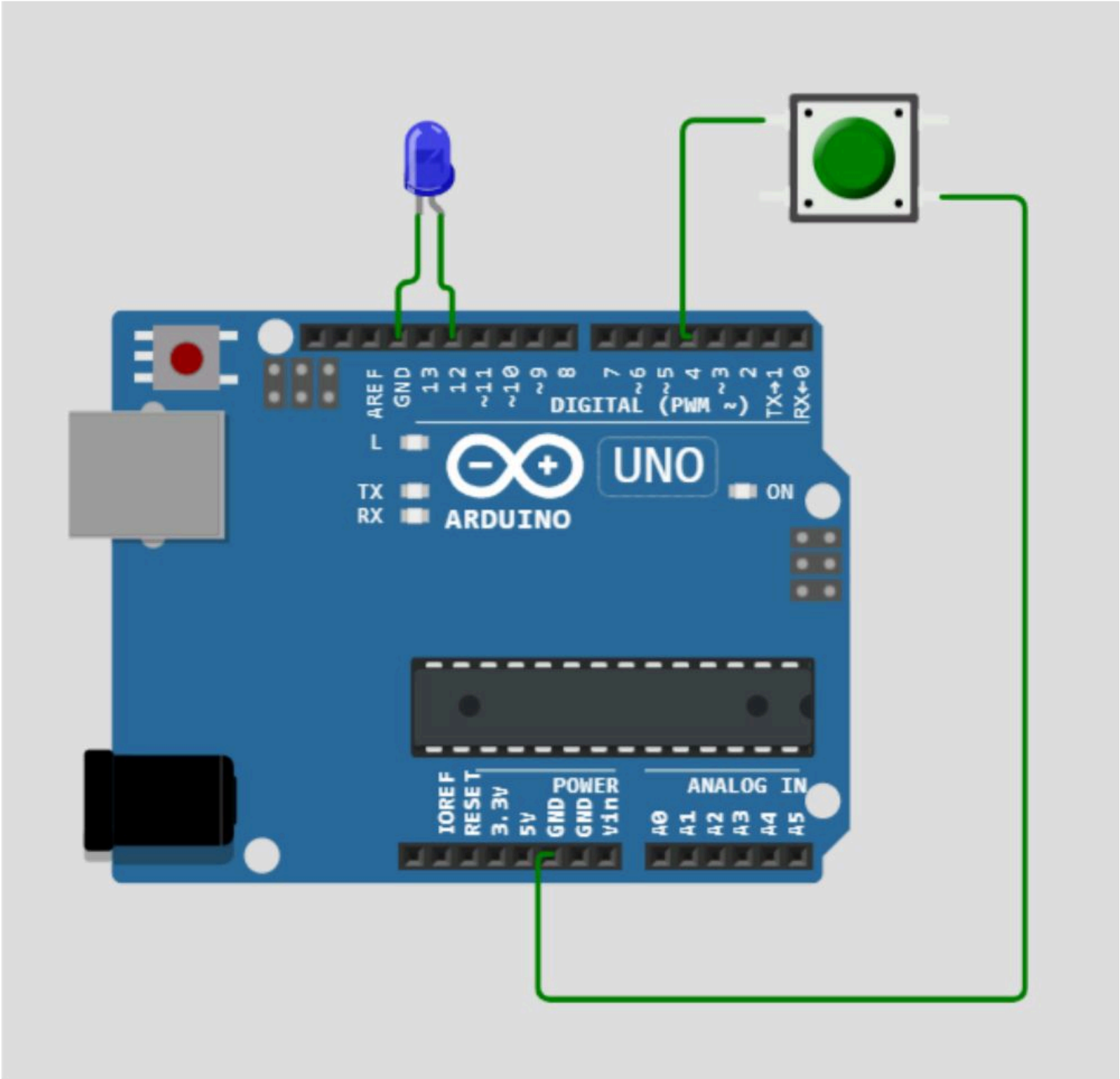

AREF
GND
13
12
~11
~10
~9
8
7
~6
~5
4
~3
2
TX→1
RX←0
DIGITAL (PWM ~)
L
TX
RX
ARDUINO
UNO
ON
IOREF
RESET
3.3V
5V
GND
GND
Vin
POWER
A0
A1
A2
A3
A4
A5
ANALOG IN


AREF
GND
13
12
~11
~10
~9
8
7
~6
~5
4
~3
2
TX→1
RX←0
DIGITAL (PWM ~)
L
TX
RX
ARDUINO
UNO
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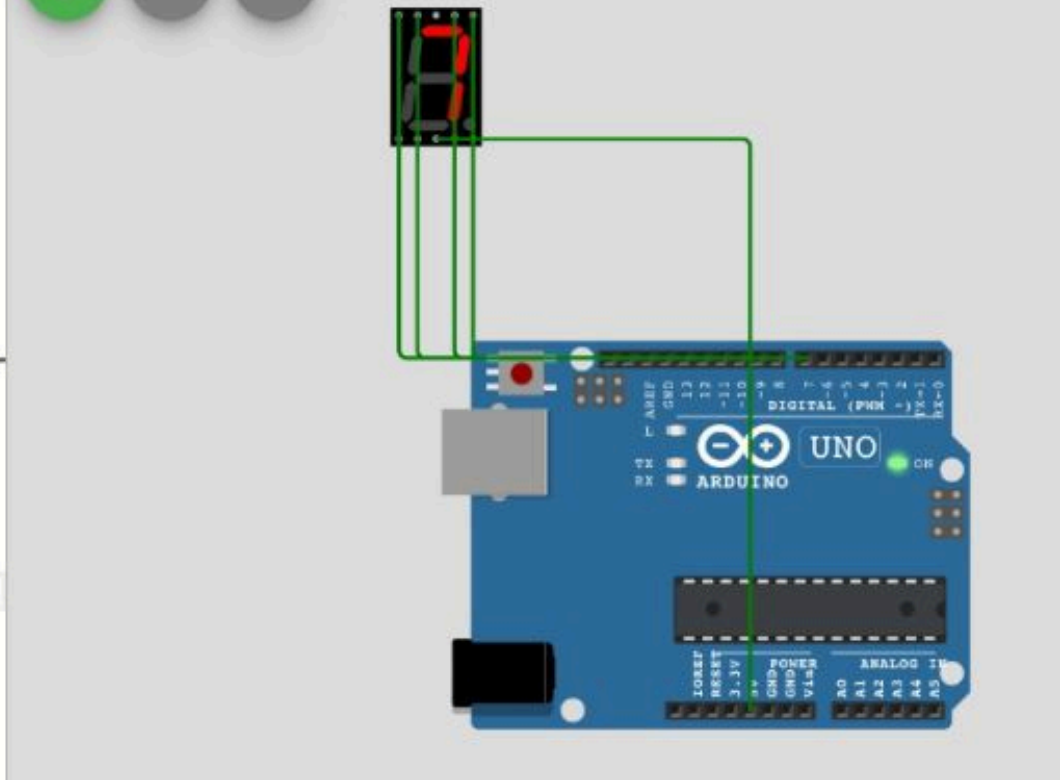
RESET
3.3v
5v
GND
GND
Vin
POWER
ANALOG IN
A0
A1
A2
A3
A4
A5

sketch.ino ● diagram.json ● Library Manager

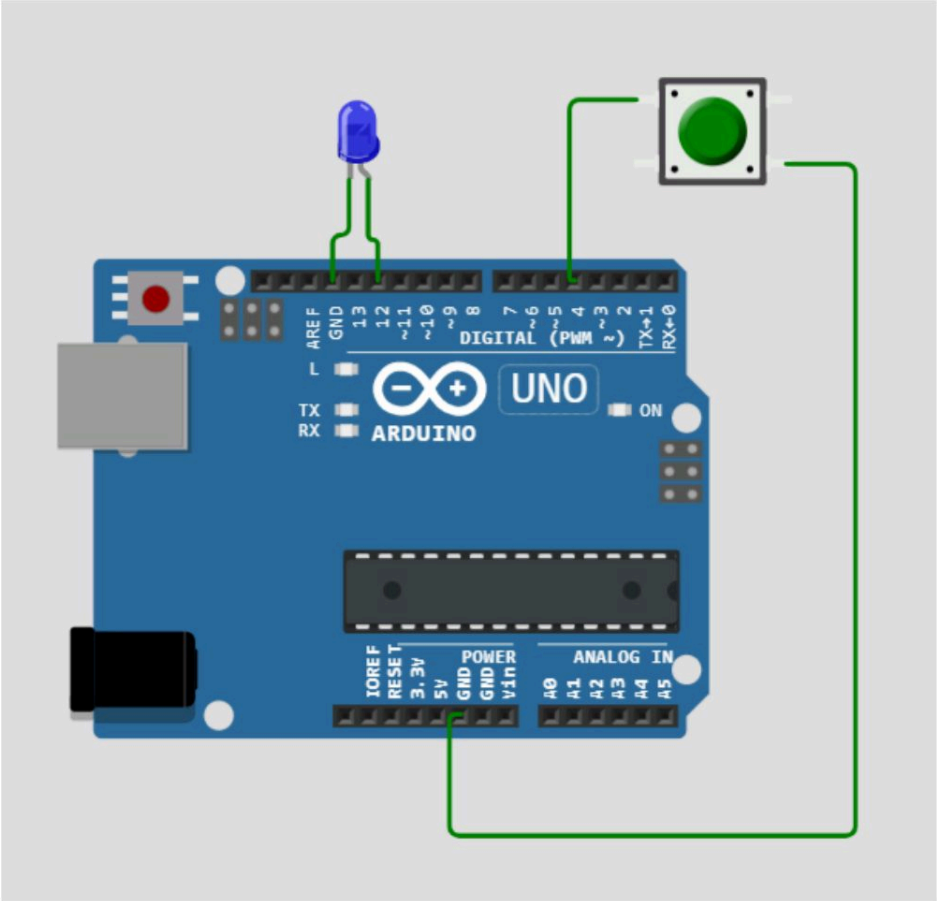
```
8   void setup() {
9   pinMode(a, OUTPUT);
10  pinMode(b, OUTPUT);
11  pinMode(c, OUTPUT);
12  pinMode(d, OUTPUT);
13  pinMode(e, OUTPUT);
14  pinMode(f, OUTPUT);
15  pinMode(g, OUTPUT);
16  }
17
18  void loop() {
19   digitalWrite(a,0);
20  digitalWrite(b,0);
21  digitalWrite(c,0);
22  digitalWrite(d,1);
23  digitalWrite(e,1);
24  digitalWrite(f,1);
25  digitalWrite(g,1);
26  delay(500);
27  }
28
```

Simulation

00:42.982



Interfacing Diagram:-



Results:- The embedded program is written and simulated succ