

## 7. CIRCUIT DESIGNING WORKSHOP

IEEE Student Branch College of Engineering Karunagappally in association with PELS/ NPSS organized a Circuit Designing Workshop on 10th April 2019. There was an active participation of 25 members. The sessions was handled by Mr. Hari Prasad, student Representative Travancore hub IA/IE/PELS Jt. Chapter Kerala attention and dedication shown by each and everyone attended the workshop turned it into a huge success.

