

ACADEMICS

COURSE	INSTITUTION	AGGREGATE	YEAR
M.Tech (VLSI and Embedded Systems)	Govt. Model Engineering College,	8.25 CGPA	2020-2022
	Thrikkakara (KTU)	[Ongoing 4th Sem]	
B.Tech (Electronics and Communication Engineering)	Govt. College of Engineering Cherthala (KTU)	6.74 CGPA	2015-2019
Class XII (Science)	St. Antony's Higher Secondary School, Kacheripady, Kerala (State Board)	92%	2015
Class X	St. Antony's Higher Secondary School, Kacheripady, Kerala (State Board)	98%	2013

AREAS OF EXPERTISE

- Technical Skills: Xilinx, Arduino, Raspberry pi
- Operating Systems known: Windows, Linux
- Areas of Interest: Embedded System design, VLSI Design

PROJECTS

Project: Arduino Based Automatic Bell System - Design Project (BTech)

Team Size:4

Duration: 4 Months

- The objective of the project is to design an automatic bell system using Arduino which can be configured for every class of the school
- 0 Role: Leader
- Technology: Arduino

Project: Cloud Based Smart Parking System

Team Size:4

Duration:1Week

Duration: 10 Days

Duration: 6 Months

- The objective of the project is to design a cloud based smart parking system where the user can automatically find an available parking lot easily using mobile app.
- Role: Hardware
- Technology: Arduino, Firebase, Android Studio Project
- Project: Verilog HDL Design and Simulation of Single Precision Floating Point ALU (MTech)

Duration:4 Months

- The objective of the project is to design and simulation of single precision floating point ALU which is a part of math coprocessor.
- Technology: Xilinx ISE 14.6, Icarus Verilog
- Project: CNN based Object Detection for Advanced Driving Assistance Systems (MTech)

Duration:6 Months

- The objective of the project is to design and implement YOLO v3 neural network trained to detect three classes of objects (traffic participants / road signalization): Cars ,Pedestrians & Traffic lights.
- **Technology**: Spyder IDE Anaconda, Raspberry Pi

INTERNSHIPS AND WORKSHOPS

- Internship in Dooradarshan High Power Transmitter, Kochi

Internship programme on IOT at Neo Green Labs, Kaloor

POSITIONS OF RESPONSIBILITY

- Event head of a technical event, held in Zealous2k19, annual techno-cultural festival of College of Engineering Cherthala
- Leader of 4 member team in academic design project
- Led a team of 12 members in a school level English Magazine Contest
- Coordinated a college level IEEE quiz programme
- Vice -Captain of Sub-Junior Kho-Kho Ernakulam district team

INTERESTS AND ACTIVITIES

Hobbies: Watching movies, Reading, Drawing

REFERENCES

Prof. Dr. Jacob Thomas V, Principal, Govt. Model Engineering College, Thrikkakkara. Email id: principal@mec.ac.in