

Log Share

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[2024-09-07 10:18:54 UTC] iverilog '-wall' '-g2012' design.sv testbench.sv && unbuffer vvp a.out
VCD info: dumpfile fifo_test.vcd opened for output.
Time: 0 | wr: 0 | rd: 0 | FIFO Memory: xx xx xx xx xx xx xx xx | Full: x | Empty: x | Valid: x | Overflow: x | Underflow: x
Time: 5 | wr: 0 | rd: 0 | FIFO Memory: xx xx xx xx xx xx xx xx | Full: 0 | Empty: x | Valid: x | Overflow: 0 | Underflow: x
Time: 7 | wr: 0 | rd: 0 | FIFO Memory: xx xx xx xx xx xx xx xx | Full: 0 | Empty: 1 | Valid: 0 | Overflow: 0 | Underflow: 0
Test Case 1: Fill FIFO
Time: 30 | wr: 1 | rd: 0 | FIFO Memory: xx xx xx xx xx xx xx xx | Full: 0 | Empty: 1 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 35 | wr: 1 | rd: 0 | FIFO Memory: 24 xx xx xx xx xx xx xx | Full: 0 | Empty: 1 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 45 | wr: 1 | rd: 0 | FIFO Memory: 24 81 xx xx xx xx xx xx | Full: 0 | Empty: 1 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 55 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 xx xx xx xx xx | Full: 0 | Empty: 1 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 63 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 xx xx xx xx xx | Full: 0 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 65 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 63 xx xx xx xx | Full: 0 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 75 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 63 0d xx xx xx | Full: 0 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 85 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d xx xx | Full: 0 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 95 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 xx | Full: 0 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 105 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Test Case 2: Write when FIFO is full
Time: 110 | wr: 0 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 120 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 125 | wr: 1 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 0 | Overflow: 1 | Underflow: 0
Test Case 3: Empty FIFO
Time: 130 | wr: 0 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 0 | Overflow: 1 | Underflow: 0
Time: 135 | wr: 0 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 140 | wr: 0 | rd: 1 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 0 | Overflow: 0 | Underflow: 0
Time: 147 | wr: 0 | rd: 1 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 1 | Empty: 0 | Valid: 1 | Overflow: 0 | Underflow: 0
Time: 175 | wr: 0 | rd: 1 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 0 | Empty: 0 | Valid: 1 | Overflow: 0 | Underflow: 0
Test Case 4: Read when FIFO is empty
Time: 220 | wr: 0 | rd: 0 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 0 | Empty: 0 | Valid: 1 | Overflow: 0 | Underflow: 0
Time: 230 | wr: 0 | rd: 1 | FIFO Memory: 24 81 09 63 0d 8d 65 12 | Full: 0 | Empty: 0 | Valid: 1 | Overflow: 0 | Underflow: 0
Test Case 5: Alternate write and read operations
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EPWave

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Time: 1545 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 62 8f 9f | Full: 0 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1555 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 8f 9f | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1560 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 8f 9f | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1590 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 8f 9f | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1603 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 8f 9f | Full: 1 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1635 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 8f 9f | Full: 0 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1640 | wr: 1 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 8f 9f | Full: 0 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1645 | wr: 1 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 9f | Full: 0 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1655 | wr: 1 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1660 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1665 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1670 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1673 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1680 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1695 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1705 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 1 | Underflow: 0
Time: 1720 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 1 | Underflow: 0
Time: 1725 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1730 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1740 | wr: 1 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1743 | wr: 1 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1745 | wr: 1 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 1 | Overflow: 1 | Underflow: 0
Time: 1750 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 1 | Overflow: 1 | Underflow: 0
Time: 1755 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 1 | Overflow: 1 | Underflow: 0
Time: 1757 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 1 | Underflow: 0
Time: 1760 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 1 | Underflow: 0
Time: 1765 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1770 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1771 | wr: 0 | rd: 1 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1790 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 1 | Overflow: 0 | Underflow: 0
Time: 1799 | wr: 0 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1800 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 0 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1835 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 0 | Underflow: 0
Time: 1845 | wr: 1 | rd: 0 | FIFO Memory: 5f cb 79 79 c3 c3 d9 d9 | Full: 1 | Empty: 0 | valid: 0 | Overflow: 1 | Underflow: 0

testbench.sv:133: \$finish called at 1930 (1s)

Finding VCD file...

./fifo_test.vcd

[2024-09-07 10:18:54 UTC] Opening EPWave...
Done

