



Vidya Vikas Education Trust's

Universal College of Engineering

Gujarati Linguistic Minority Institution

Date: 31/05/2021	Internal Assessment Test: 2	Branch: Computer
Semester: 5	Subject: Microprocessor	Marks: 20

Q1 Attempt any 5 from 7

(10 Marks)

- Write a short note on switch port and variable port addressing
- Explain in detail, register file of SPARC processor
- Write a short note on 8284 clock generator
- Draw the timing diagram and explain for memory read in minimum mode
- Explain cache/MMU organization of super SPARC processor
- Explain assembler directives of 8086
- Write a detailed note on Branch instruction of the 8086

Q2 Attempt any 1 from 3

(5 Marks)

- Draw and explain various instruction formats of SPARC processor
- What is maximum mode of 8086 ? How it differs from minimum mode ?
- Fixed and Variable port addressing formats

Q3 Attempt any 1 from 3

(5 Marks)

- Write a short note on mixed mode programming
- What are the advantages of segmentation with reference to 8086 microprocessor, also explain the default segment assignments
- What are the characteristics of a microprocessor
