Date: 29/05/2021	Internal Assesment Test: 2	Branch: Computer
Semester: 5	Subject: Microprocessor	Marks: 20

Q1 Attempt any 5 from 6   **(10 Marks)**

  a. Explain the operation of IC 8259 with the block diagram. Explain all the signals in detail
  b. Draw and explain various instruction formats of SPARC processor
  c. Explain assembler directives of 8086
  d. What are the basic functions of microprocessor
  e. Write a detailed note on Branch instruction of the 8086
  f. Explain the architecture of super SPARC microprocessor with the help of neat block diagram

Q2 Attempt any 1 from 2   **(5 Marks)**

  a. Difference between Software and Hardware interrupt
  b. Fixed and Variable port addressing formats

Q3 Attempt any 1 from 2   **(5 Marks)**

  a. Explain the necessity of a bus controller in 8086 maximum mode operation also explain the 8288 bus controller in detail
  b. Write a short note on mixed mode programming

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