1 Attempt any 5 from 6

1 Write a detailed note on Branch instruction of the 8086

2 What is an ISR ? How does 8086 acknowledge an interrupt ? Draw flowchart from interrupt processing sequence

3 Explain the data types supported by SPARC architecture

4 Write a short note on Addressing modes in the 8086

5 Draw and explain various instruction formats of SPARC processor

6 Draw the circuit diagram for generation of reset signal and explain its working

2 Attempt any 1 from 2

1 Explain the necessity of a bus controller in 8086 maximum mode operation also explain the 8288 bus controller in detail

2 Explain assembler directives of 8086

3 Attempt any 1 from 2

1 Write a short note on mixed mode programming

2 Explain in detail, register file of SPARC processor