

https://68kmla.org/forums/uploads/monthly_2018_03/PC-MAC_PS_pinouts.jpg.4f304d7d5458a80f5c4bcaf1c9928bc0.jpg

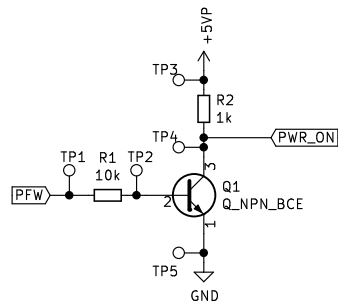
The main schematic shows several components:

- J1 ATX-20:** A central connector with pins labeled PS_ON# (pin 14), PWR_OK (pins 8, 10, 11, 12), +12V (pin 4), +5V (pin 5), +5VSB (pin 9), +3.3V (pin 1), -5V/NC (pin 18), and -12V (pin 12).
- J2 39-28-1223:** A multi-pin connector with multiple rows of pins.
- J3, J4, J5, J6:** Four 02x06 pin headers connected counter-clockwise from position 1.
- D1-D6 LED_Small:** Six small LEDs connected through resistors R3-R8 to ground.
- R1-R8:** Resistors ranging from 10kΩ to 1MΩ.
- TP1-TP5:** Test points located around the transistor Q1.

PWR_ON: A control signal that is pulled up to +5 V by the PSU and must be driven low to turn on the PSU.
PWR_OK: A control signal that is low when other outputs have not yet reached, or are about to leave, correct voltages.
+5VSB: supplies power even when the rest of the supply wire lines are off. This can be used to power the circuity that controls the power-on signal.

Circuit to control the Power_On input to the ATX Power supply.
This works opposite from the Mac's PFW pin
http://bylenga.ddns.net/index.php?page=Centris_ATX.php

		Title:
Sheet: / File: asdf.sch	Date:	Kicad E.D.A. kicad (5.1.4)-1
Size: A4	Rev:	Id: 1/1



Circuit to control the Power_On input to the ATX Power supply.
This works opposite from the Mac's PFW pin
http://bylenga.ddns.net/index.php?page=Centris_ATX.php



Sheet: /
File: asdf.sch

Title:

Size: A4

Date:

Size: V1	Date:
KiCad E.D.A.	kicad (5.1.4)-1

Rev:

Id: 1/1