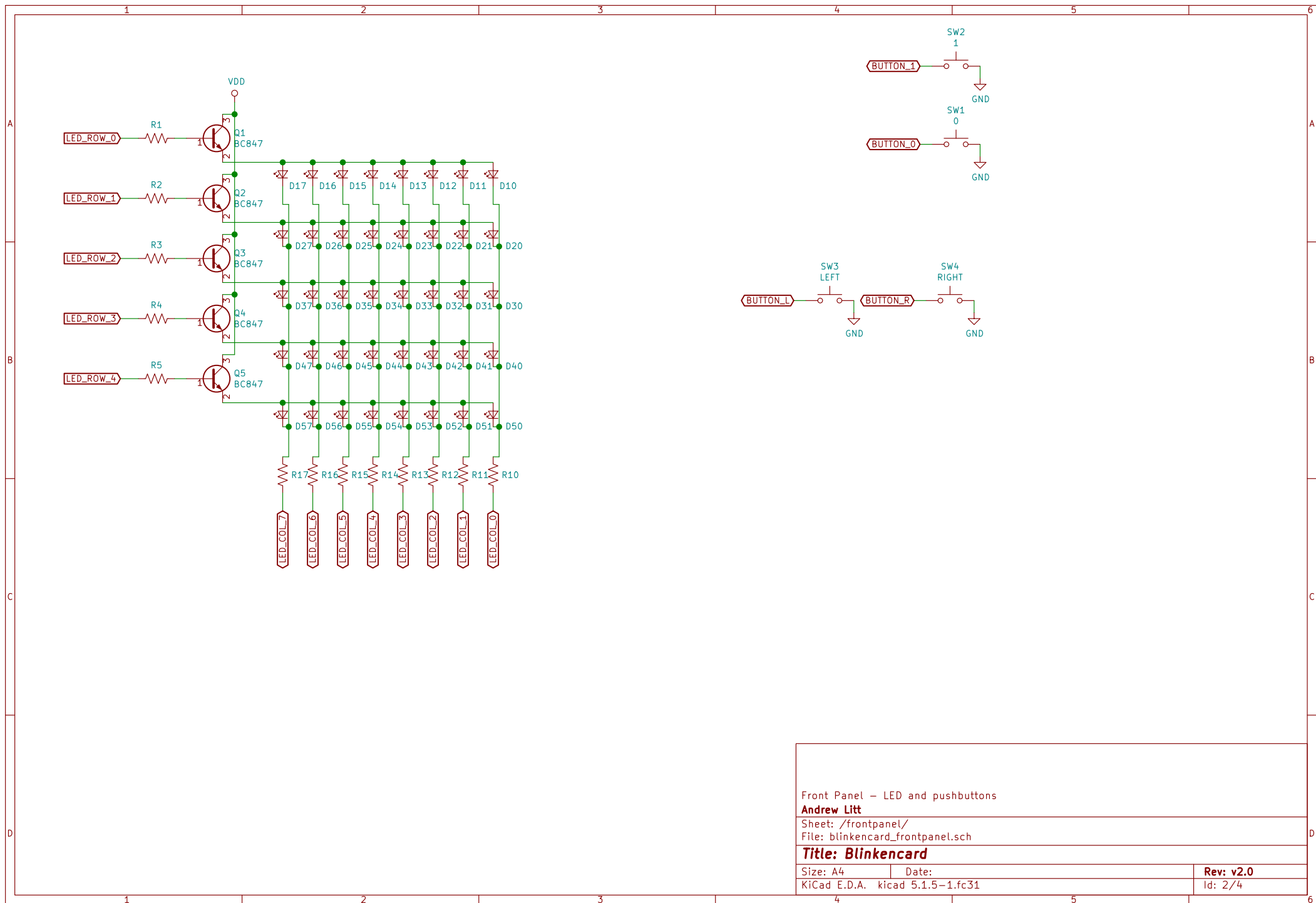
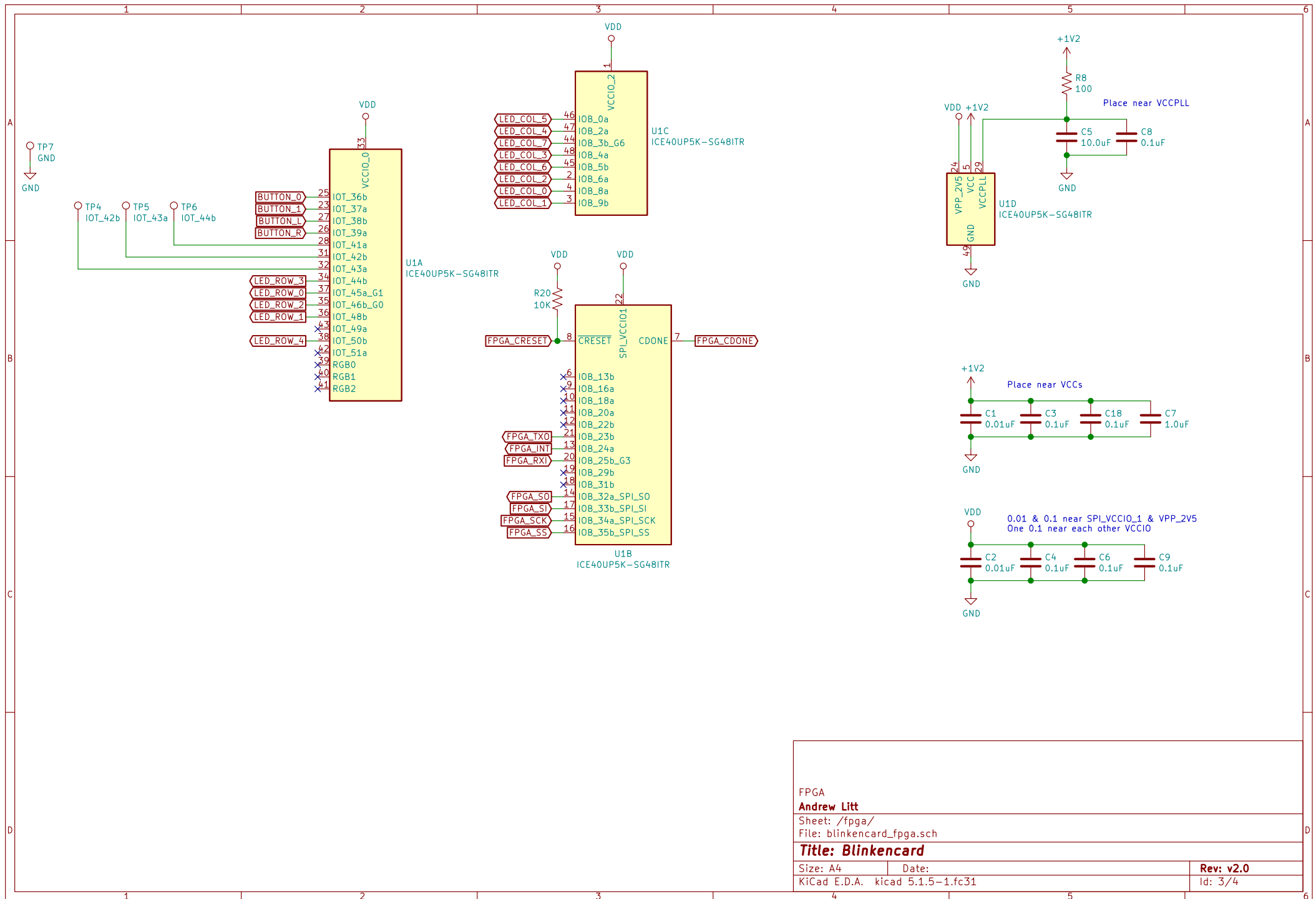


	1	2	3	4	5	6
A	<div>Sheet: fpga</div> <div>File: blinkencard_fpga.sch</div>					<div>Sheet: frontpanel</div> <div>File: blinkencard_frontpanel.sch</div>
B						
C	<div>Sheet: wifi_and_power</div> <div>File: blinkencard_wifi_pwr.sch</div>					
D						<div>Top Level</div> <div>Andrew Litt</div> <div>Sheet: /</div> <div>File: blinkencard.sch</div> <div>Title: Blinkencard</div> <div>Size: A4Date:KiCad E.D.A. kicad 5.1.5-1.fc31</div> <div>Rev: v2.0Id: 1/4</div>
	1	2	3	4	5	6





FPGA

Andrew Litt

Sheet: /fpga/

File: blinkencard_fpga.sch

Title: Blinkencard

Size: A4

Date:

KiCad E.D.A. kicad 5.1.5-1.fc31

Rev: v2.0

Id: 3/4

