

ALEENA SAEED

BSCS-4

COMPUTER ARCHITECTURE _ LAB 11

56270

TASK 1:

Data	Dec			112t1 X	112t2 X	11
Registers						
Name	Width		Data			
AC	16	1		1	INP	
AR	12	1		2	ISZ	009
DR	16	3		3	OUT	
E	1	0		4	HLT	
I	1	0		5		
IR	16	-8191				
PC	12	4				
S	1	-1				

EXECUTING...
Enter Inputs, the first of which must be an Integer: 1
Output: 1
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

TASK 2:

Data

Dec

Registers

Name	Width	Data
AC	16	4
AR	12	1
DR	16	3
E	1	0
I	1	0
IR	16	-8191
PC	12	4
S	1	-1

I12t1 ×

I12t2 >

1 INP

2 ISZ 009

3 OUT

4 HLT

5

EXECUTING...

Enter Inputs, the first of which must be an Integer: 4

Output: 4

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]