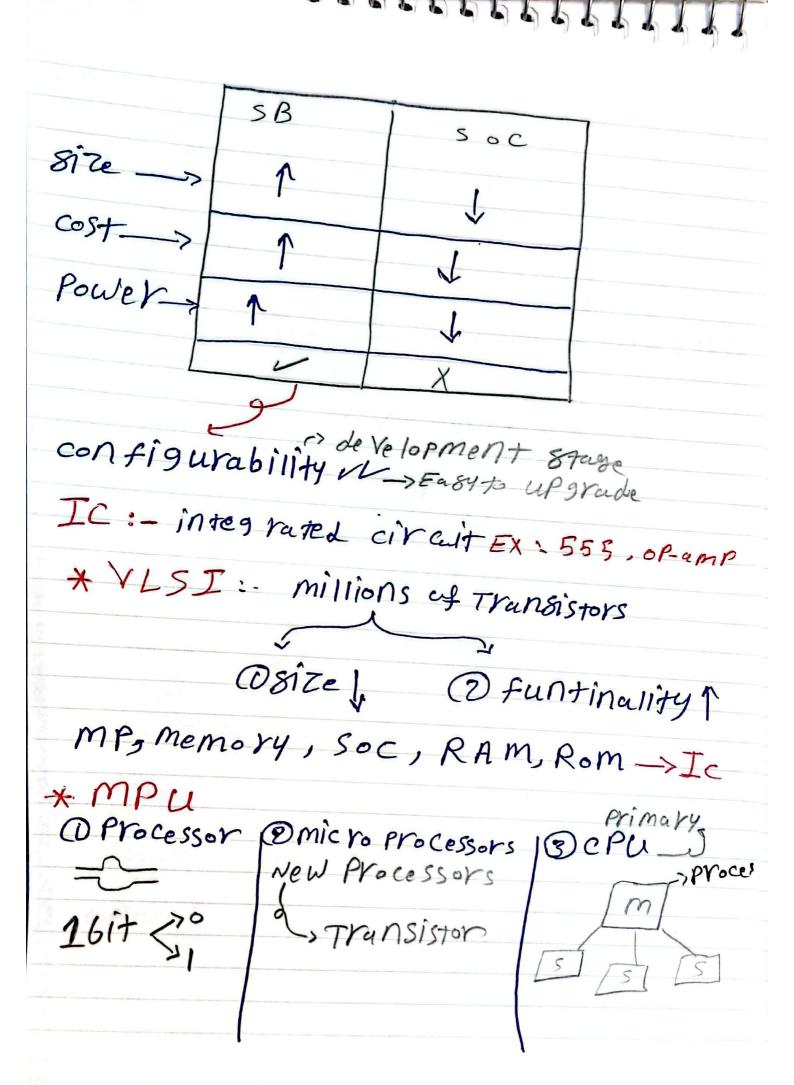
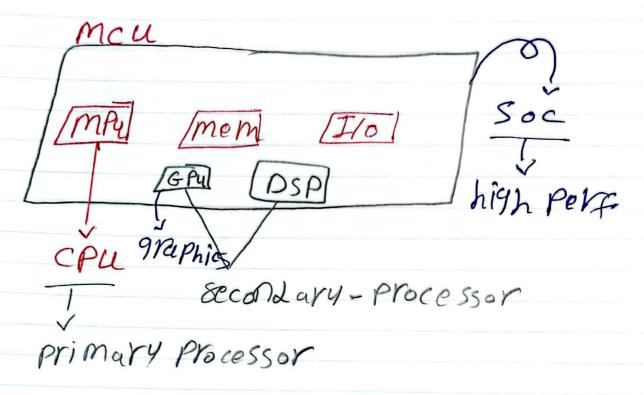
555566666666666666666 in troduction to Embe 22ed 545 tems CONCEPTS:--, Processor computing system memory specific General Pur Pos Purpos EX: LaPtoP EX: Embedded 845 tems constrains => Es chalings power cost size speed Hime Embedded 84stems: , @845tem of Chep Osyste mboard



*MCU Micro controler unit computing 845"



APP SOC

Berenetal SW
APP

APP

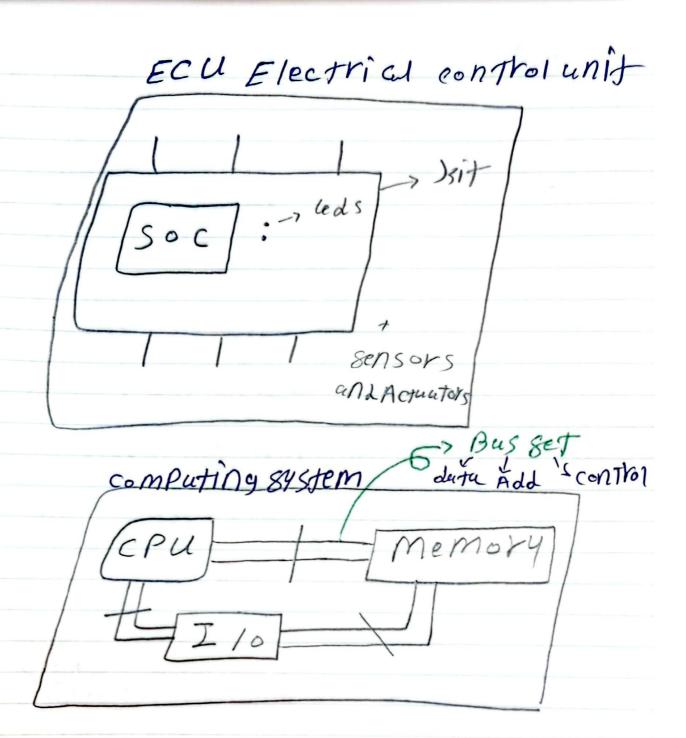
APP

APP

Sernal

drivers

Arivers



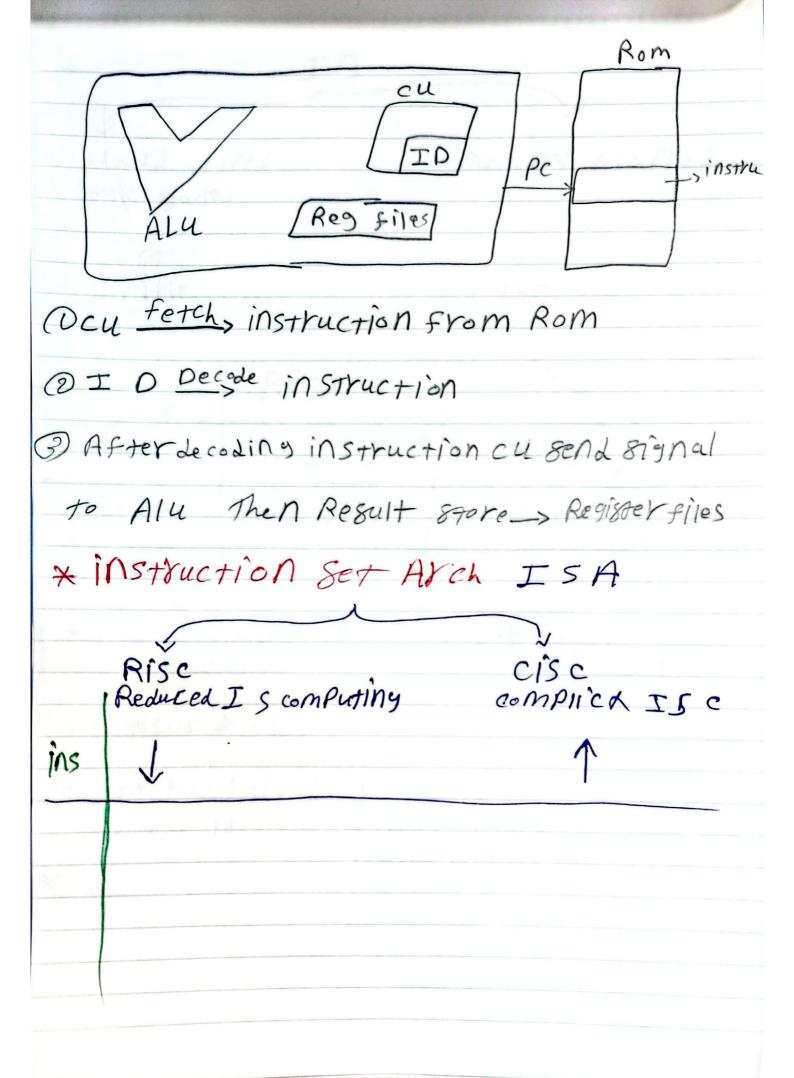
* Process DY :
instruction life cycle

O Fetch

Cu fetch The instruction from rom

to IR "instruction resector

@ Decode insgruction de coder in cu de code the instruction 3) Exe aute



ID

Hard wired

memory mapped

fast Simple Rics

810W CISC

	Rísc	cisc
8iZe	Alw J ID1	ALUA, IDJ
cost	SUT, HWI	SWL, HUT
Perfor m	_	
Power	Alul, IPT	Alwa IDJ

* Register files ...

OGENE VEN PUYPOSa Latu Store Jempo va vy @ special Purose Each register hoss pecific pur Pose

* Stecial Pur Pose Register
OPC -> Program counter L> Next instruction
@SP_>stack pointer
3) ACC->ACCUMULADO -> result store temp
DIR-> instruction Resister—stror The instruction from memory DPSW-> Process status world L> flags
C) flags
* memory: Set of Locations
Ovolatile -> RAM
@NON volatile -> Rom
3) HY 6ri L

Ram	-> Stad	tic -> transis tor amic -> Basedon	
17. 18.	L>D4 No	amic -> Basedon apacitor	
O DYN Advan	a mic Ra tage	m	
Osimpl	e H.W		
10 low	Cost Per	-6H	
3 High	den isity		
D low	power cor	Sumption	
D Stuti	c Ram		
fuster	, tíjgh cos		
each bit has 6 Transistor			
	5		
5/2e	↓	M	
cost	1		
Per	1		
Power	J	1	
*	•		

(4) EEPhom SEttings Storage 3 EP Romold Etasable (8) PRom " programmable rom"-rone time (1) mas sed Rom -> fixed code Programmabe

B flash Rom Rown table firmware

الممسوحة ضوئيا بـ CamScanner

ROB !