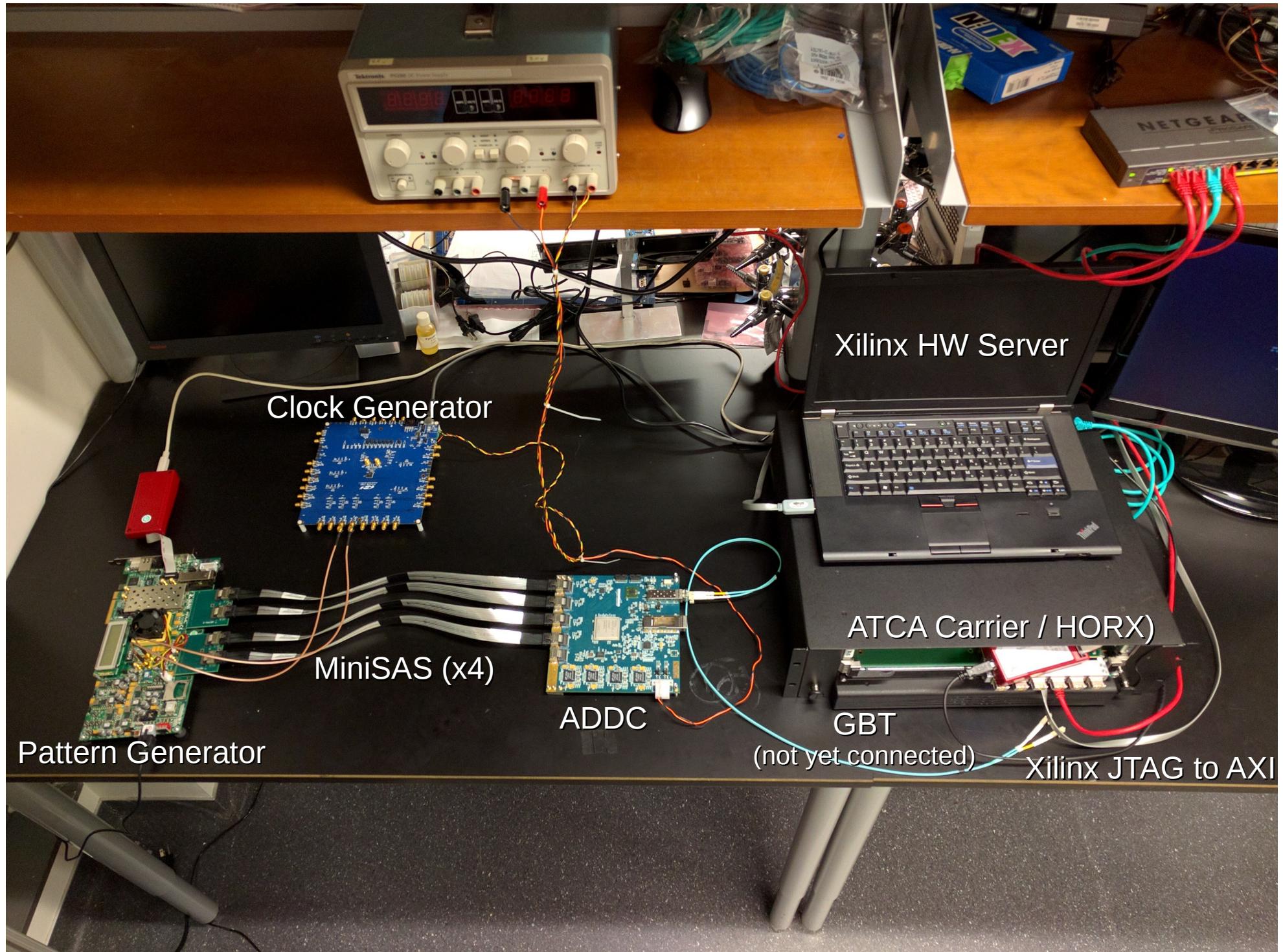


MM Trigger Processor Hardware Testing Update



Status

- ATCA SRS/AMC Powered up with JTAG connected directly to AMC.
- Trigger Processor implemented with JTAG to AXI interface and supports AXI bursts
- DAQ written in TCL using Matlab to generate stimulus data and analyze results.

Status

- Starting to send hit data to the Trigger Processor using an internal ADDC Emulator
- Currently modifying HDL to include alignment corrections and other algorithm modifications
- Changing DAQ software to accommodate hardware changes
- GBT FPGA not yet included in SRS implementation. Looking into what cables we need.

