

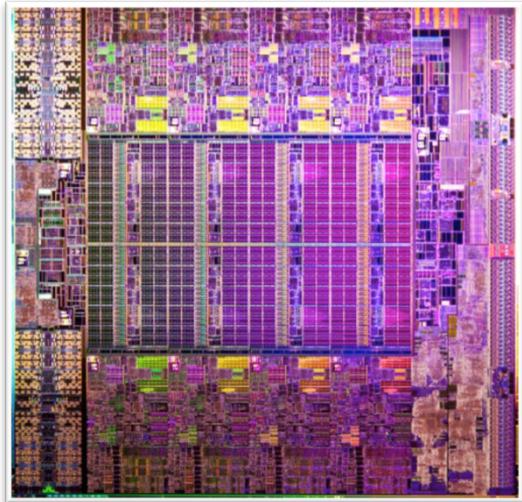


James Lin
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Alacrity is Commercializing Versatile Memory

CMOS Compatible



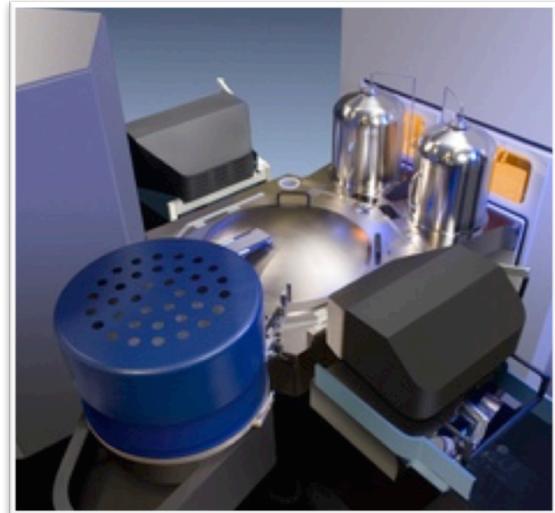
100% better density,
capacitorless design

Ultra-low Power



Up to 25% more
power efficiency

Plug-and-Play to Foundry

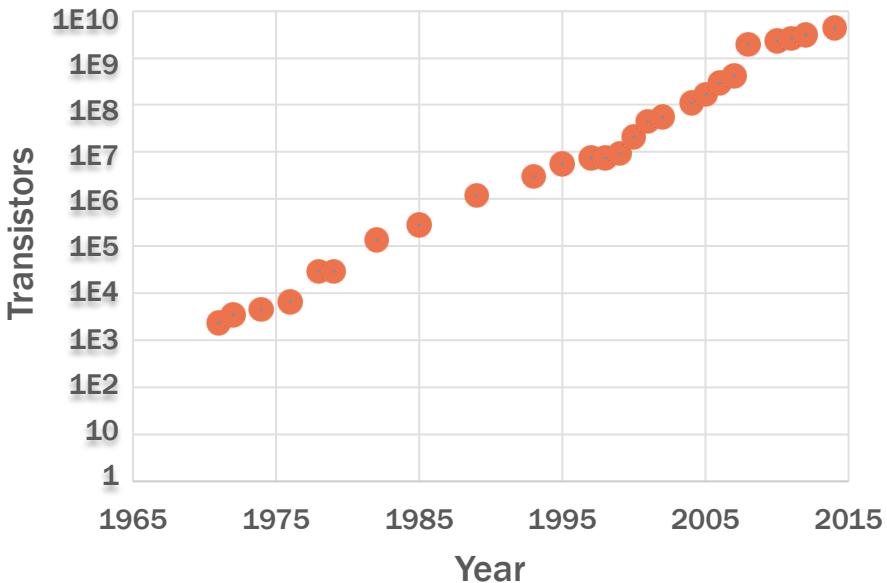


\$0 new equipment
costs



How is Alacrity Unique?

Moore's Law Scaling Slowing



Only memory startup that requires
no foundry changes

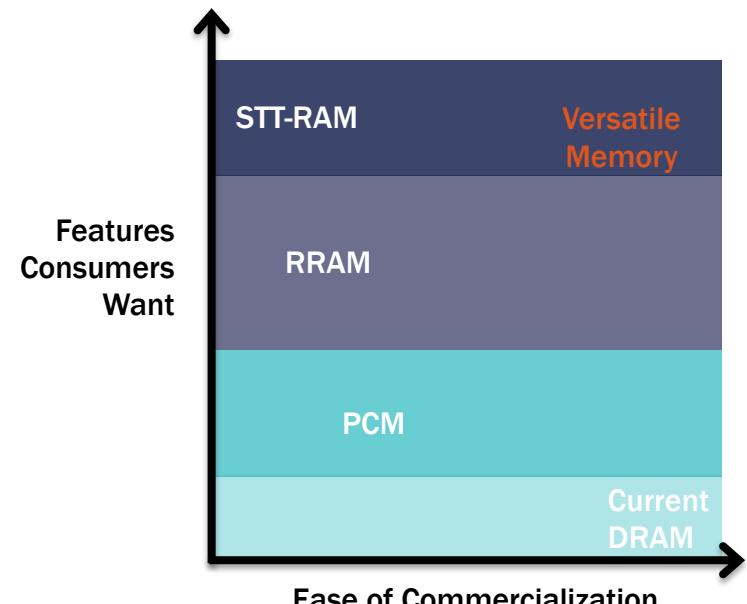


Why Alacrity, Why now?

Better Performance Compared to other Technologies

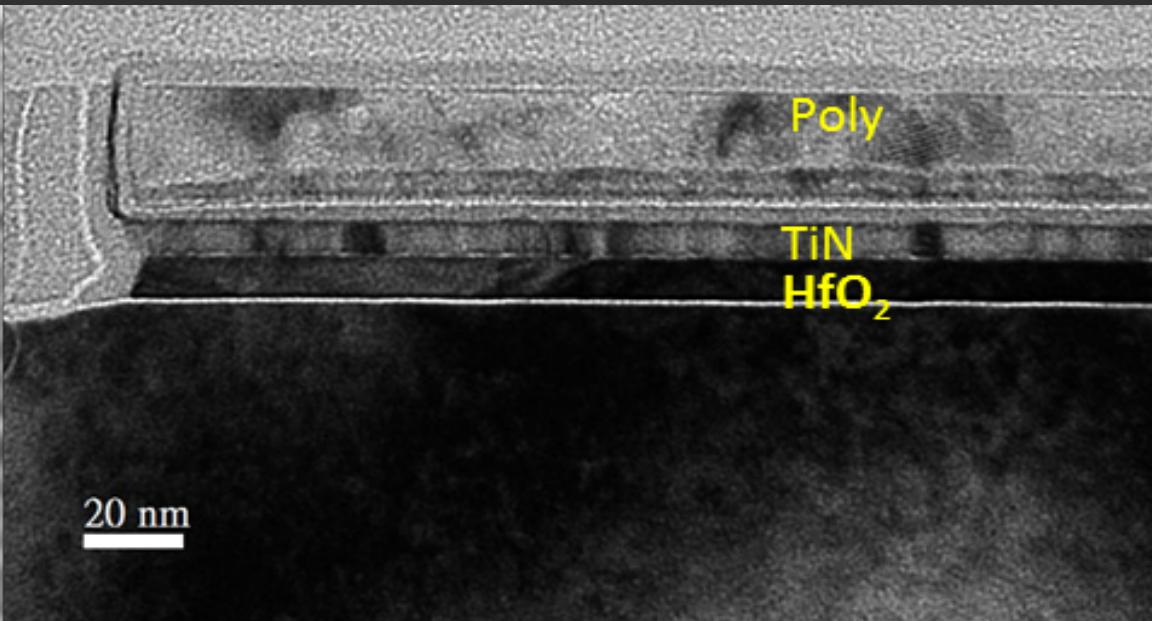
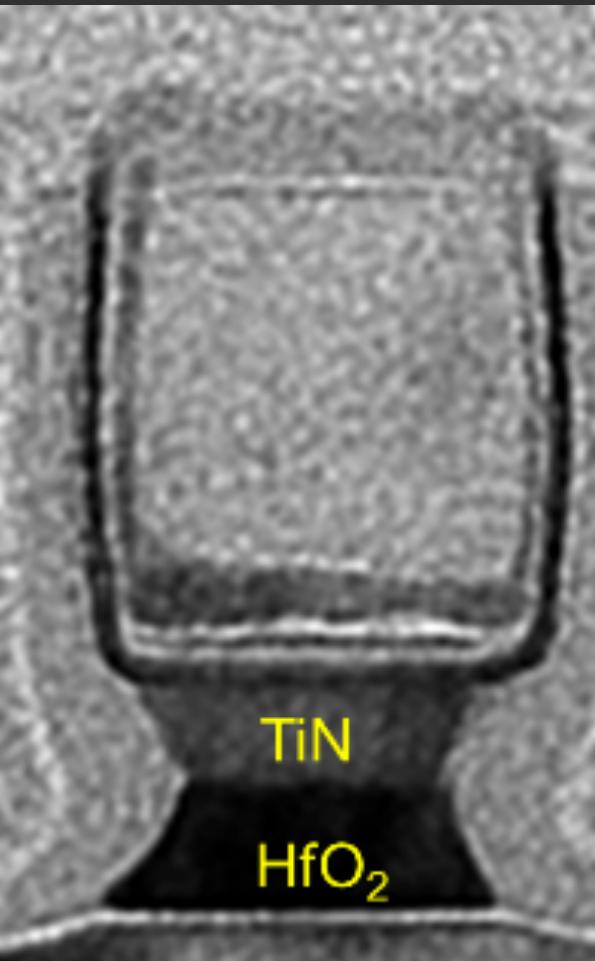
	Versatile Memory	DRAM	PCM	RRAM	STT-RAM
No New Foundry (Low Cost)	Yes	Yes	No	No	No
Cell Size	4 F ²	6 F ²	6 F ²	6 F ²	4 F ²
Energy per bit access	0.1 fJ	2 pJ	100 pJ	2 pJ	0.02 pJ
Write energy per bit	1 fJ	4 fJ	6 pJ	??	2.5 pJ
Scalability	Yes	No	Yes	Yes	Maybe
Easy Embedded Application	Yes	No	Maybe	Maybe	No

Superior Positioning





Where Are We Now?



Raising Series A in
Q3 2015

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