

PWB 509101 REV D  
2-7-07  
01  
REV A  
TOP SIDE MOTTOW

F28

MAC APPRENTICESHIP

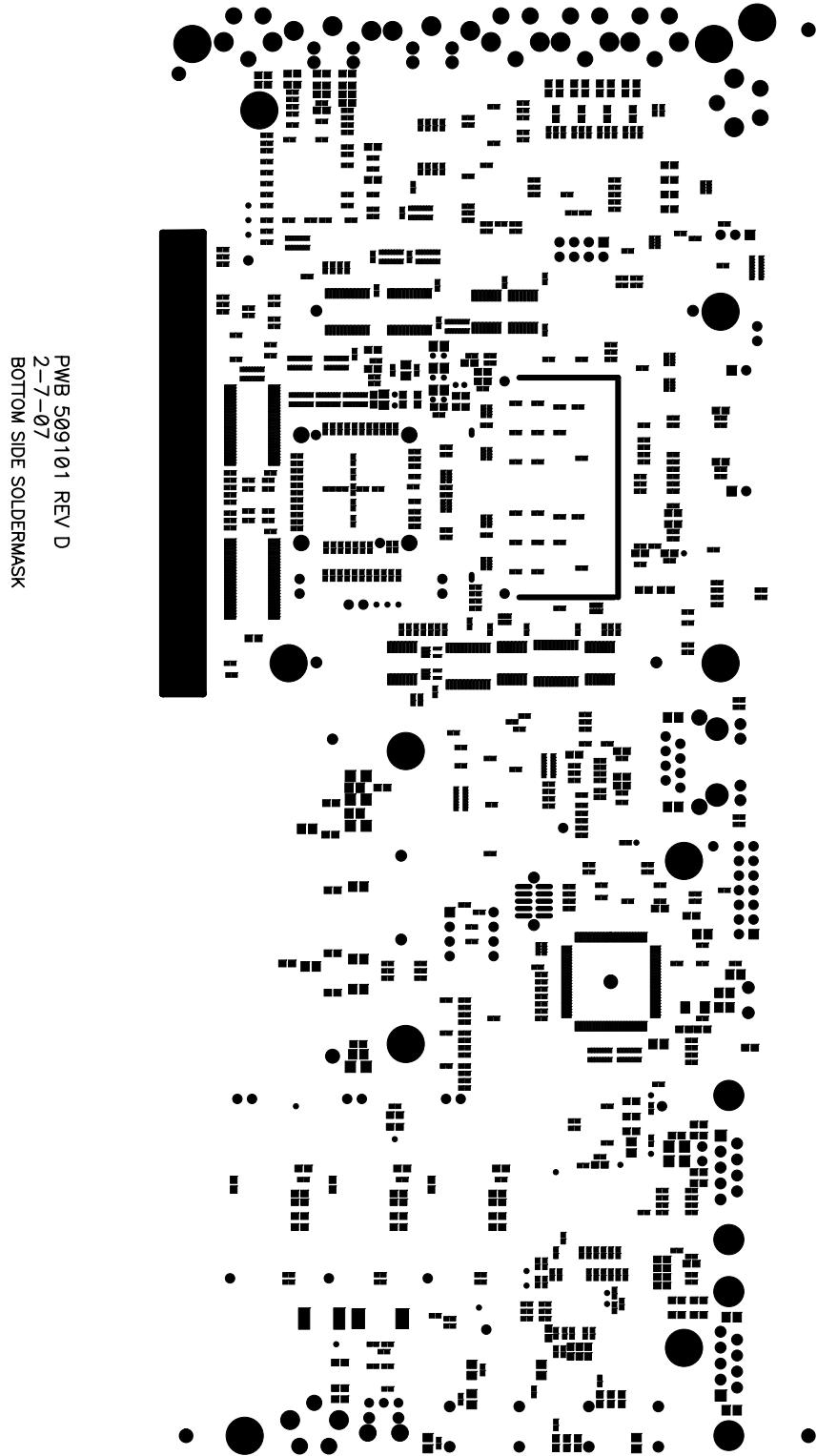
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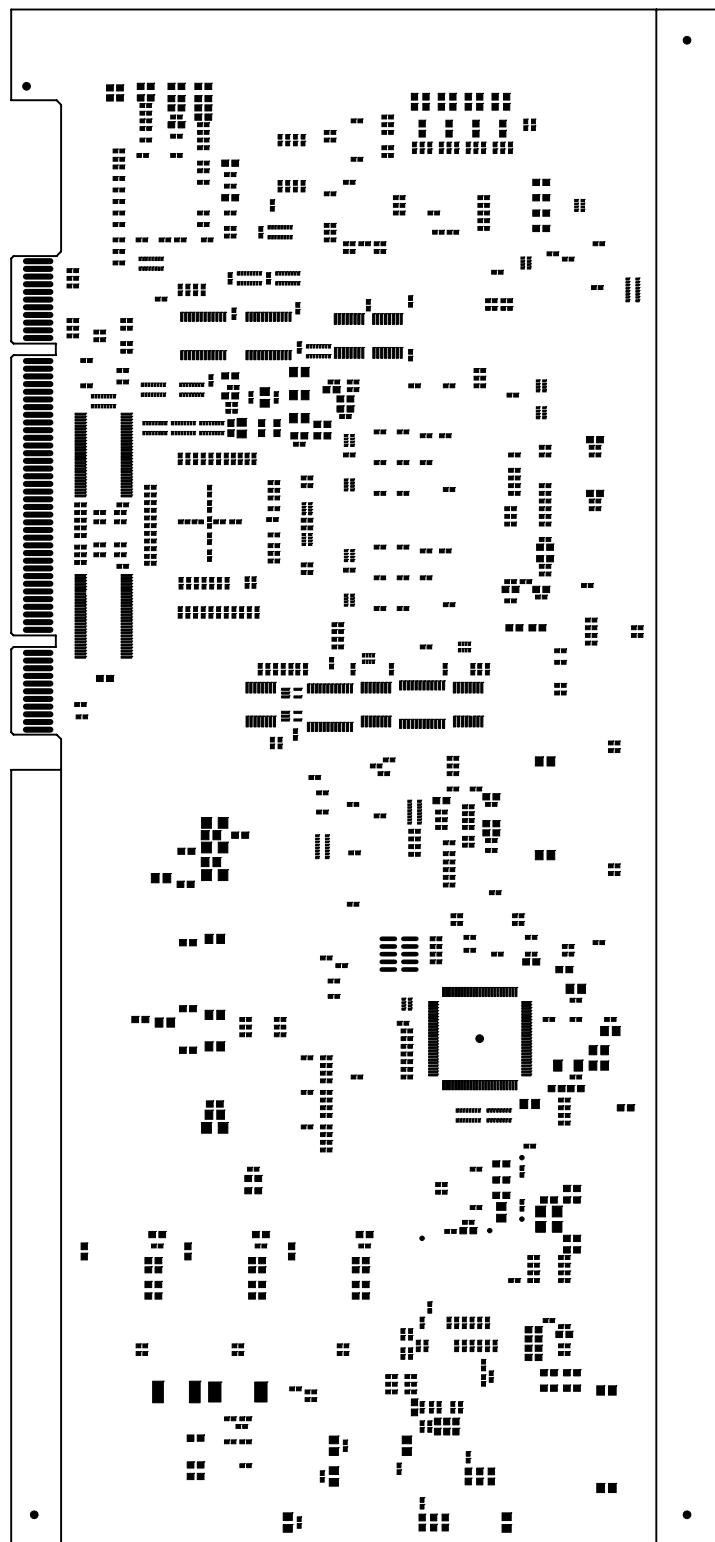
152

PWB 509101 REV D  
2-7-07  
BOTTOM SIDE SCREEN

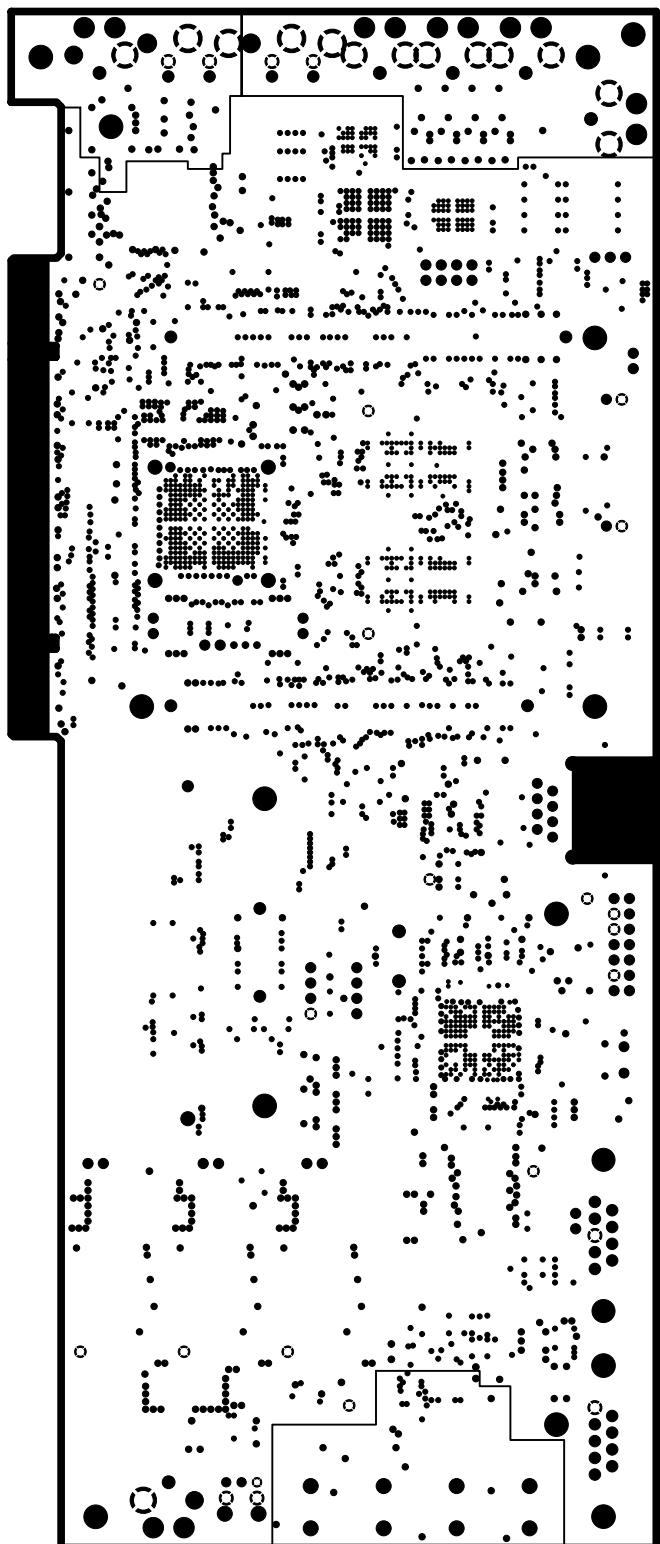


PWB 509101 REV D  
2-7-07  
BOTTOM SIDE SOLDERMASK

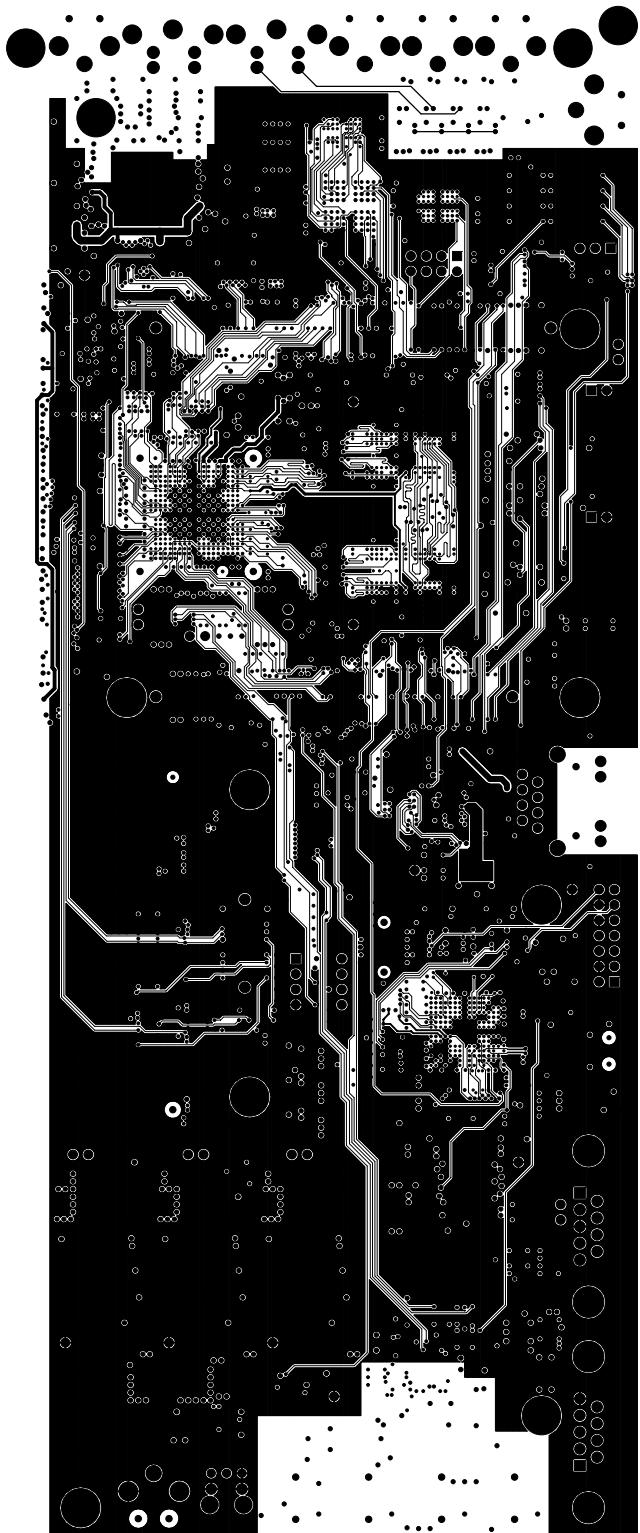
OPERATOR EDGE  
LEADING EDGE SIDE  
SOUNDING EDGE



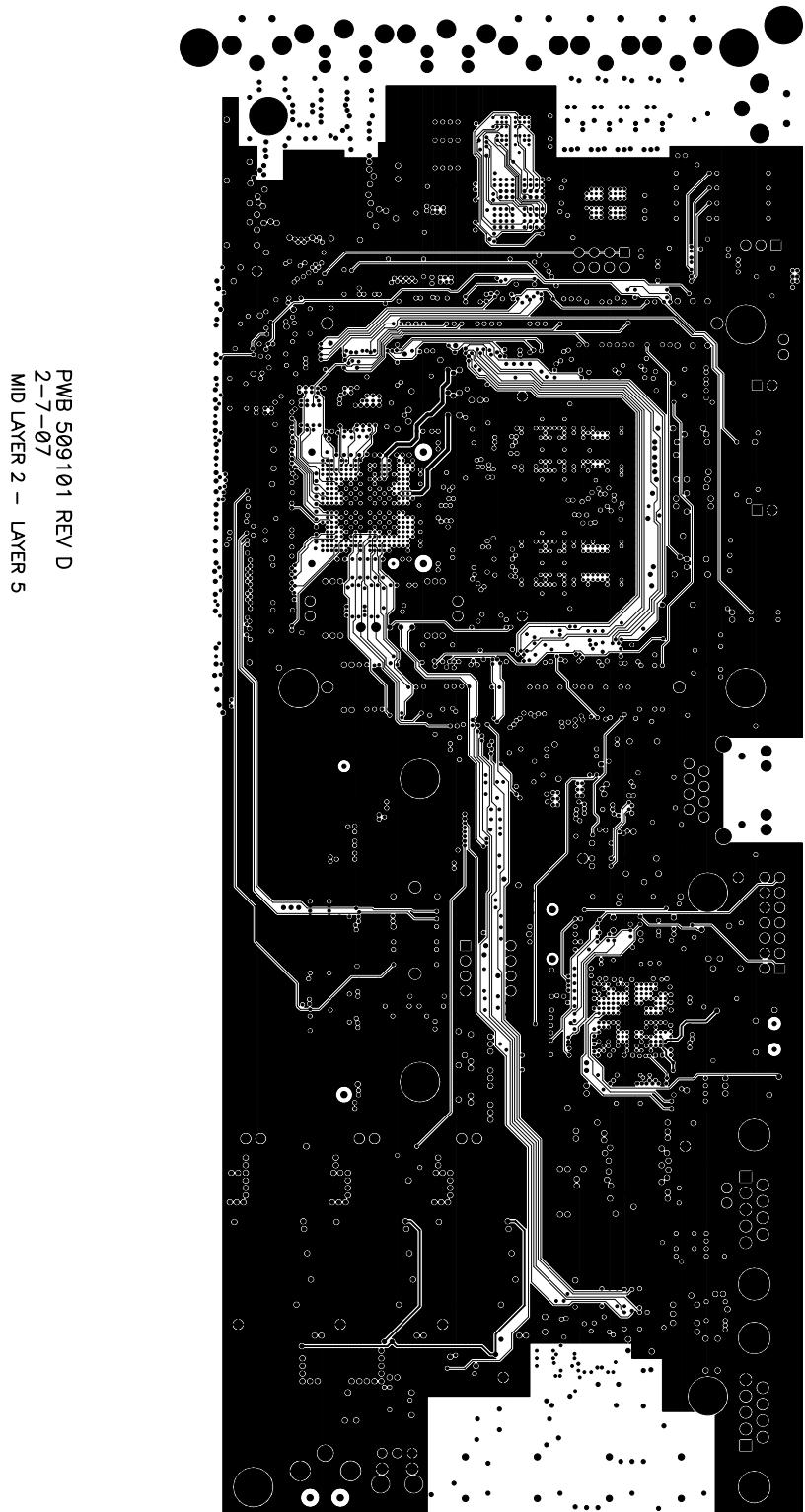
PWB 509101 REV D  
2-7-07  
BOTTOM SIDE SOLDERPASTE

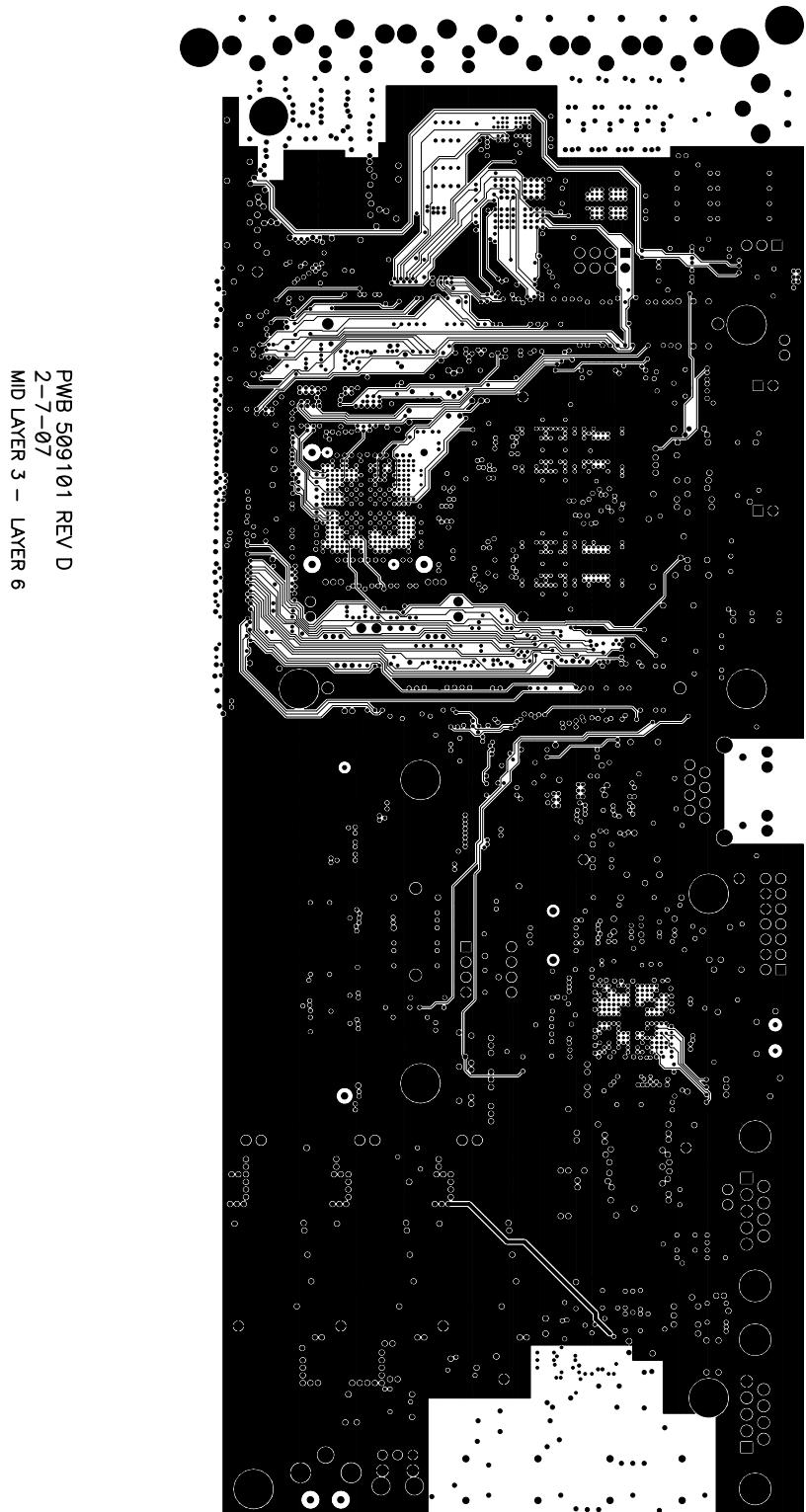


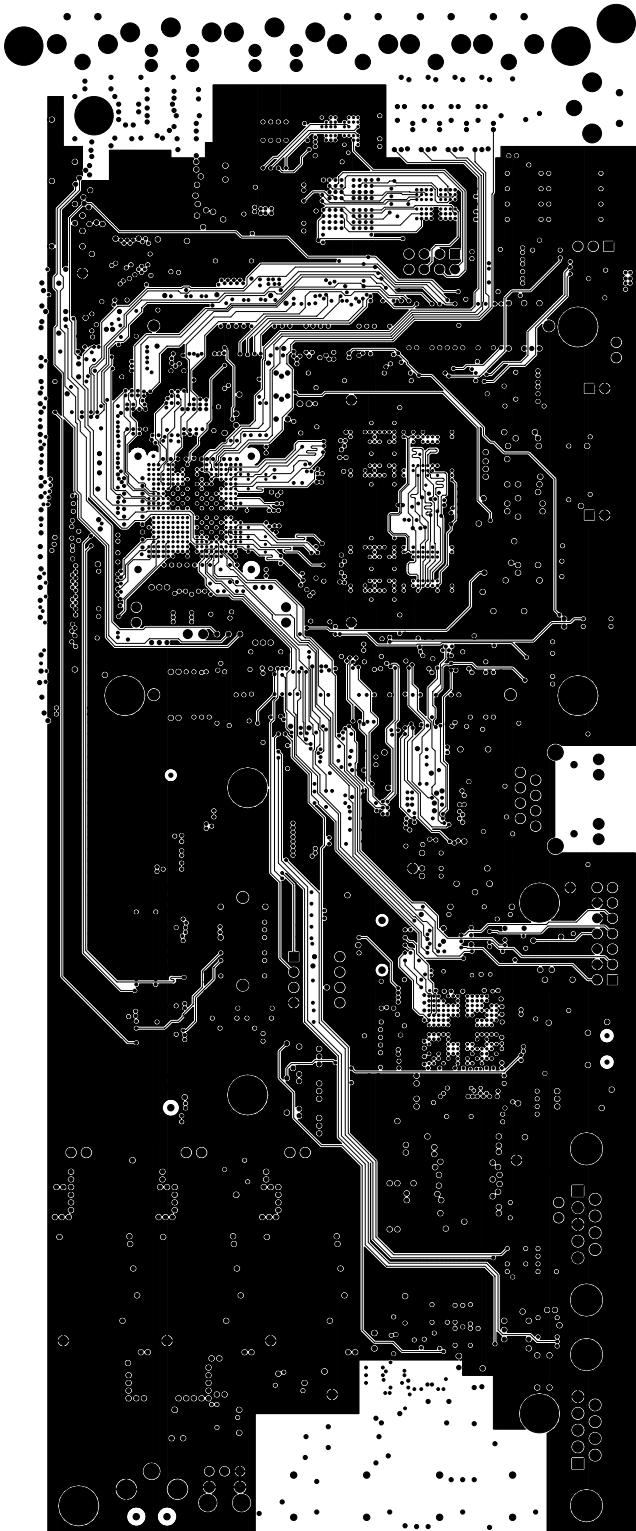
PWB 509101 REV D  
2-7-07  
GND PLANE - LAYER 2 & 9



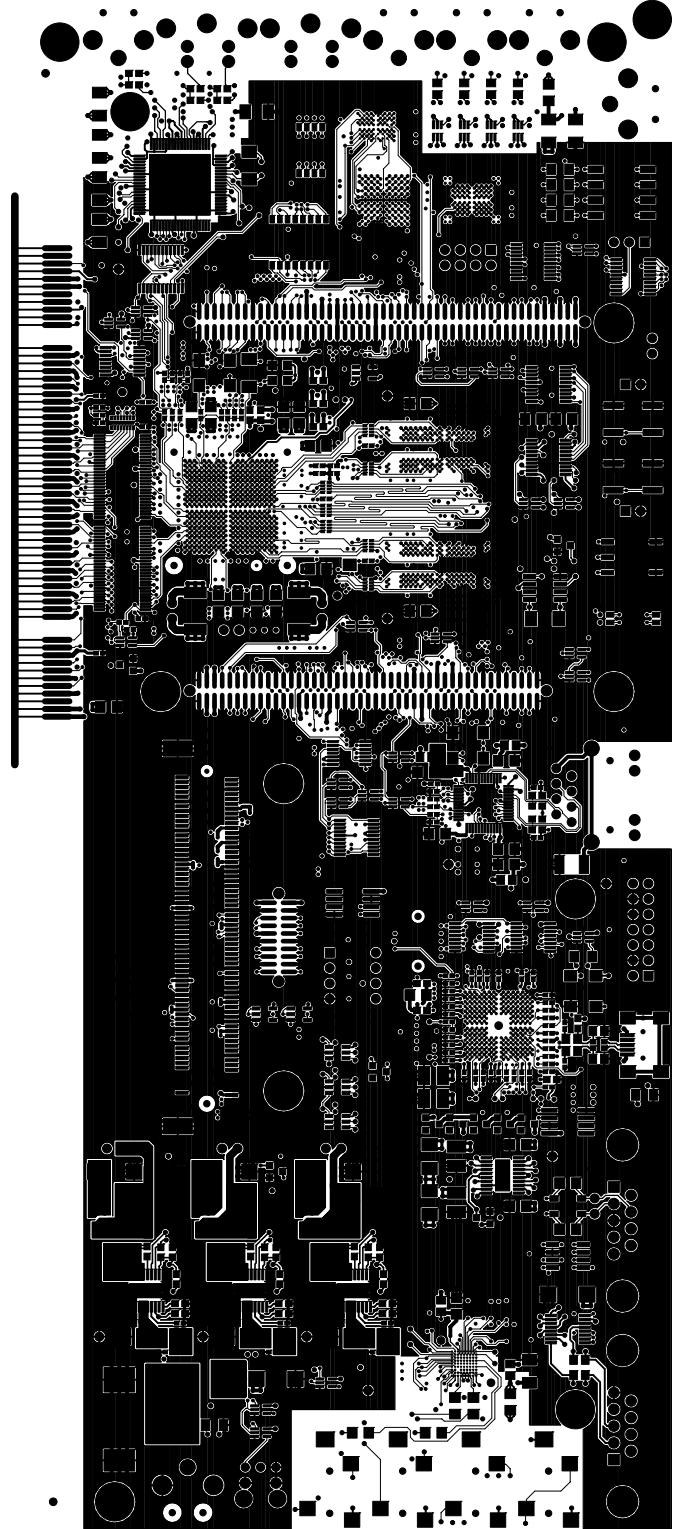
PWB 509101 REV D  
2-7-07  
MID LAYER 1 - LAYER 3



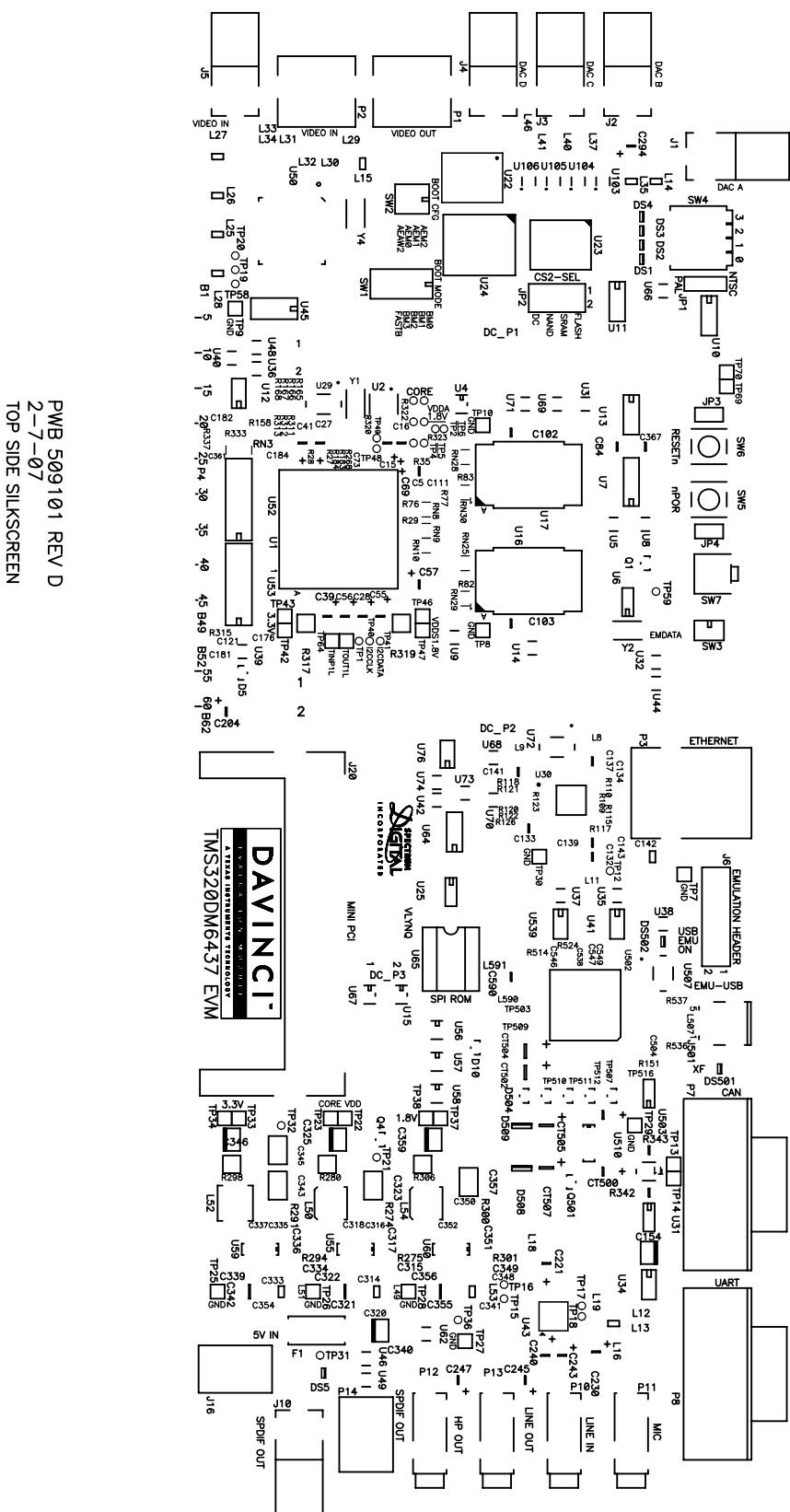


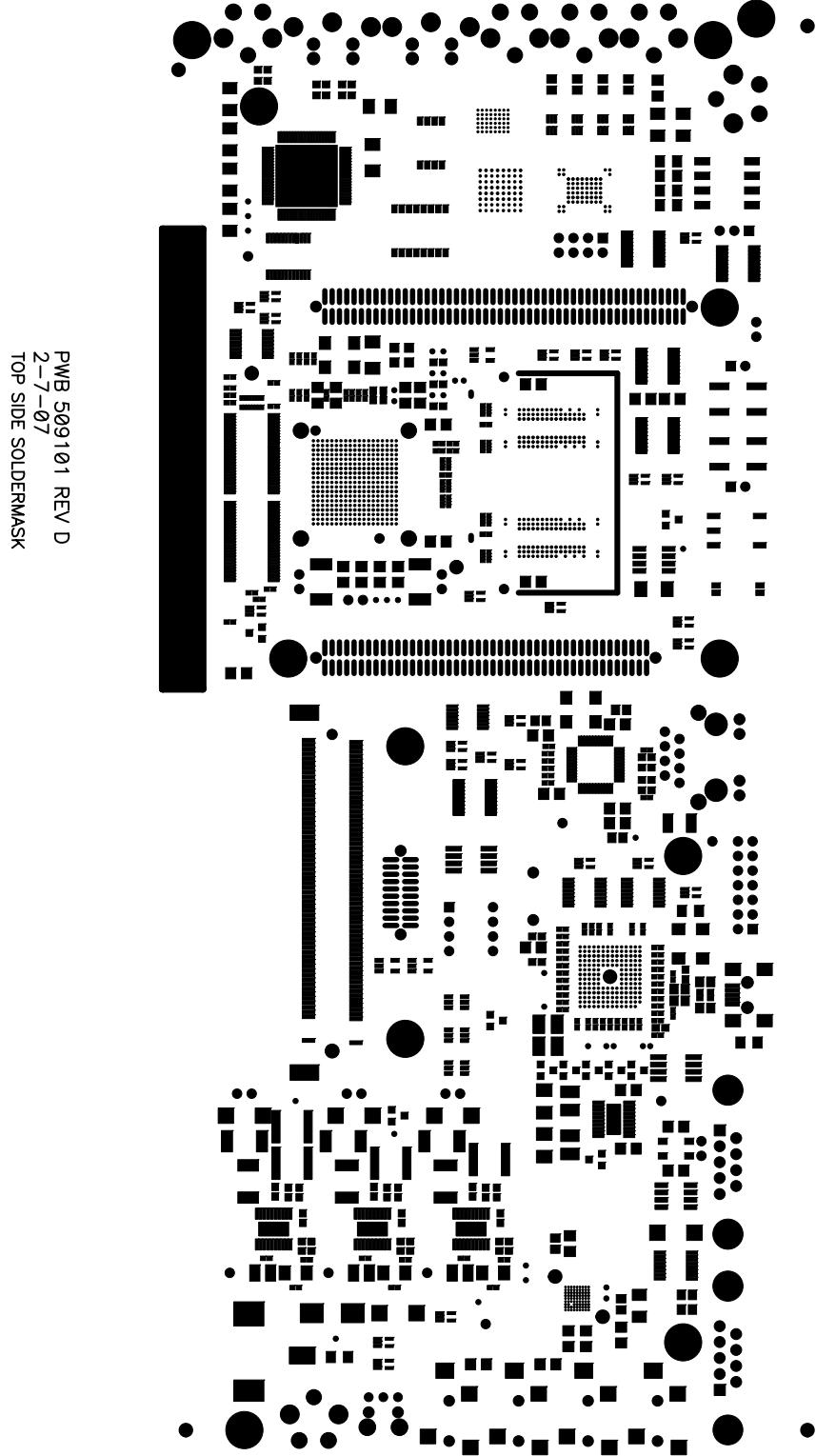


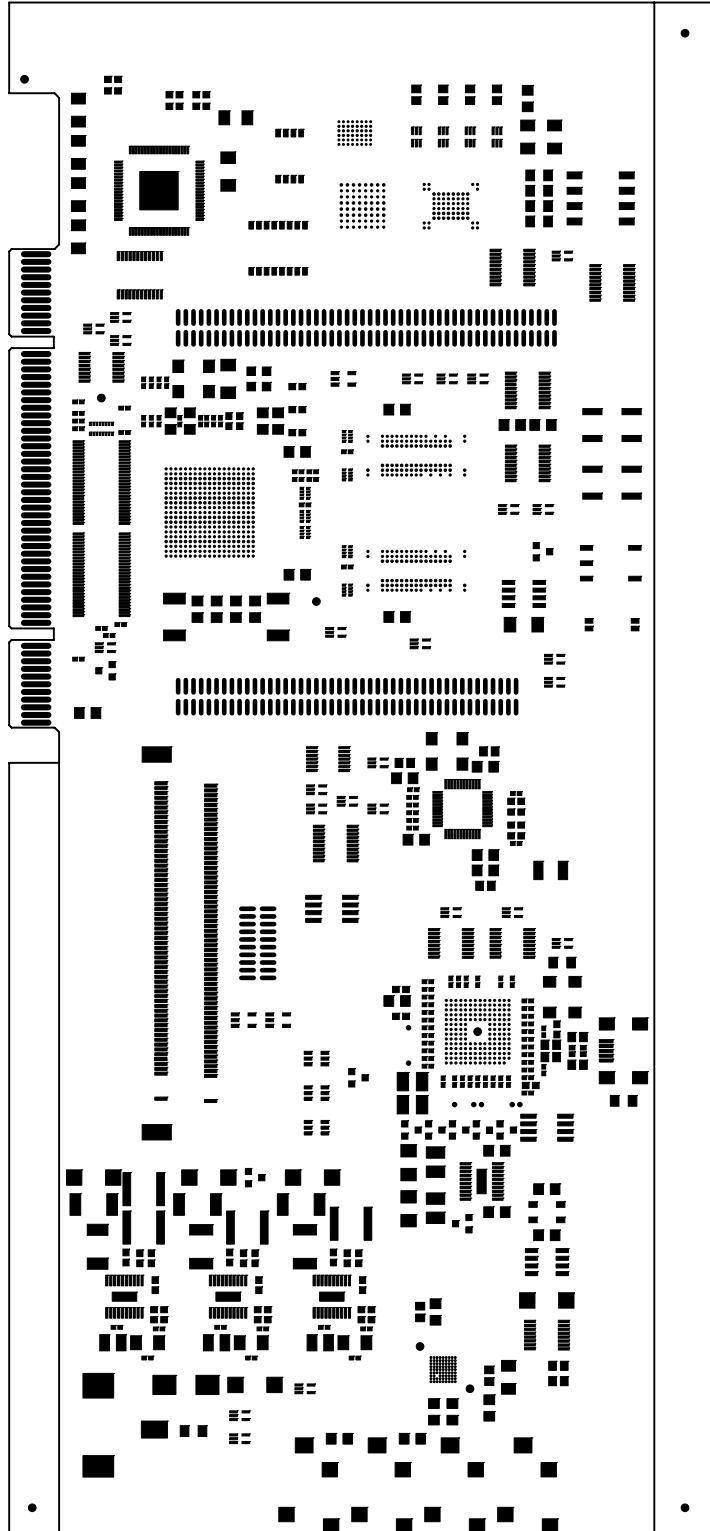
PWB 509101 REV D  
2-7-07  
MID LAYER 4 - LAYER 8



PWB 509101 REV D  
2-7-07  
TOP SIDE - LAYER 1

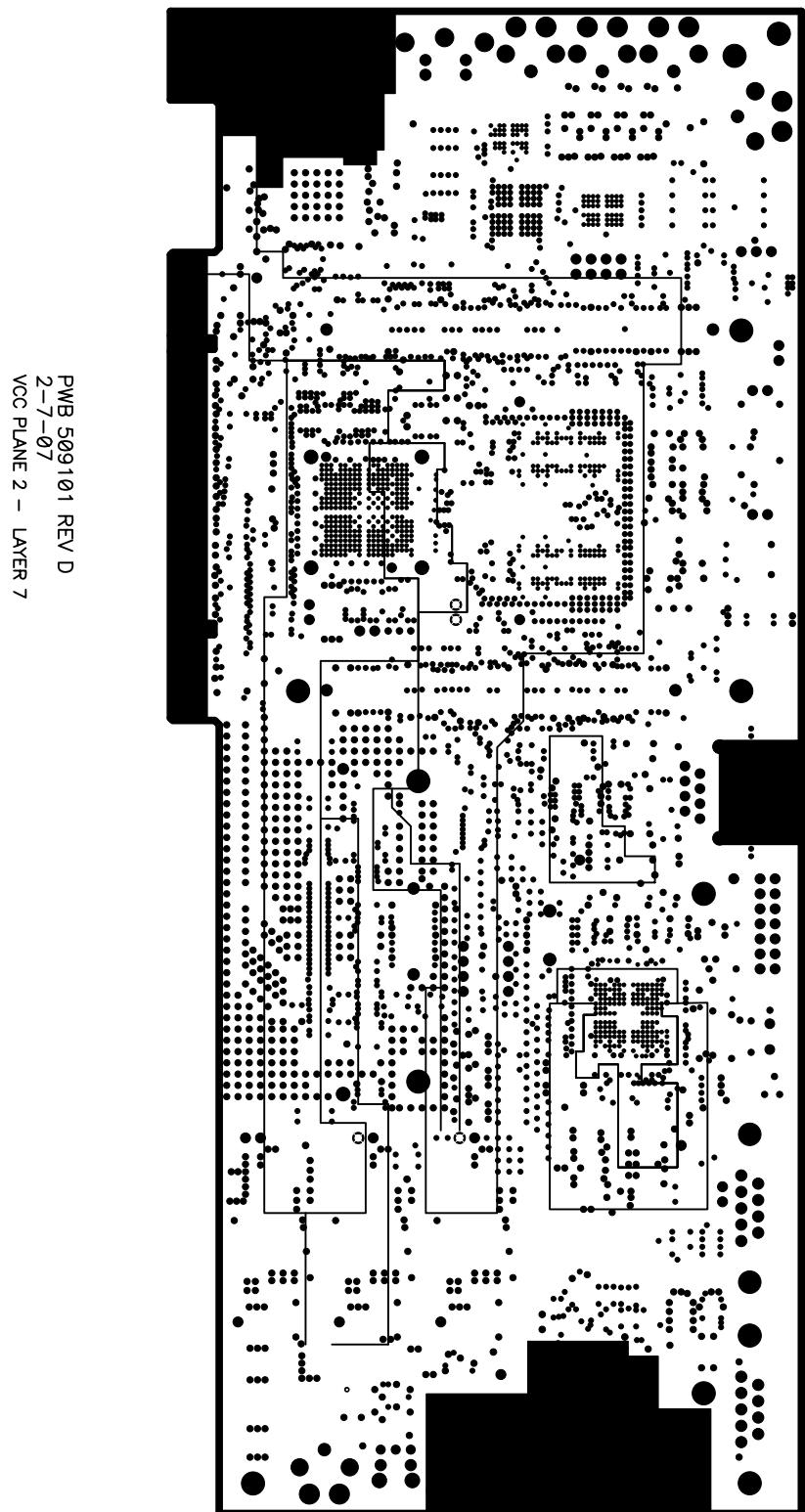


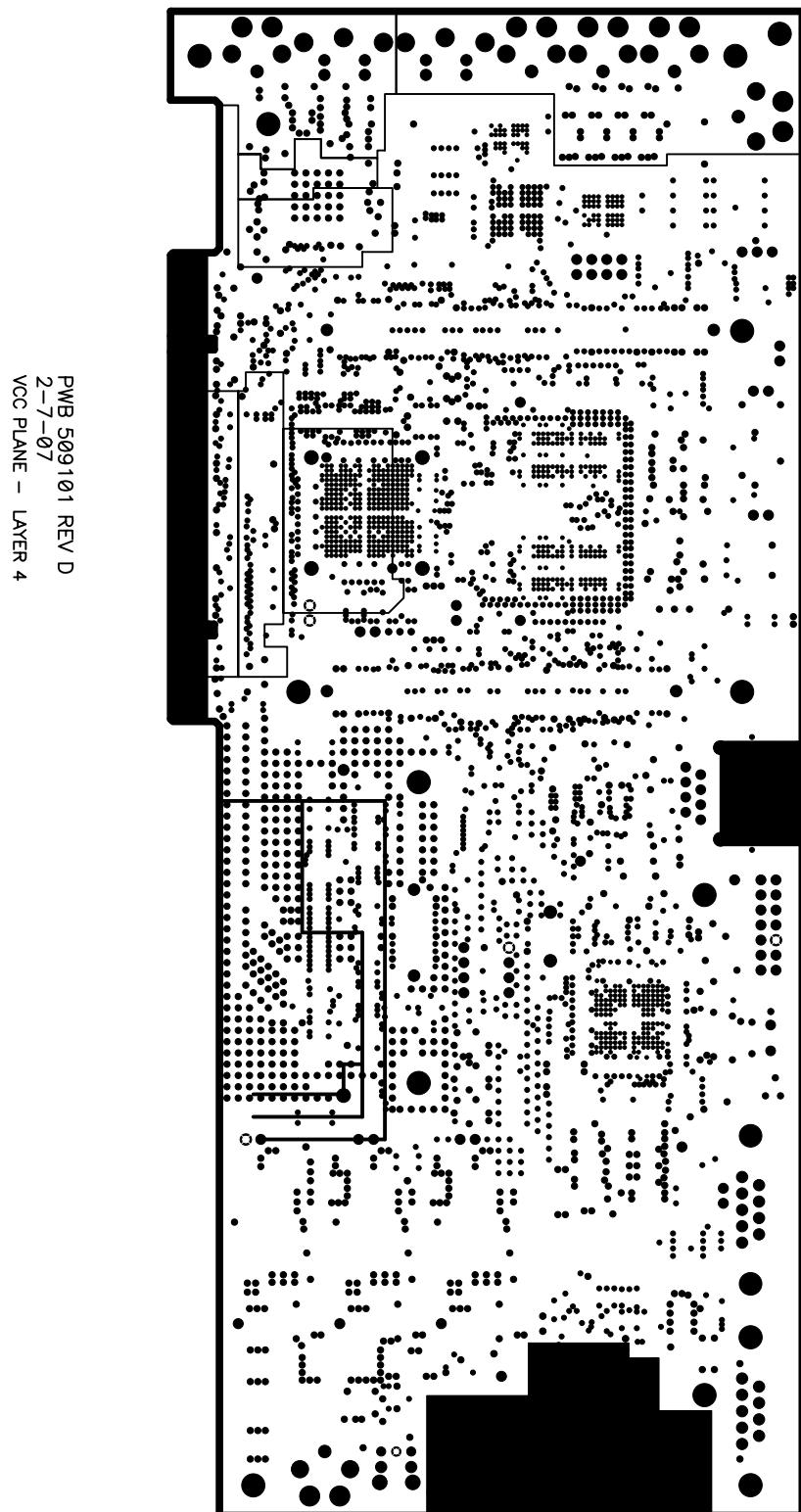




SQUEEGEE SIDE  
LEADING EDGE →  
OPERATOR EDGE ←

PWB 509101 REV D  
2-7-07  
TOP SIDE SOLDERPASTE





PWB 509101 REV D  
2-7-07  
VCC PLANE – LAYER 4

NOTES: UNLESS OTHERWISE SPECIFIED

- 1 MATERIAL: HIGH TEMP FR4 ( $T_g \geq 170$ ). THICKNESS: .062".
- 2 DIMENSIONS APPLY TO THE CENTER OF HOLES AND PADS.
- 3 ALL NON-DIMENSIONAL HOLES ARE ON .025 GRID INCREMENTS IN RELATION TO DATUM 0.
- 4 HOLES ARE TO BE LOCATED WITHIN .013" dia. TRUE POSITION OF DATUM 0 WHEN PINNED TO PLANE A.
- 5 MINIMUM COPPER WALL THICKNESS SHALL BE .001 FOR ALL PLATED THRU HOLES.

17 5.1 MIL TRACES ON LAYERS 3 AND 10 TO BE

- 55 OHM, SINGLE ENDED AND 100 OHM DIFFERENTIAL.  
16 5.1 MIL TRACES ON LAYERS 1 TO BE 55 OHM,  
SINGLE ENDED AND 100 OHM DIFFERENTIAL.

15 CONTROLLED IMPEDANCE OF 50 OHMS TO BE MATCHED

ON ALL SIGNAL LAYERS.

14 BOARD IS TO BE 100% ELECTRICALLY TESTED.

13 SILKSCREEN BOTH SIDES WITH WHITE EPOXY BASED INK PER SILKSCREEN ARTWORK.

12 EXPOSED CONDUCTOR AREAS TO BE FINISHED WITH ELECTROLESS NICKEL THICKNESS .000050 MINIMUM, PLUS IMMERSION GOLD, THICKNESS .000005 MINIMUM.

11 SOLDER MASK SHALL BE LIQUID PHOTOMAGEABLE MATTE FINISH, COLOR GREEN, WITH A THICKNESS NOT TO EXCEED 1.5 MILS AND SHALL BE APPLIED TO BOTH SIDES OF BOARD OVER BARE COPPER PER IPC-SM-840, CLASS 2.

10 EDGE CONNECTOR TABS ARE TO BE ELECTROPLATED TO A MINIMUM THICKNESS OF .00003 GOLD OVER A MINIMUM THICKNESS OF .0001 NICKEL.

9 INTERNAL AND EXTERNAL CORNERS TO BE .06 RADIUS MAX AT SHOP OPTION.

8 MAXIMUM ALLOWABLE LINE REDUCTION DUE TO PIN HOLES, NICKS AND LINE SHRINKAGE IS TO BE 20%.

7 HOLE DIAMETERS AND TOLERANCES SHOWN ARE AFTER PLATING.

6 ALL ARTWORK VIEWED FROM COMPONENT SIDE.

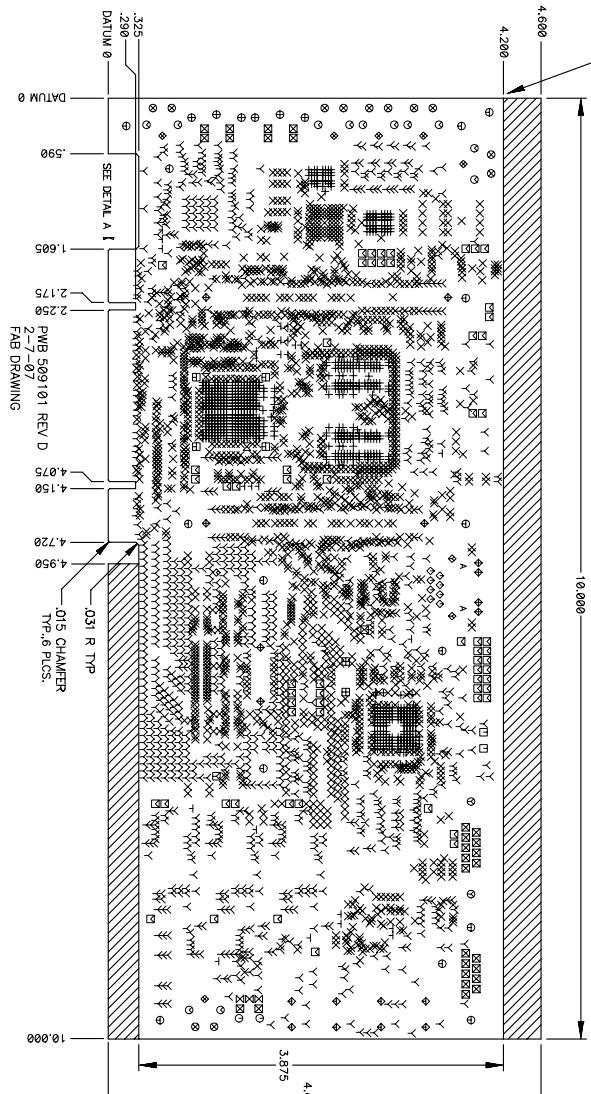
5 GRID INCREMENTS IN RELATION TO DATUM 0.

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SEE DETAIL A 1

DET A

DET B

DET C

DET D

DET E

DET F

DET G

DET H

DET I

DET J

DET K

DET L

DET M

DET N

DET O

DET P

DET Q

DET R

DET S

DET T

DET U

DET V

DET W

DET X

DET Y

DET Z

DET A

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DET C

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