

# ALAKUNTA ANILKUMAR

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## PROFESSIONAL SUMMARY

Enthusiastic and detail-oriented fresher with a strong foundation in **HTML, CSS, JavaScript, React.js, Python, Django and SQL**. Skilled in building **dynamic and responsive web applications**, **writing clean and efficient code**, and working with databases to build scalable solutions. Eager to contribute **technical expertise, problem-solving abilities, and a continuous learning mindset** to a growth-oriented organization.

## EDUCATION

<b>Anantha Lakshmi Institute of Technology and Sciences</b> Bachelor of Technology in Electronics and Communication Engineering CGPA: 7.80/10	<b>Nov 2021 - Apr 2025</b>
<b>Swamy Vivekananda Junior College, Anantapur</b> Intermediate (MPC) CGPA: 8.87/10	<b>Jun 2019 - Mar 2021</b>
<b>Z.P.H.S, Ramapuram, Anantapur</b> Secondary School Certificate (10th) CGPA: 8.70/10	<b>Jun 2018 - Apr 2019</b>

## SKILLS SUMMARY

**Programming Languages:** Python, OOP Concepts, Data Structures (Lists, Tuples, Sets, Dictionaries)

**Frameworks:** Django

**Web Technologies:** HTML, CSS, Tailwind CSS, JavaScript (Basic), React.js (Basic)

**Databases:** SQL (Query Writing, Performance Optimization)

**Tools:** Visual Studio Code, GitHub

**Professional Skills:** Problem-Solving, Analytical Thinking, Teamwork & Collaboration, Effective Communication, Adaptability, Quick Learning

## INTERNSHIPS

### Full Stack Development Intern – Blackbucks Engineers Pvt. Ltd., 2025

- Developed and deployed **dynamic web applications** using **Python, Django, SQL, HTML, CSS, and JavaScript**.
- Designed and integrated a **responsive front-end** with a **Django backend**, improving user experience and reducing load time by ~20%.
- Optimized database queries**, improving **application performance and efficiency** by ~15%.

## PROJECTS

### Implementation and Analysis of High-Efficiency FIFO Buffer for Digital Systems

- Implemented** a FIFO buffer using Verilog HDL on a Genesys FPGA board with Xilinx Vivado.
- Achieved **low power consumption of 2.189W** and optimized hardware utilization (**0.008% LUT, 0.002% IO, 3.13% BUF**).
- Improved efficiency, making the design suitable for **embedded systems and real-time communication applications**.

### Personal Portfolio Website Development

- Built a fully responsive personal portfolio** website using **HTML, CSS, and VS Code** to showcase projects, skills, and achievements.
- Created a clean, interactive UI** with hover effects, icons, and organized content blocks for enhanced user experience.
- Integrated a contact form and social media** links for easy recruiter engagement.

## ACHIEVEMENTS

- Successfully led and coordinated a 5-member final year project team**, resolving delays and ensuring timely task completion, resulting in our team being the **first in the batch to submit the project to the HOD**.

## CERTIFICATIONS

- Python Full Stack Development** – Thops Tech Career Solutions, 2025
- AI & Machine Learning Certification** – IIIT-Blackbuck Engineers Pvt. Ltd