

## Terminal size

236 × 80

☒ Term size = win size☐ Auto window resize

## New-line

Receive: CR+LF ▾

Transmit: CR+LF ▾

OK

Cancel

Help

Terminal ID: VT100 ▾

☒ Local echoAnswerback: ☐ Auto switch (VT<->TEK)

## Coding (receive)

UTF-8 ▾

## Coding (transmit)

UTF-8 ▾

[Click to open coding tab in Additional Settings dialog](#)

Port: COM5 ▾

Speed: 115200 ▾

Data: 8 bit ▾

Parity: none ▾

Stop bits: 1 bit ▾

Flow control: none ▾

New setting

Cancel

Help

Transmit delay

0 msec/char 0 msec/line

Device Friendly Name: Silicon Labs CP210x USB to UART Bridge ▲

Device Instance ID: USB\VID\_10C4&amp;PID\_EA60\0001

Device Manufacturer: Silicon Labs

Provider Name: Silicon Laboratories Inc.

Driver Date: 4-13-2023

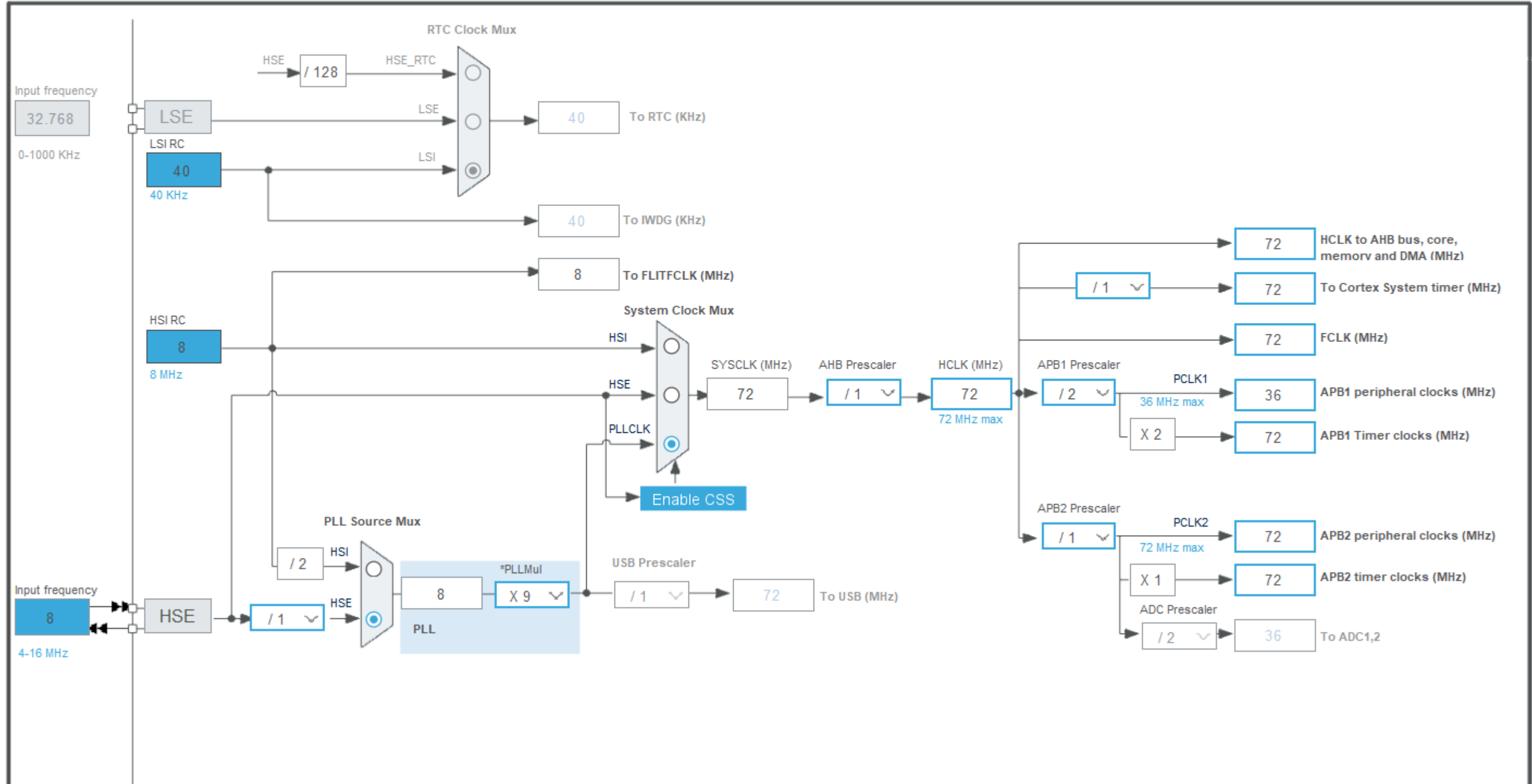
Driver Version: 11.3.0.176 ▼

```
97
98  /* Configure the system clock */
99  SystemClock_Config();
100
101  /* USER CODE BEGIN SysInit */
102
103  /* USER CODE END SysInit */
104
105  /* Initialize all configured peripherals */
106  MX_GPIO_Init();
107  MX_USART1_UART_Init();
108  MX_USART2_UART_Init();
109  /* USER CODE BEGIN 2 */
110  HAL_UARTEx_ReceiveToIdle_IT(&huart1, message, 128);
111  HAL_UARTEx_ReceiveToIdle_IT(&huart2, message, 128);
112
113
114  /* USER CODE END 2 */
115
116  /* Infinite loop */
117  /* USER CODE BEGIN WHILE */
118  while (1)
119  {
120      /* USER CODE END WHILE */
121
122      /* USER CODE BEGIN 3 */
123  }
124  /* USER CODE END 3 */
```

```
55 /* USER CODE BEGIN PFP */
56
57 /* USER CODE END PFP */
58
59 /* Private user code -----*/
60 /* USER CODE BEGIN 0 */
61 uint8_t message[128];
62
63 void HAL_UARTEx_RxEventCallback(UART_HandleTypeDef *huart, uint16_t Size)
64 {
65     if (huart->Instance==USART1)
66     {
67         HAL_UART_Transmit(&huart2, message, Size, 1000);
68     }
69     else if (huart->Instance==USART2)
70     {
71         HAL_UART_Transmit(&huart1, message, Size, 1000);
72     }
73     HAL_UARTEx_ReceiveToIdle_IT(huart, message, 128);
74 }
75
76 /* USER CODE END 0 */
77
78 /**
79  * @brief The application entry point.
80  * @retval int
81  */
82 int main(void)
```



Resolve Clock Issues



Pinout & Configuration

Clock Configuration

Project Manager

Tools

Software Packs

Pinout

Search



Categories A->Z

- IWDG
- NVIC
- ✓ RCC
- ✓ SYS
- WWDG

Analog >

Timers >

Connectivity >

- CAN
- I2C1
- I2C2
- SPI1
- SPI2
- ✓ USART1
- ✓ USART2
- USART3
- USB

Computing >

Middleware and Software P... >

USART2 Mode and Configuration

Mode

Mode Asynchronous

Hardware Flow Control (RS232) Disable

Configuration

Reset Configuration

✓ NVIC Settings

✓ DMA Settings

✓ GPIO Settings

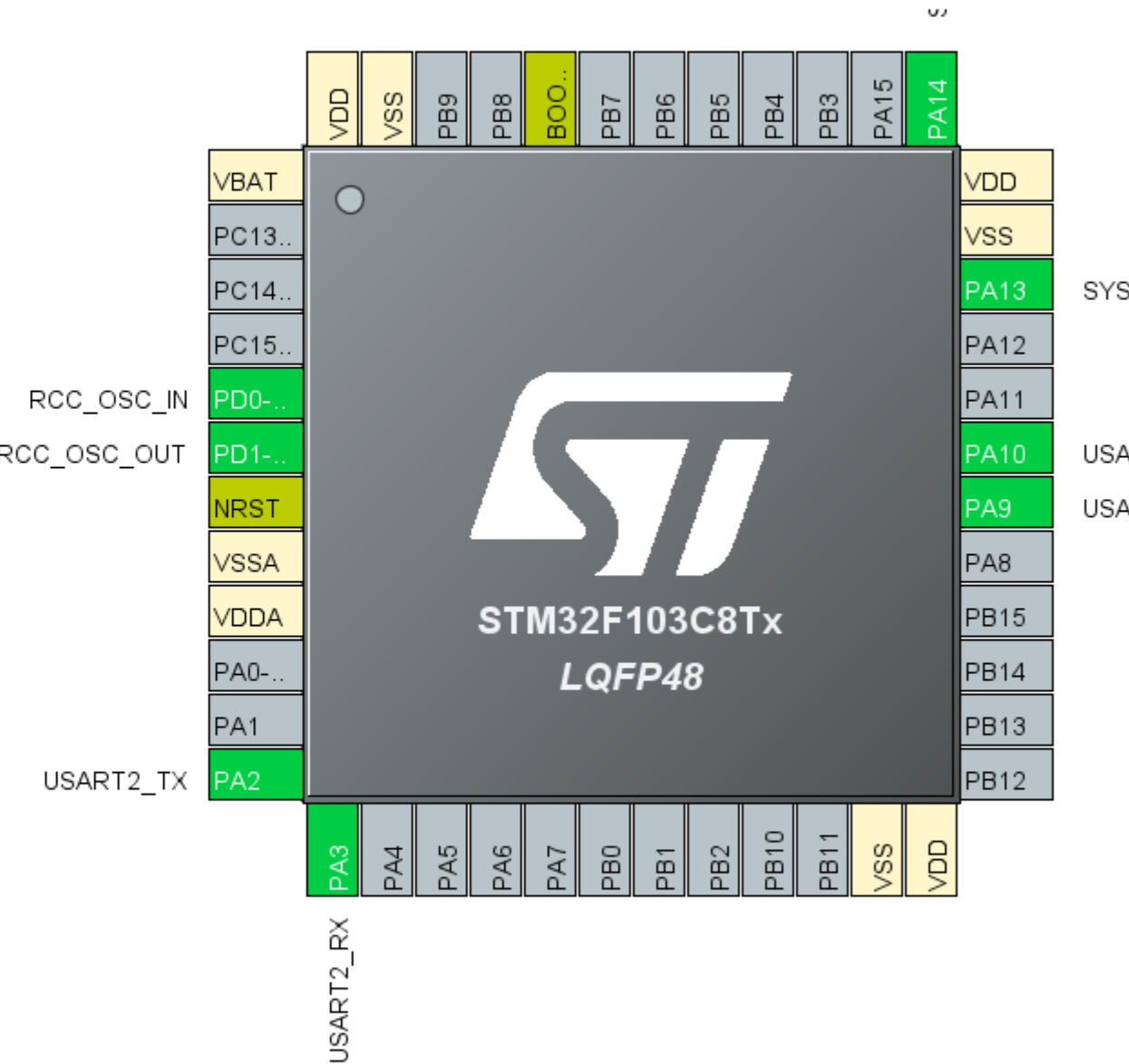
✓ Parameter Settings

✓ User Constants

NVIC Interrupt Table	Enabled	Preemption Priority	Sub Priority
USART2 global interrupt	✓	0	0

Pinout view

System view



Search



Pinout



A->Z

WWDG

### Analog

## Timers

## Connectivity

USB

## Computing

Middleware and Software P...

## USART1 Mode and Configuration

Mode

Mode Asynchronous

Hardware Flow Control (RS232) Disable

## Configuration

## Reset Configuration

✓ NVIC Settings

DMA Settings

GPIO Settings

## VIC Interrupt Table

Enabled

## Preemption Priority

Sub Priority


USART1 global interrupt

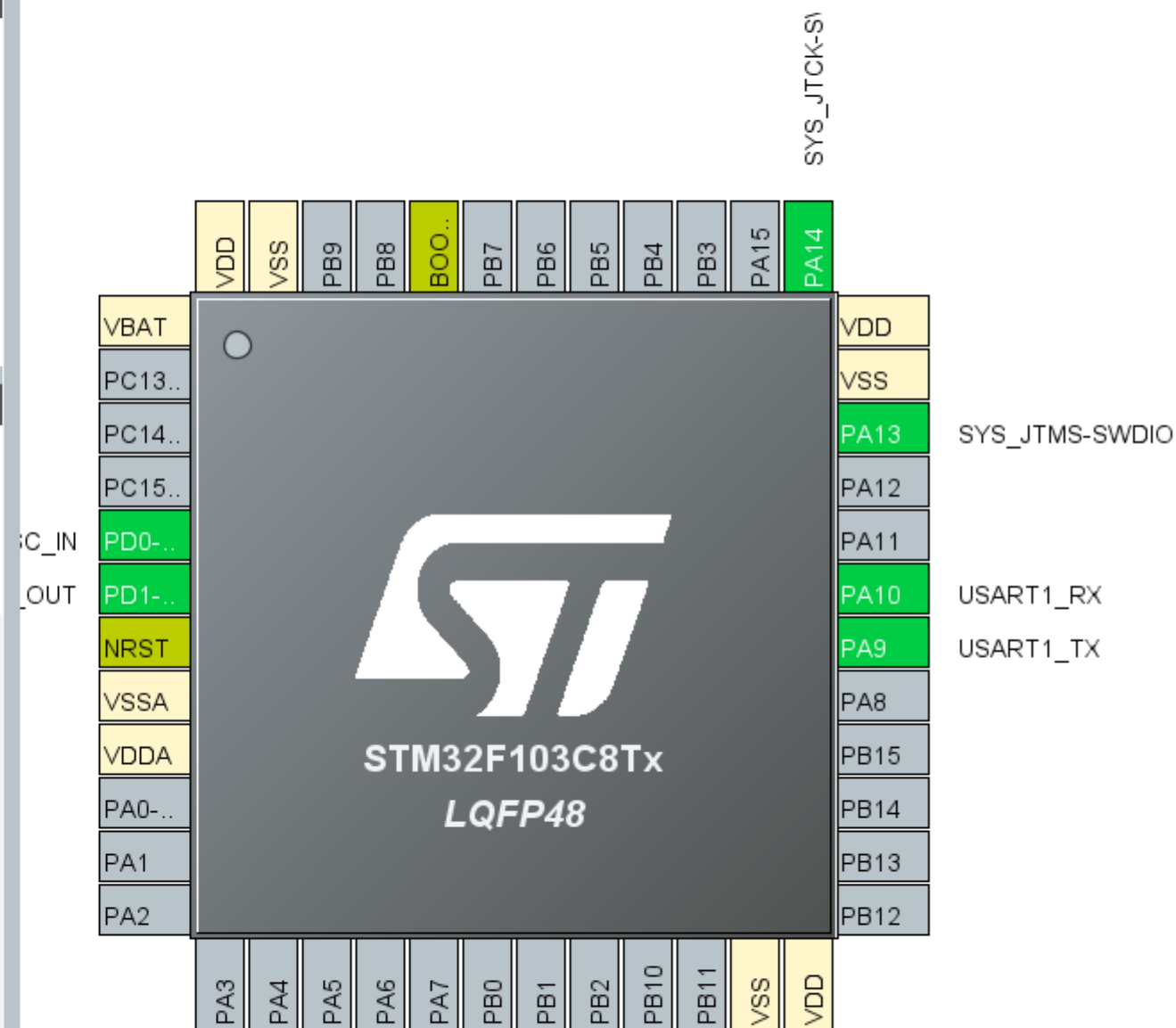


0

0

 Pinout view

 System view



**STM32F103C8Tx**  
**LQFP48**



Pinout & Configuration

Clock Configuration

Project Manager

Tools

Software Packs

Pinout

Categories

A->Z

IWDG

NVIC

✓

RCC

✓

SYS

WWDG

Analog

Timers

Connectivity

CAN

I2C1

I2C2

SPI1

SPI2

✓

USART1

USART2

USART3

USB

Computing

Middleware and Software P...

USART1 Mode and Configuration

Mode

ModeAsynchronous

Hardware Flow Control (RS232)Disable

Configuration

Reset Configuration

✓

 NVIC Settings

✓

 DMA Settings

✓

 GPIO Settings

✓

 Parameter Settings

✓

 User Constants

Configure the below parameters :

Search (Ctrl+F)

Basic Parameters

Baud Rate115200 Bits/s

Word Length8 Bits (including Parity)

ParityNone

Stop Bits1

Advanced Parameters

Data DirectionReceive and Transmit

Over Sampling16 Samples

Pinout view

System view

VDD

VSS

PB9

PB8

BOOT

PB7

PB6

PB5

PB4

PB3

PA15

PA14

PA13

PA12

PA11

PA10

PA9

PA8

PB15

PB14

PB13

PB12

VSS

VDD

PA3

PA4

PA5

PA6

PA7

PB0

PB1

PB2

PB10

PB11

VSS

VDD

PC13..

PC14..

PC15..

PD0..

PD1..

NRST

VSSA

VDDA

PA0..

PA1

PA2

VBAT

PC13..

PC14..

PC15..

PD0..

PD1..

NRST

VSSA

VDDA

PA0..

PA1

PA2

PA3

PA4

PA5

PA6

PA7

PB0

PB1

PB2

PB10

PB11

VSS

VDD

SYS\_JTCK-SWCLK

SYS\_JTMS-SWDIO

USART1\_RX

USART1\_TX

STM32F103C8Tx

LQFP48

SYS Mode and Configuration

Mode

Debug

Serial Wire

▼


☐ System Wake-Up

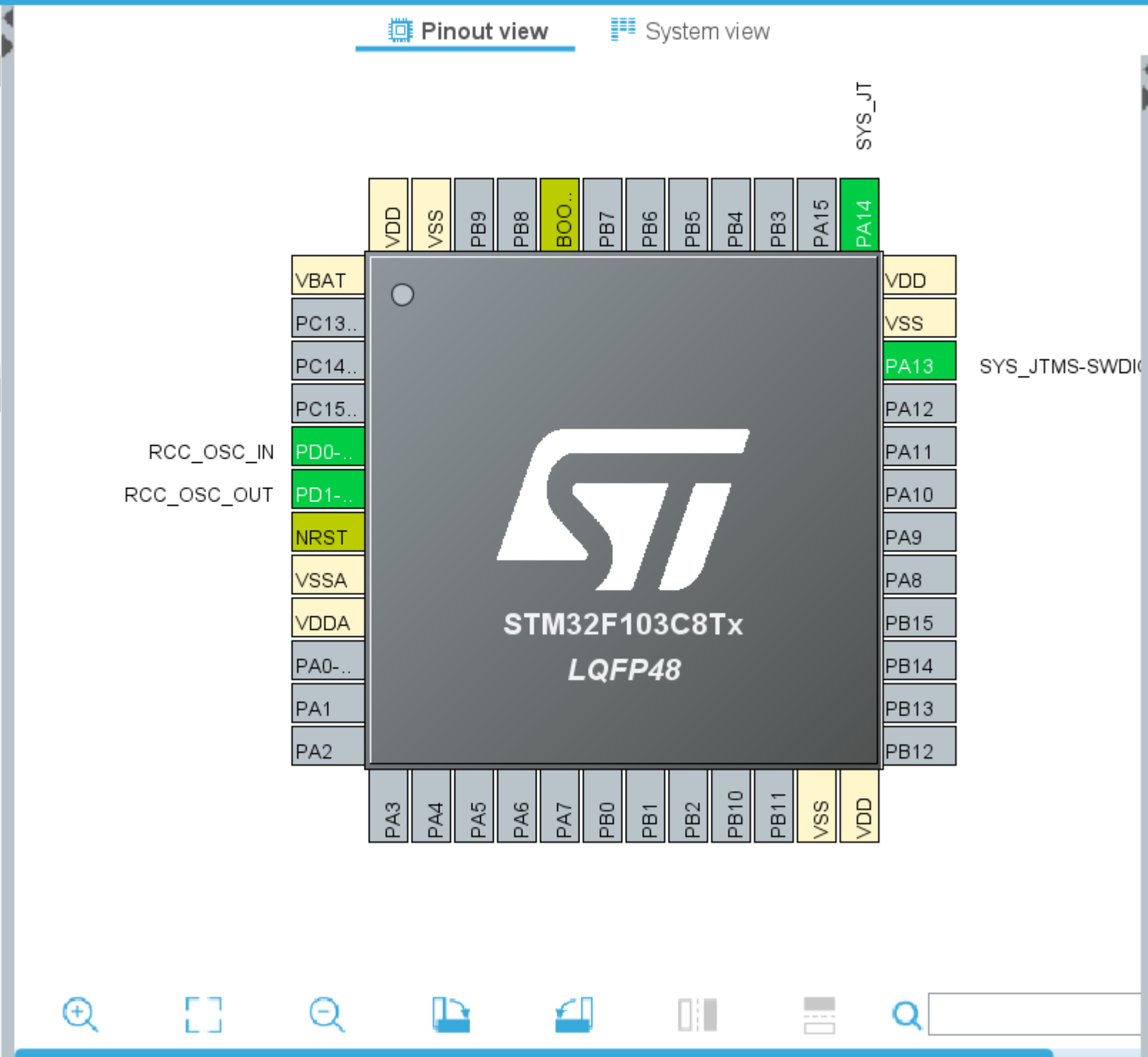
Timebase Source

SysTick

▼

Configuration

 **Warning:** This peripheral has no parameters to be configured.



Pinout & Configuration

Clock Configuration

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Categories

A->Z

System Core

DMA

GPIO

IWDG

NVIC

RCC

SYS

WWDG

Analog

Timers

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Computing

Middleware and Software Pac...

RCC Mode and Configuration

Mode

High Speed Clock (HSE)Crystal/Ceramic Resonator

Low Speed Clock (LSE)Disable

☐ Master Clock Output

Configuration

Reset Configuration

Parameter Settings

User Constants

NVIC Settings

GPIO Settings

Configure the below parameters :

Search (Ctrl+F)

System Parameters

VDD voltage (V)3.3 V

Prefetch BufferEnabled

Flash Latency(WS)0 WS (1 CPU cycle)

RCC Parameters

HSI Calibration Value16

HSE Startup Timeout Value (ms)100

LSE Startup Timeout Value (ms)5000

Pinout view

System view

RCC\_OSC\_IN

RCC\_OSC\_OUT

VDD

VSS

PB9

PB8

BOOT0

PB7

PB6

PB5

PB4

PB3

PA15

PA14

VBAT

PC13..

PC14..

PC15..

PD0..

PD1..

NRST

VSSA

VDDA

PA0-..

PA1

PA2

PA3

PA4

PA5

PA6

PA7

PB0

PB1

PB2

PB10

PB11

VSS

VDD

VDD

VSS

PA13

PA12

PA11

PA10

PA9

PA8

PB15

PB14

PB13

PB12

## Project

Project Name: 6\_DOF\_Robotic\_Hand\_Master

☐ Use default location

Location: E:\STM32 Project Folder 2\6\_DOF\_Robotic\_Hand\_Master

Browse...

## Options

## Targeted Language

☒ C ☐ C++

## Targeted Binary Type





☒ Executable ☐ Static Library

## Targeted Project Type




☒ STM32Cube ☐ Empty

MCU/MPU Selector Board Selector Example Selector Cross Selector

MCU/MPU Filters



Commercial Part Number

PRODUCT INFO

Segment

Series

Line

Marketing Status

Price

Package

Core

Coprocessor

MEMORY


Flash = 64 (kBytes)

64

EEPROM = 64 (kBytes)

Features Block Diagram Docs &amp; Resources CAD Resources Datasheet Buy

## STM32F1 Series




STM32F103C8T6

Mainstream Performance line, Arm Cortex-M3 MCU with 64 Kbytes of Flash memory, 72 MHz CPU, motor control, USB and CAN

**ACTIVE**  
Product is in mass production

Unit Price for 10kU (US\$) : **2.7946**

 LQFP 48 7x7x1.4 mm

The STM32F103xx medium-density performance line family incorporates the high-performance Arm<sup>®</sup> Cortex<sup>®</sup>-M3 32-bit RISC core operating at a 72 MHz frequency, high-speed embedded memories (Flash memory up to 128 Kbytes and SRAM up to 20 Kbytes), and an extensive range of enhanced I/Os and peripherals connected to two APB buses. All devices offer two 12-bit ADCs, three general purpose 16-bit timers plus one PWM timer, as well as standard and

MCUs/MPUs List: 2 items

 Export

*	Commercia...	Part No	Reference	Marketing St...	Unit Price fo...	Board	Package	Flash	RAM	I/O	Frequency
☆	STM32F103C...	STM32F103C8	STM32F103C...	Active	2.7946		LQFP 48 7x7x...	64 kBytes	20 kBytes	37	72 MHz
☆	STM32F103C...		STM32F103C...	Active	2.7946		LQFP 48 7x7x...	64 kBytes	20 kBytes	37	72 MHz

Pinout & Configuration

Clock Configuration

Project Manager

Tools

Software Packs

Pinout

Search



Categories A->Z

System Core

Analog

Timers

Connectivity

- CAN
- ✓ I2C1
- I2C2
- SPI1
- SPI2
- ✓ USART1
- USART2
- USART3
- USB

Computing

Middleware and Software Pac...

USART1 Mode and Configuration

Mode

Mode Asynchronous

Hardware Flow Control (RS232) Disable

Configuration

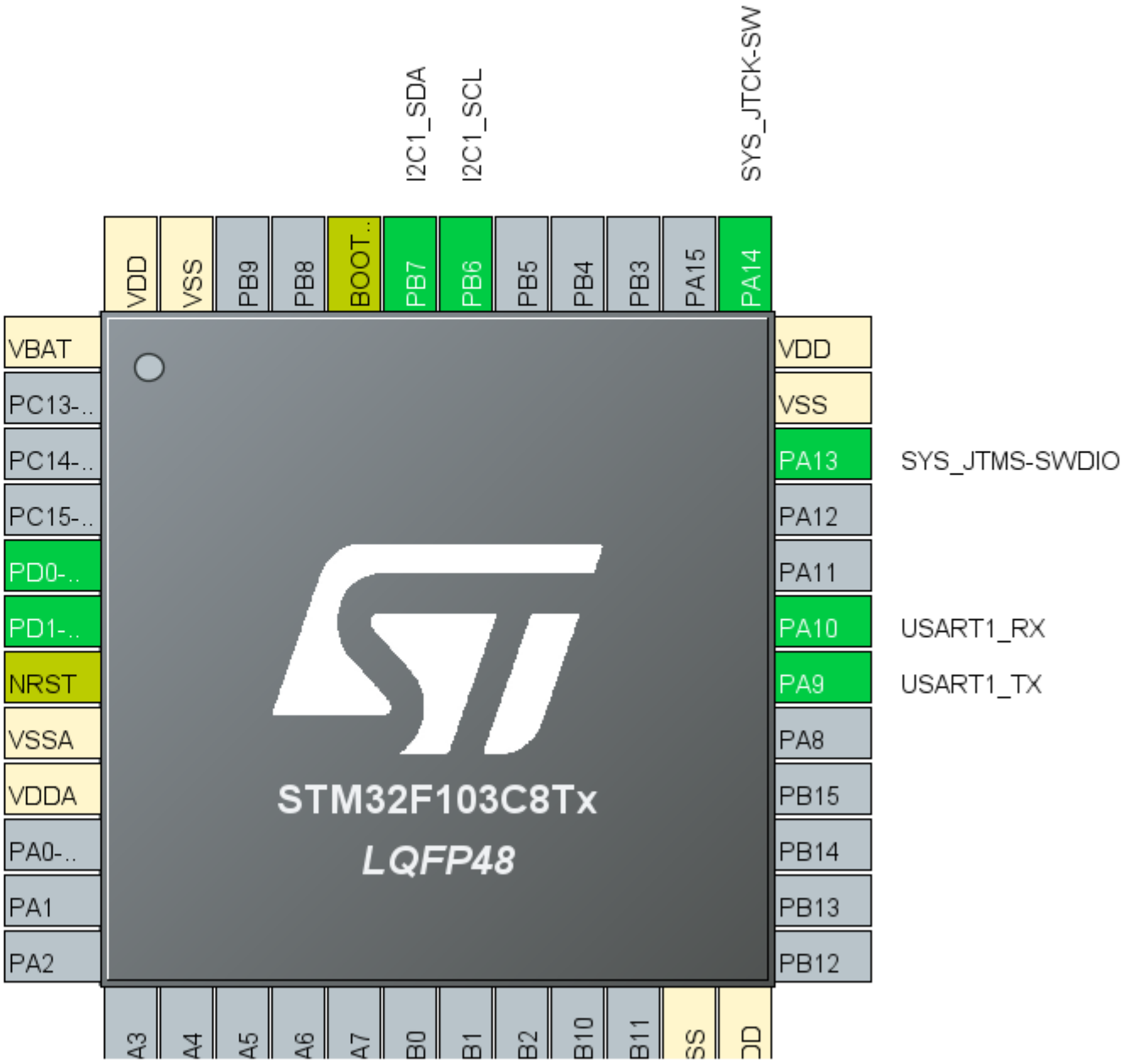
Reset Configuration

- ✓ NVIC Settings
- ✓ DMA Settings
- ✓ GPIO Settings
- ✓ Parameter Settings
- ✓ User Constants

NVIC Interrupt Table	Enabled	Preemption Priority	Sub Priority
USART1 global interrupt	✓	0	0

Pinout view

System view





Pinout view

System view

STM32F103C8Tx LQFP48

Top pins: VDD, VSS, PB9, PB8, BOOT0, PB7, PB6, PB5, PB4, PB3, PA15, PA14

Right pins: VDD, VSS, PA13 (SYS\_JTMS-SWDIO), PA12, PA11, PA10 (USART1\_RX), PA9 (USART1\_TX), PA8, PB15, PB14, PB13, PB12

Bottom pins: A3, A4, A5, A6, A7, B0, B1, B2, B10, B11, SS, DD

Left pins: VBAT, PC13, PC14, PC15, PD0, PD1, NRST, VSSA, VDDA, PA0, PA1, PA2

## Pinout &amp; Configuration

## Clock Configuration

## Project Manager

## Tools

## Software Packs

## Pinout



## I2C1 Mode and Configuration

## Mode

I2C I2C

## Configuration

Reset Configuration

NVIC Settings

DMA Settings

GPIO Settings

Parameter Settings

User Constants

Configure the below parameters :



## Master Features

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

## Slave Features

Clock No Stretch Mode Disabled

Primary Address Length sele. 7-bit

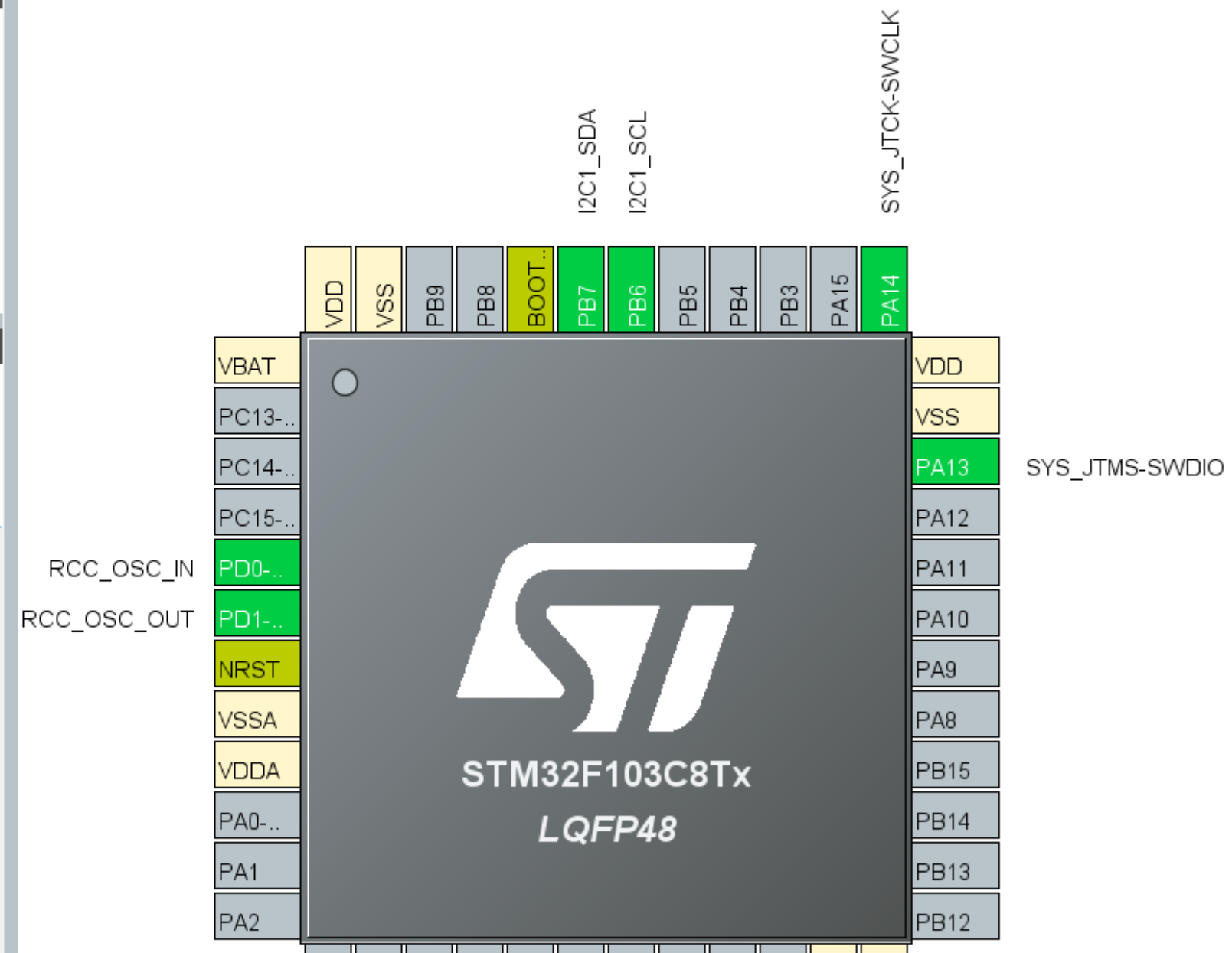
Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detecti.. Disabled

## Pinout view

## System view



## Computing

## Middleware and Software Pac...



# 6\_DOF\_Robotic\_Hand\_Slave.ioc - Clock Configuration

Pinout & Configuration

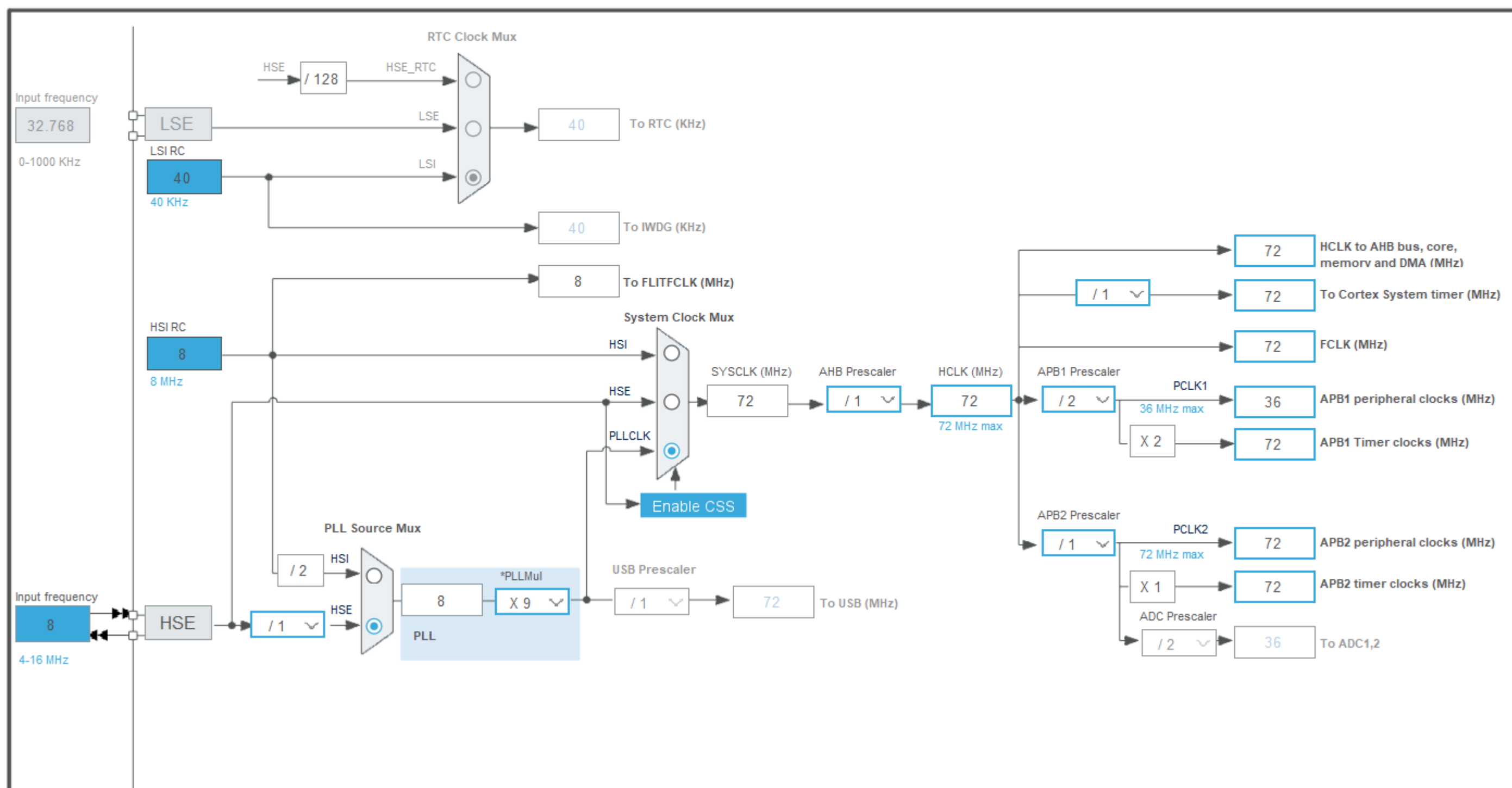
Clock Configuration

Project Manager

Tools



Resolve Clock Issues



Search



Categories A->Z

System Core

- DMA
- GPIO
- IWDG
- NVIC
- ✓ RCC
- ✓ SYS
- WWDG

Analog

Timers

Connectivity

Computing

Middleware and Software Pac...

SYS Mode and Configuration

Mode

Debug Serial Wire

☐ System Wake-Up

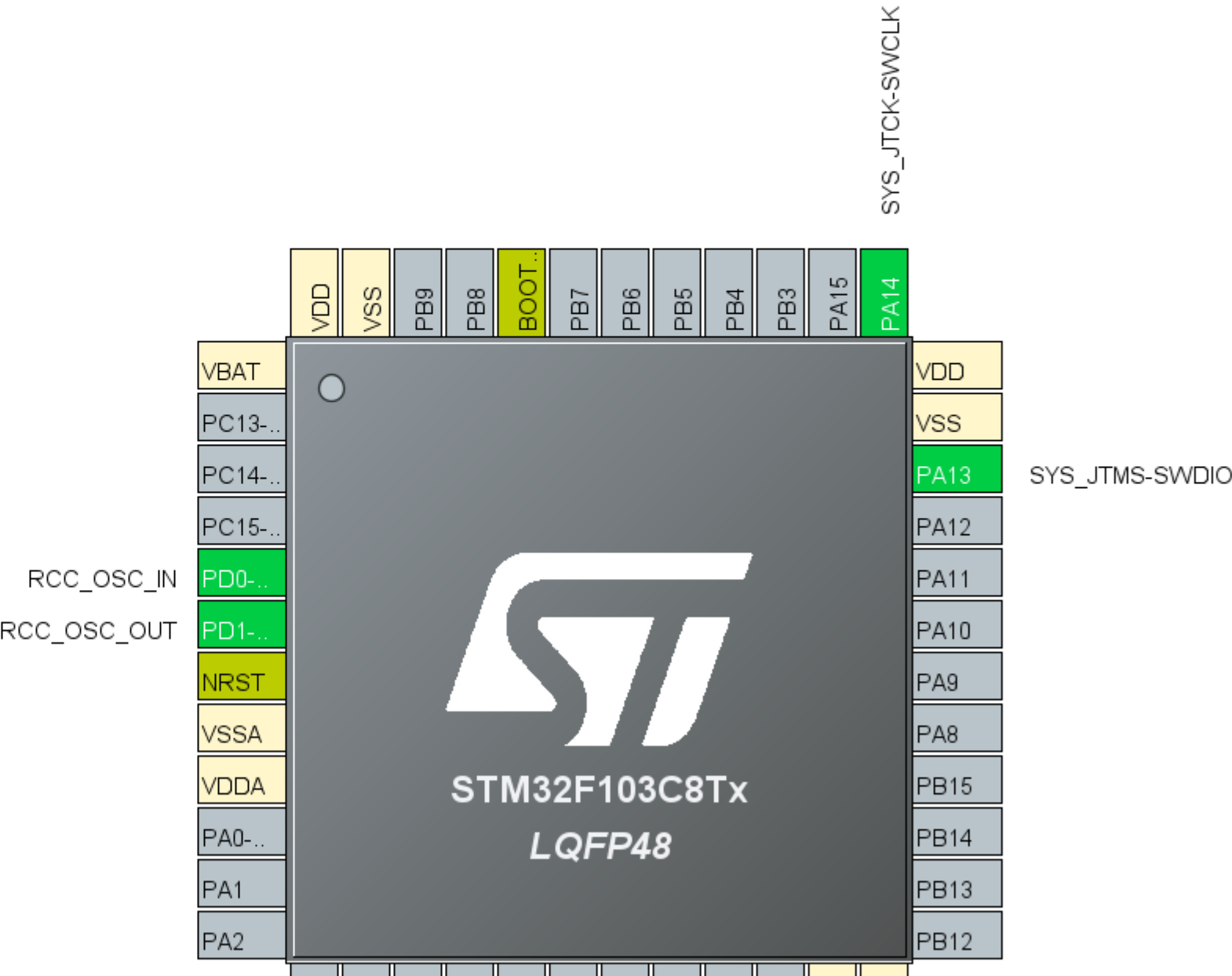
Timebase Source SysTick

Configuration

Warning: This peripheral has no parameters to be configured.

Pinout view

System view



Search



A → Z

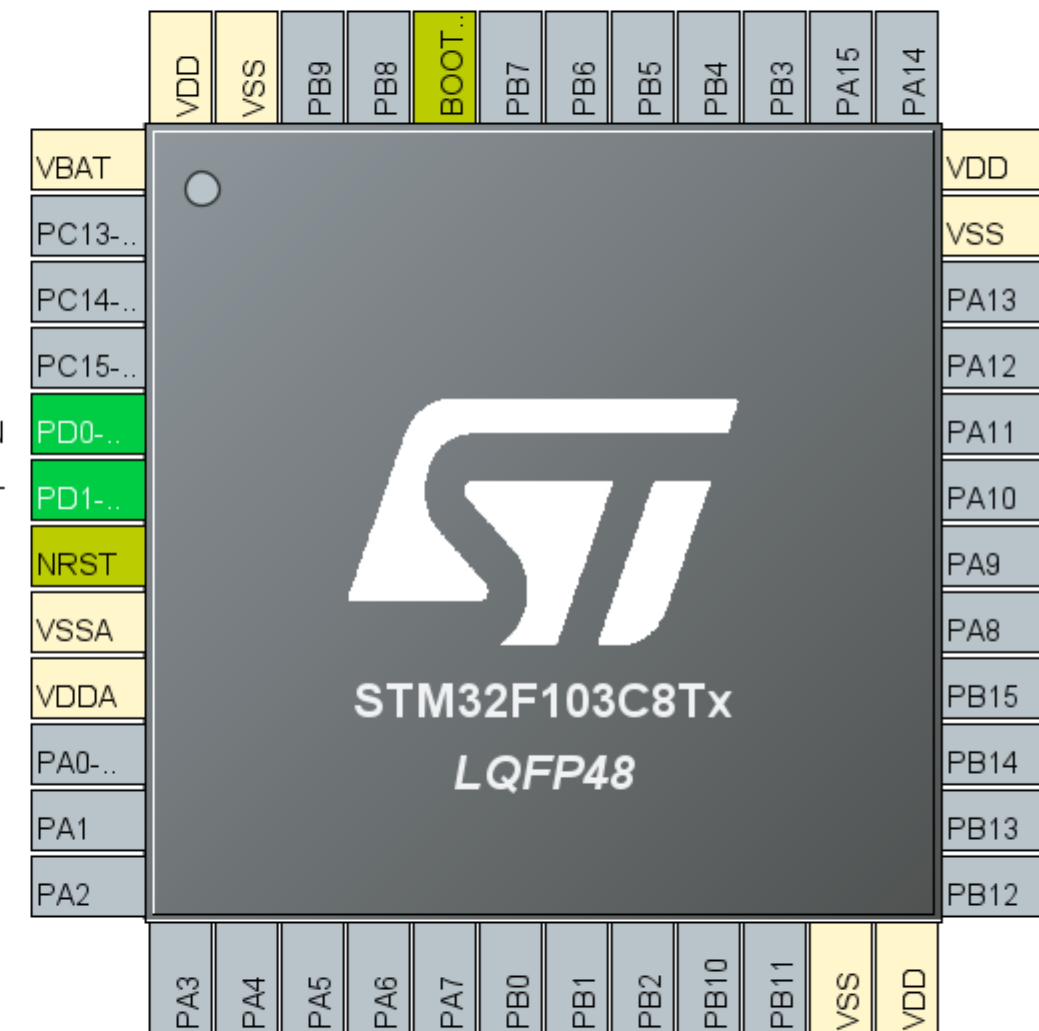
WWDG

 $\gamma$ 

2

 $\gamma$  $\gamma$ 

16



## Project

Project Name: 6\_DOF\_Robotic\_Hand\_Slave

☐ Use default location

Location: E:\STM32 Project Folder 2\6\_DOF\_Robotic\_Hand\_Bluetooth\_Controlled\_001

Browse...

## Options

## Targeted Language

☒ C ☐ C++

## Targeted Binary Type

☒ Executable ☐ Static Library

## Targeted Project Type

☒ STM32Cube ☐ Empty

MCU/MPU SelectorBoard SelectorExample SelectorCross Selector

MCU/MPU Filters

★

Commercial Part Number

STM32F103C8T6

+

-

PRODUCT INFO

Segment

>

Series

>

Line

>

Marketing Status

>

Price

>

Package

>

Core

>

Coprocessor

>

MEMORY

Flash = 64 (kBytes)

64

EEPROM = 0 (Bytes)

Features

Block Diagram

Docs & Resources

CAD Resources

Datasheet

Buy

★

STM32F1 Series

STM32F103C8T6

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