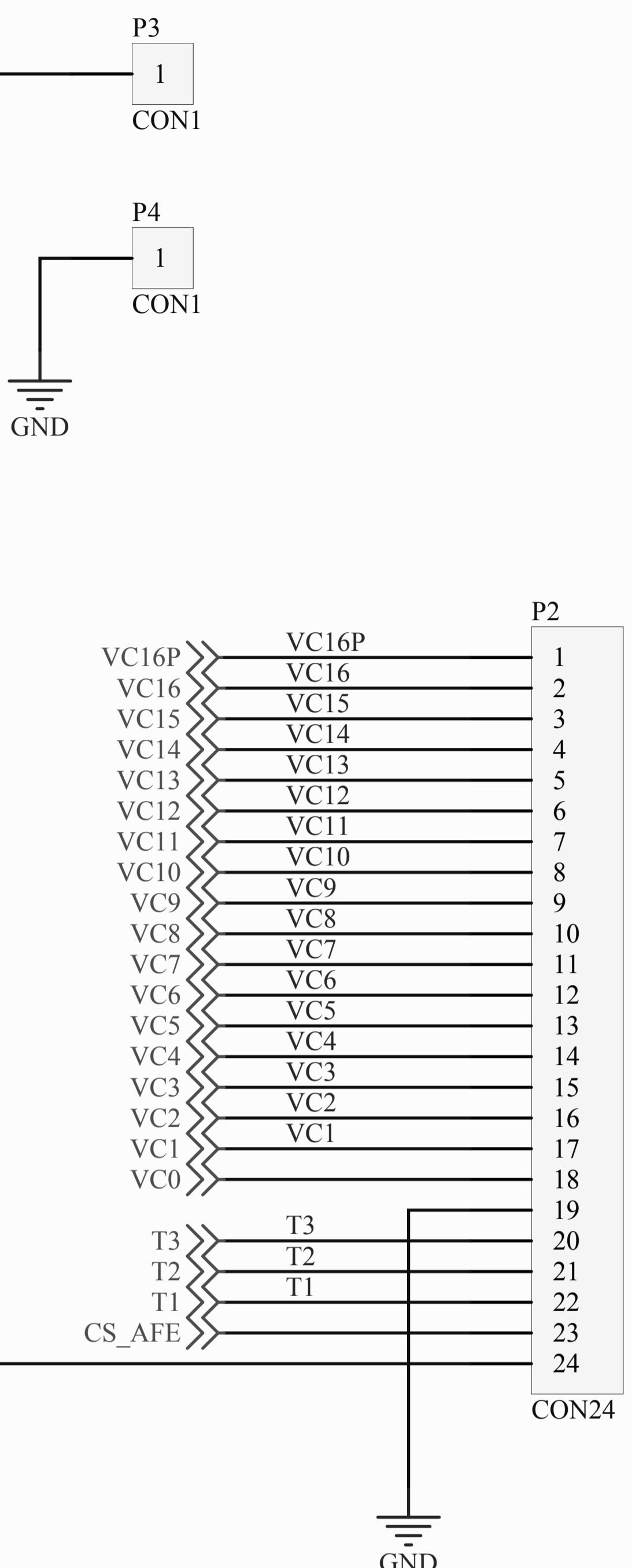
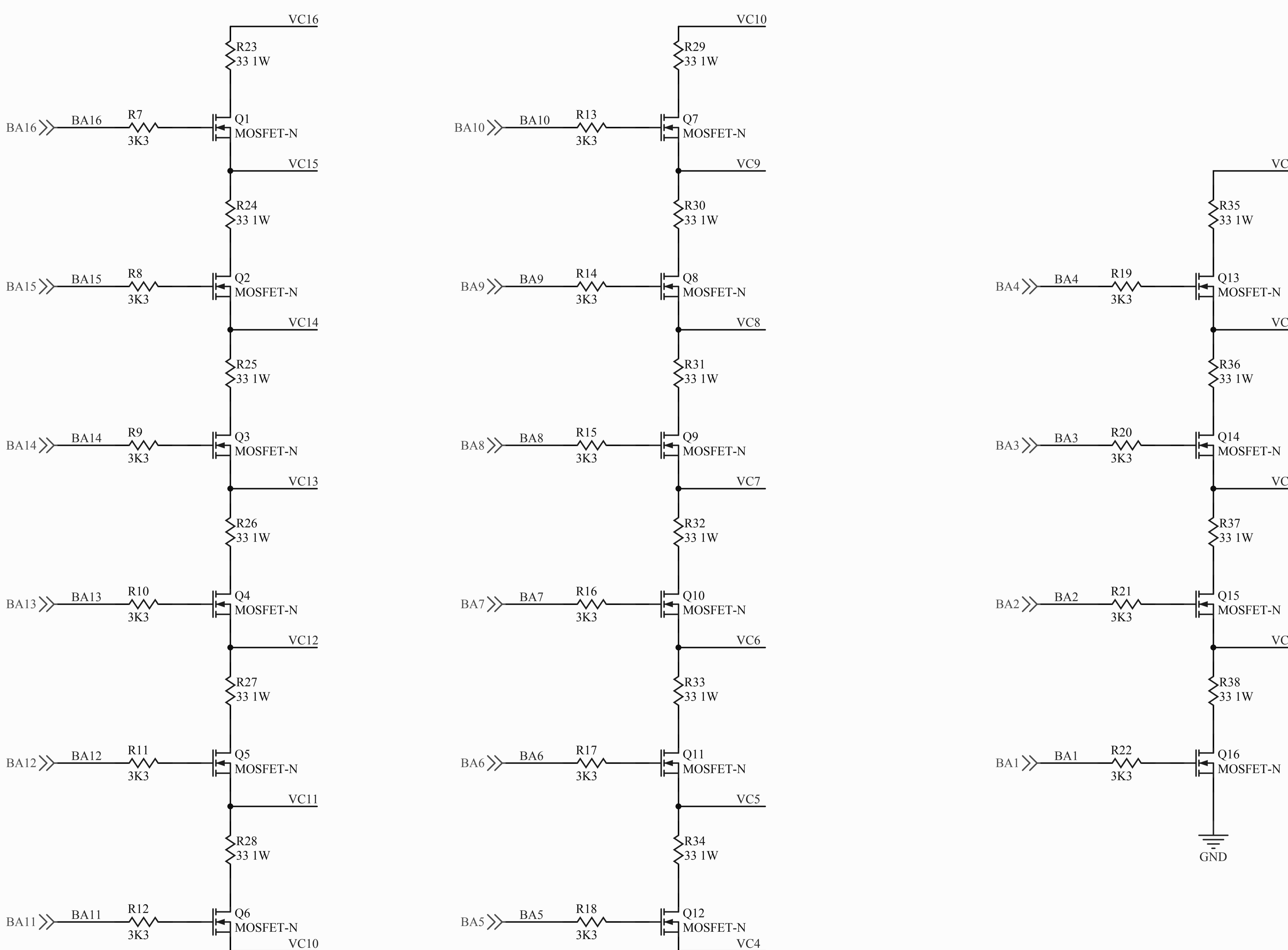


A



NOTE: REV 1.1

Title		
Size	Number	Revision
B		
Date: 8/30/2012	Sheet of	
File: C:\Users\MAX14921 AK35 B BATT.SchDw	Drawn By:	

A

A

B

B

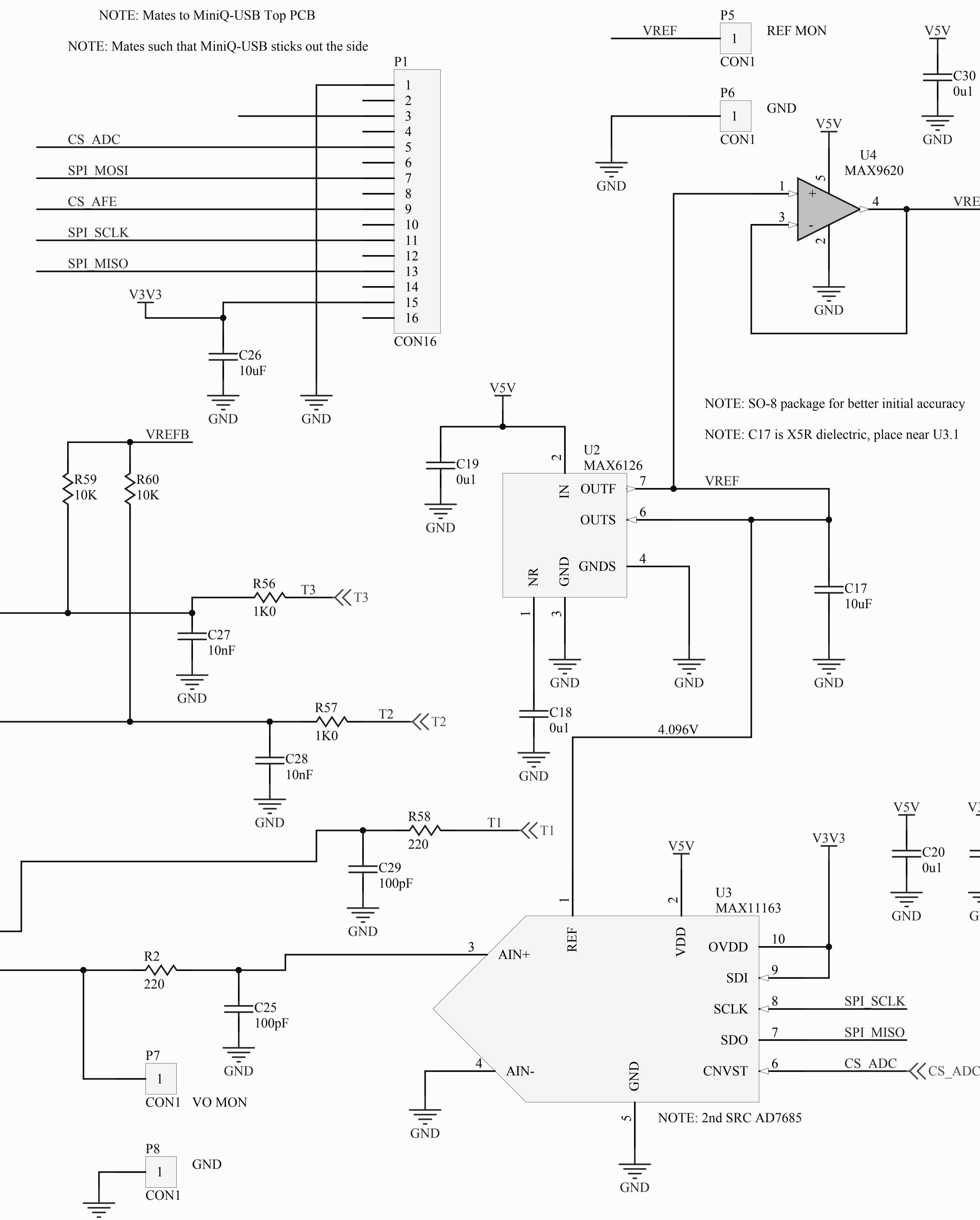
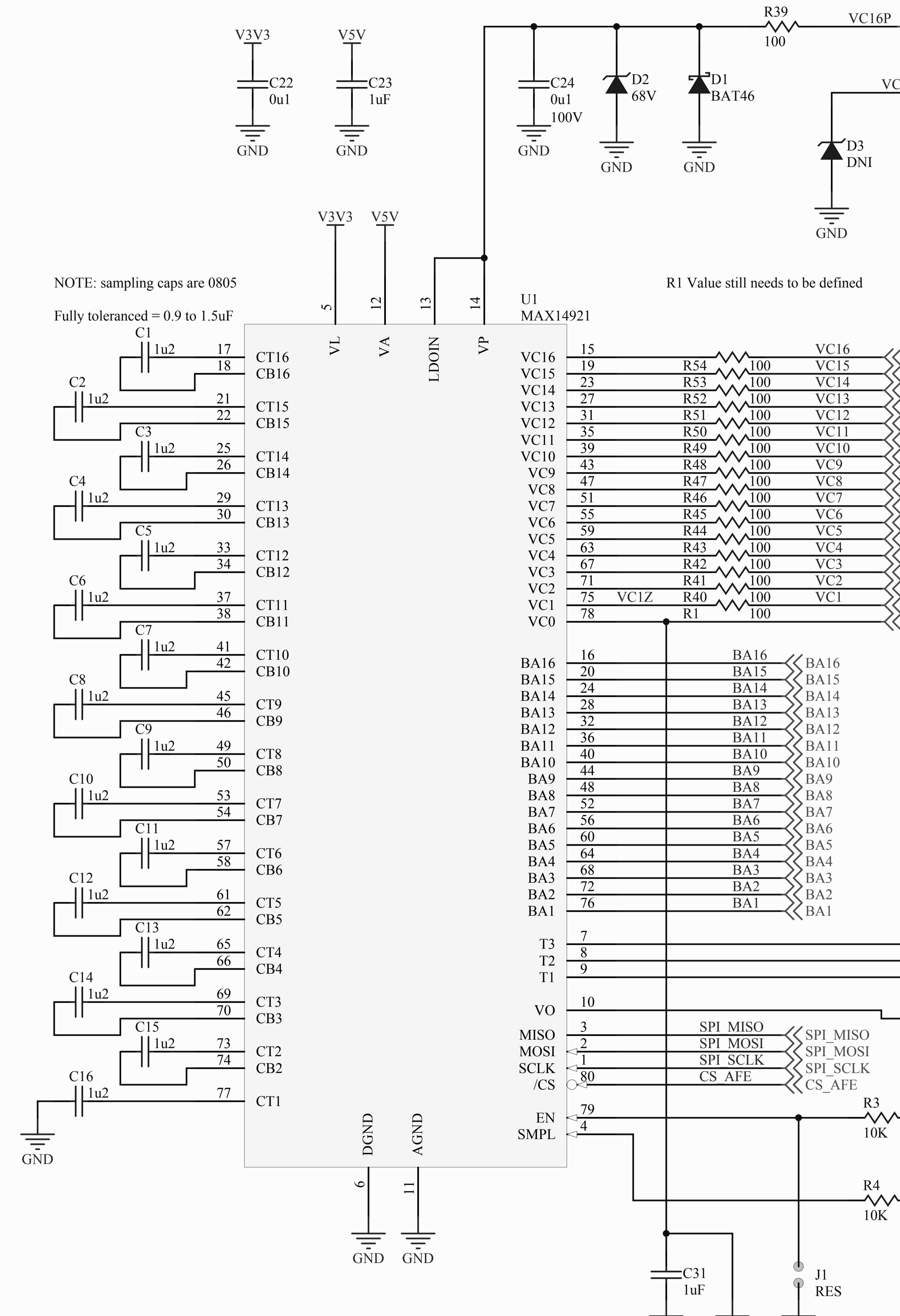
C

C

D

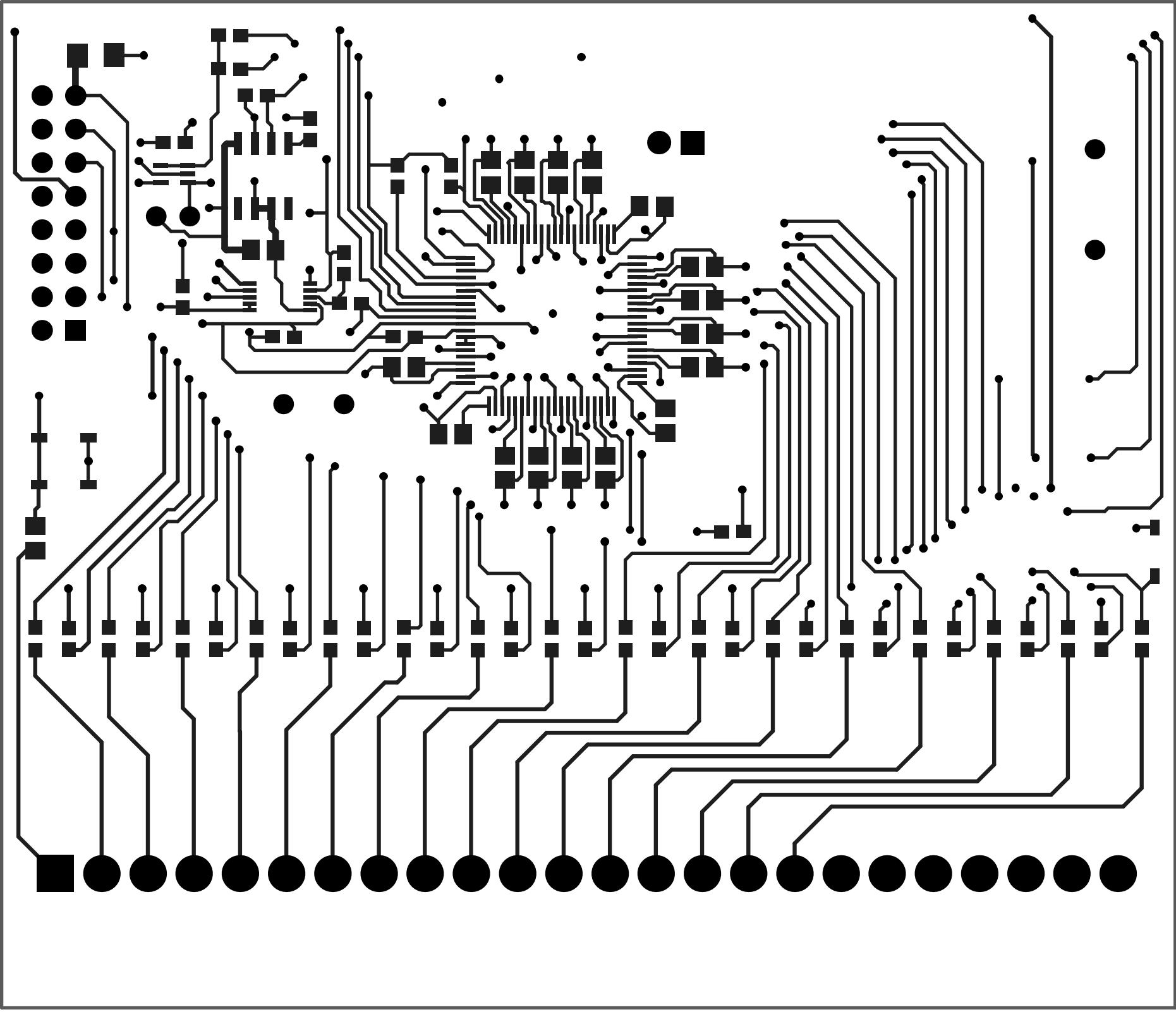
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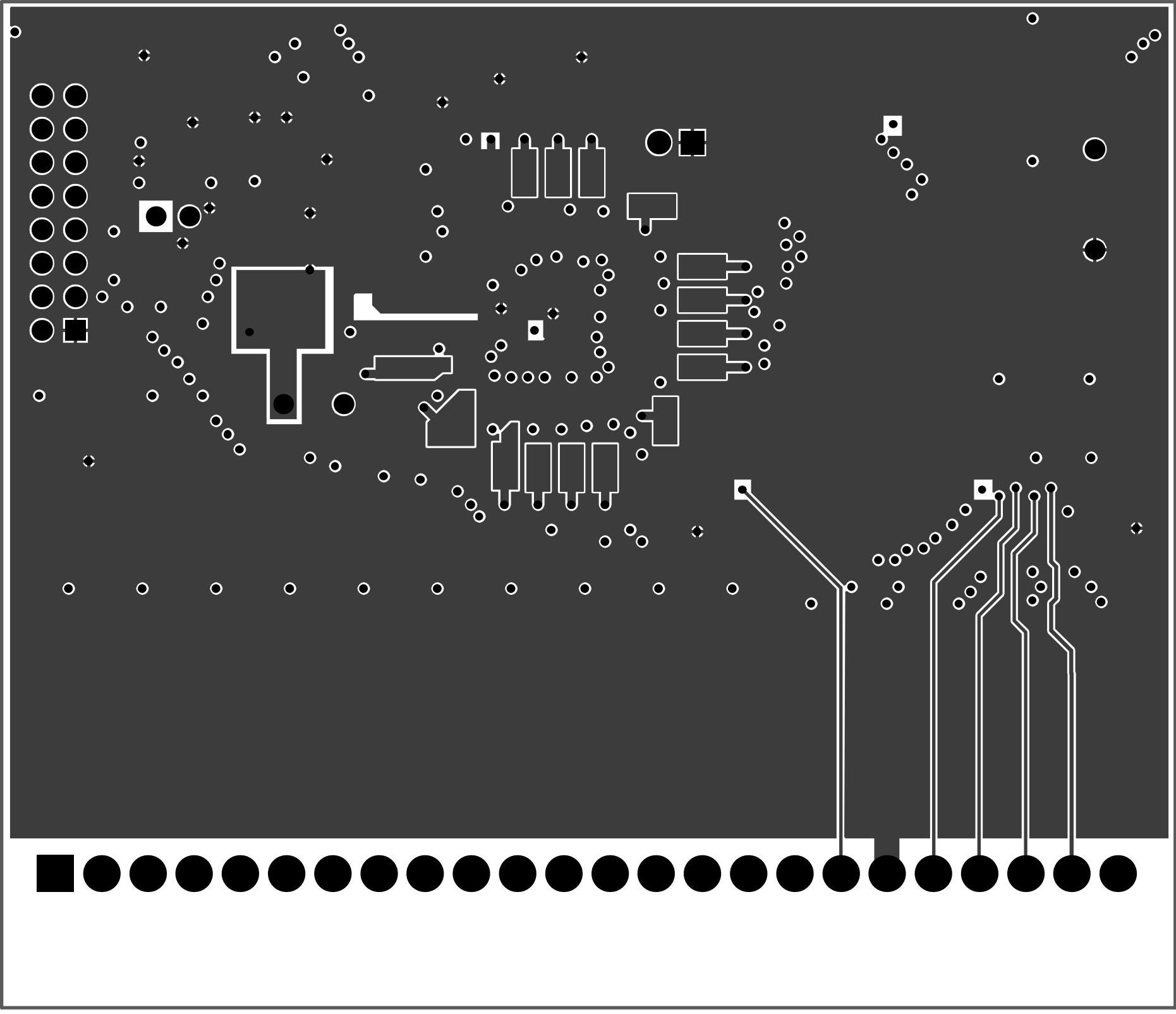
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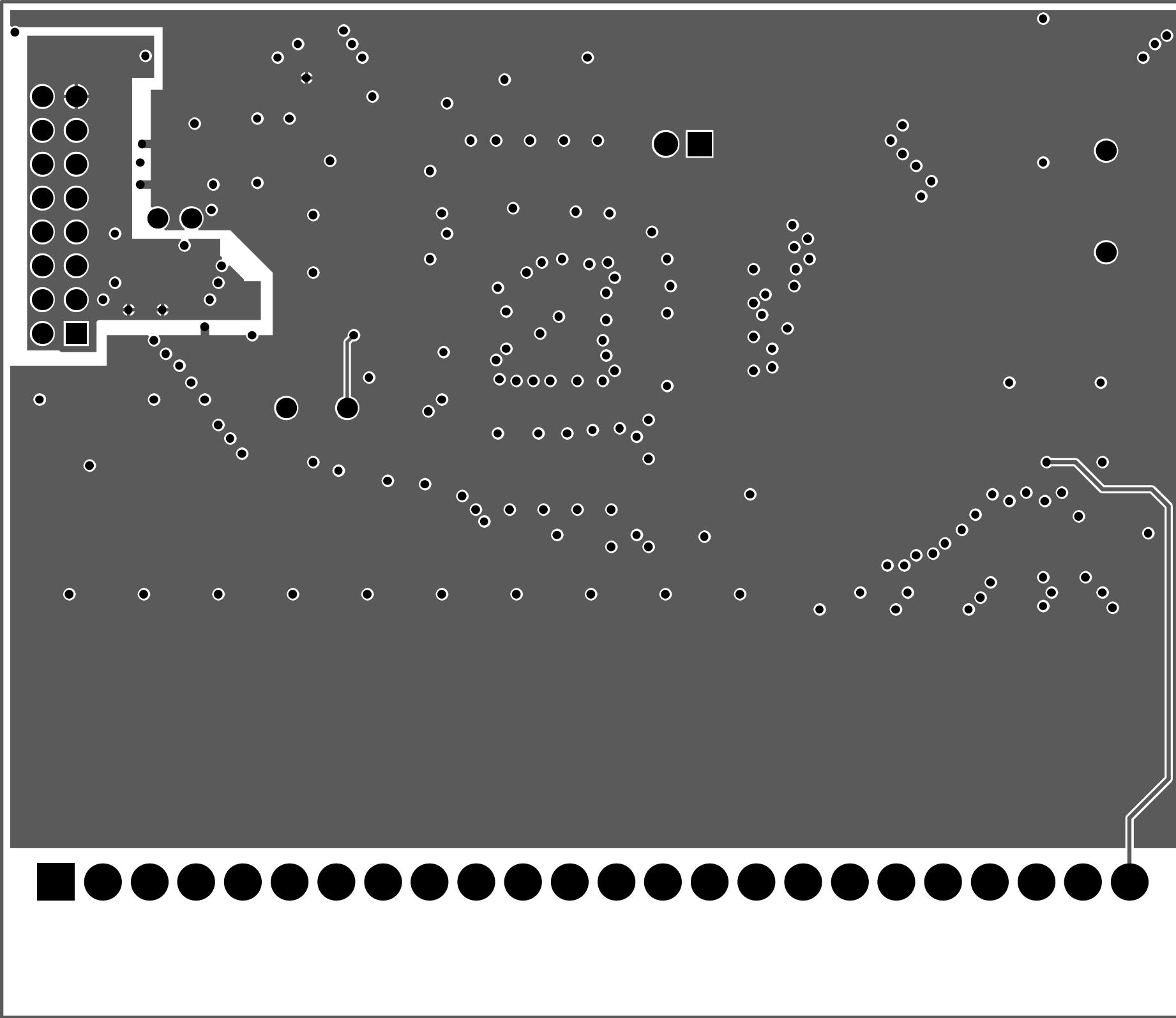


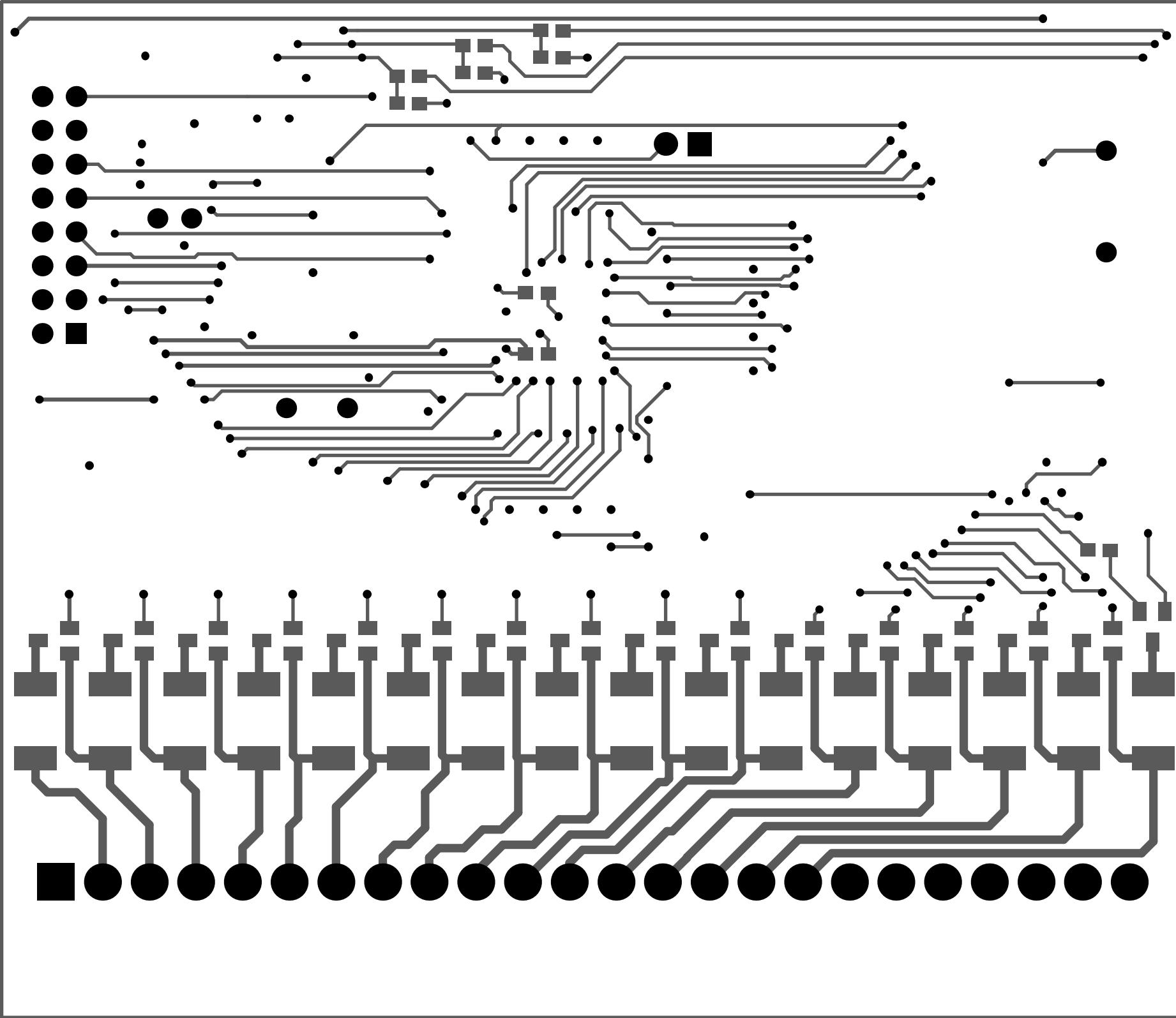
NOTE: ROBERTO TO VERIFY SMPL PIN HIGH FOR SPI CONTROL OF SAMPLING

Title		
Size	Number	Revision
B		
Date: 8/30/2012		Sheet of
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MAXIM INTEGRATED PRODUCTS

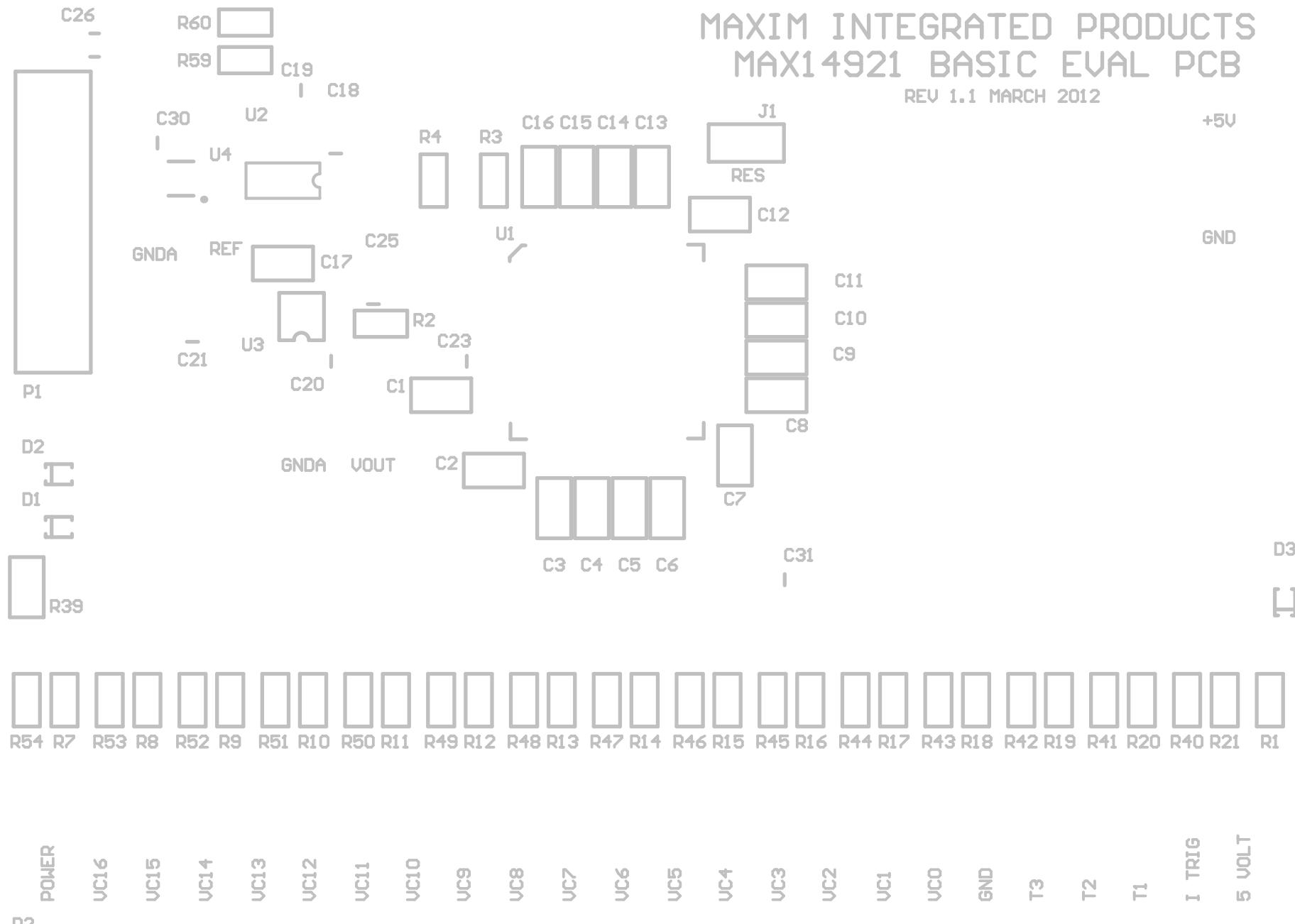
MAX14921 BASIC EVAL PCB

REV 1.1 MARCH 2012

+5V

GND

D3



T1

I TRIG

5 VOLT

T2

T3

GND

UC0

UC1

UC2

UC3

UC4

UC5

UC6

UC7

UC8

UC9

UC10

UC11

UC12

UC13

UC14

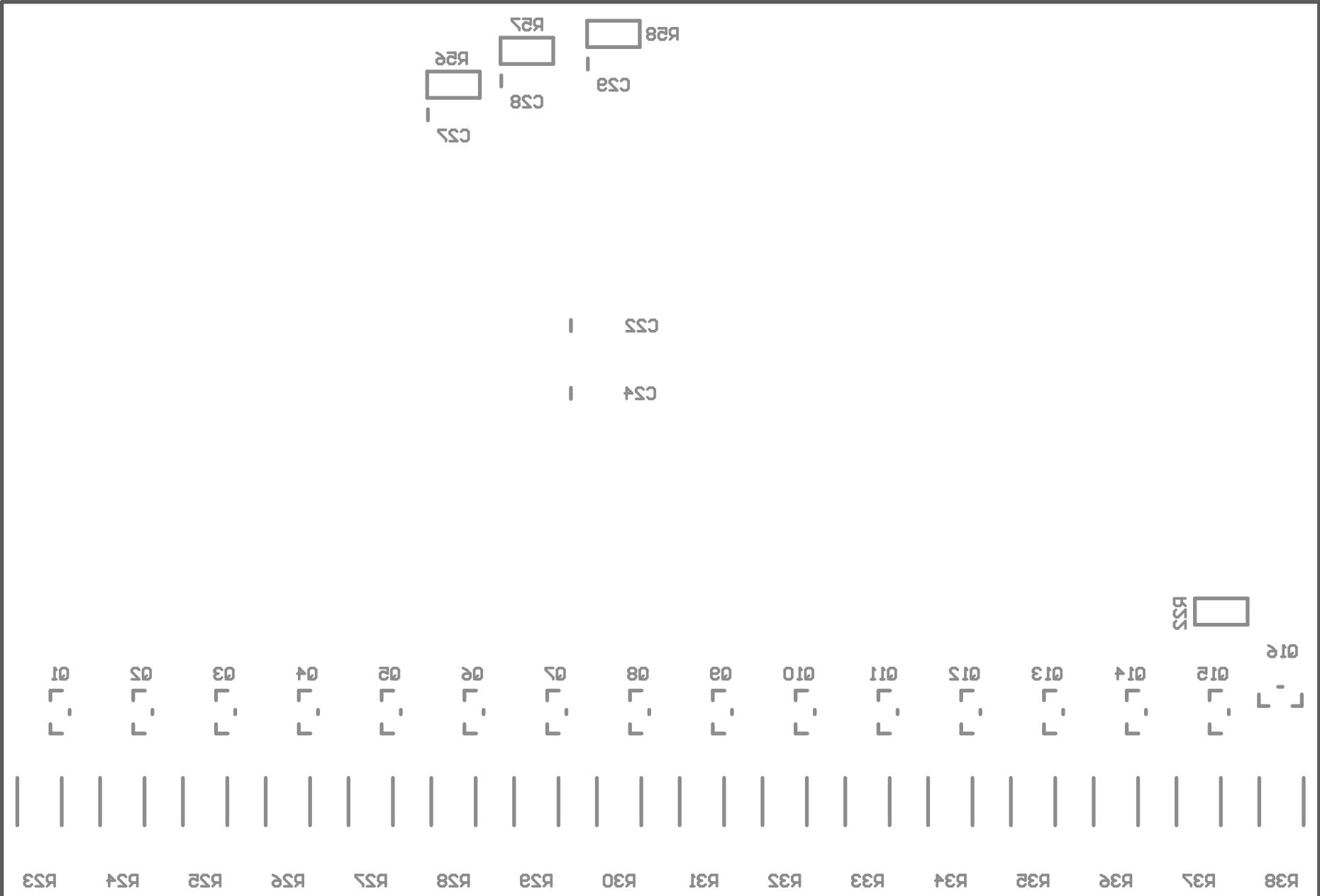
VCL6

UC15

UC16

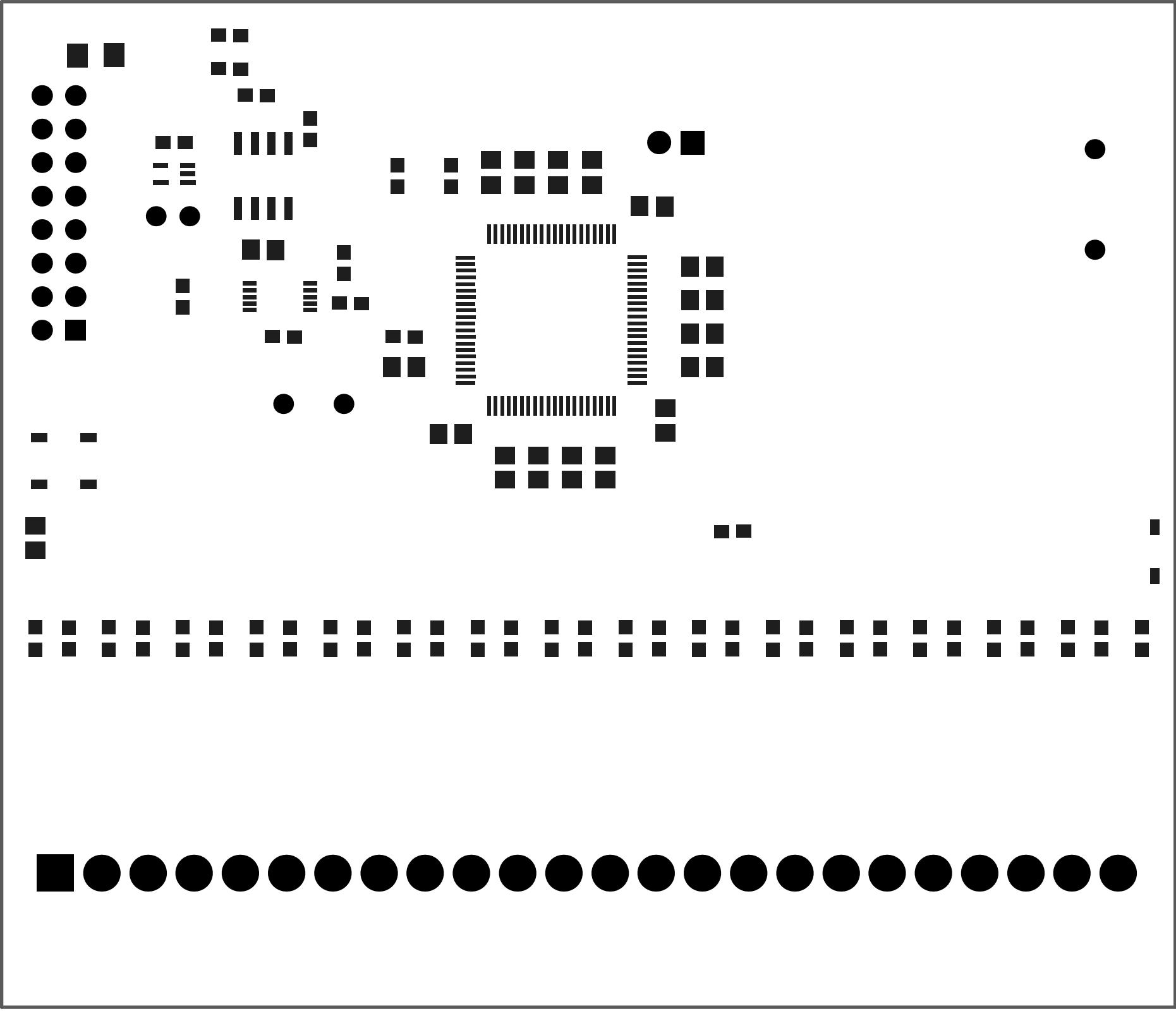
POWER

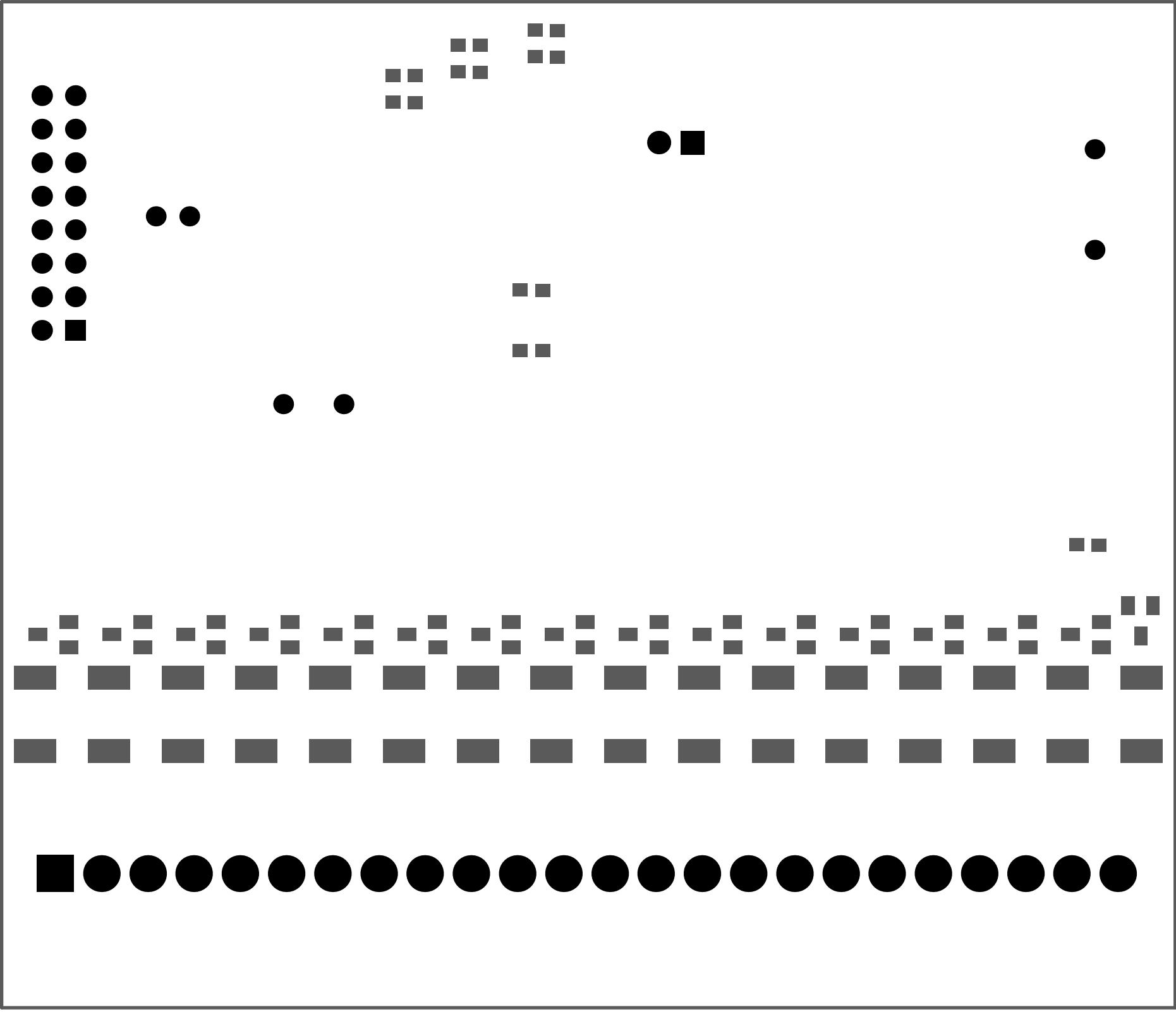
P2



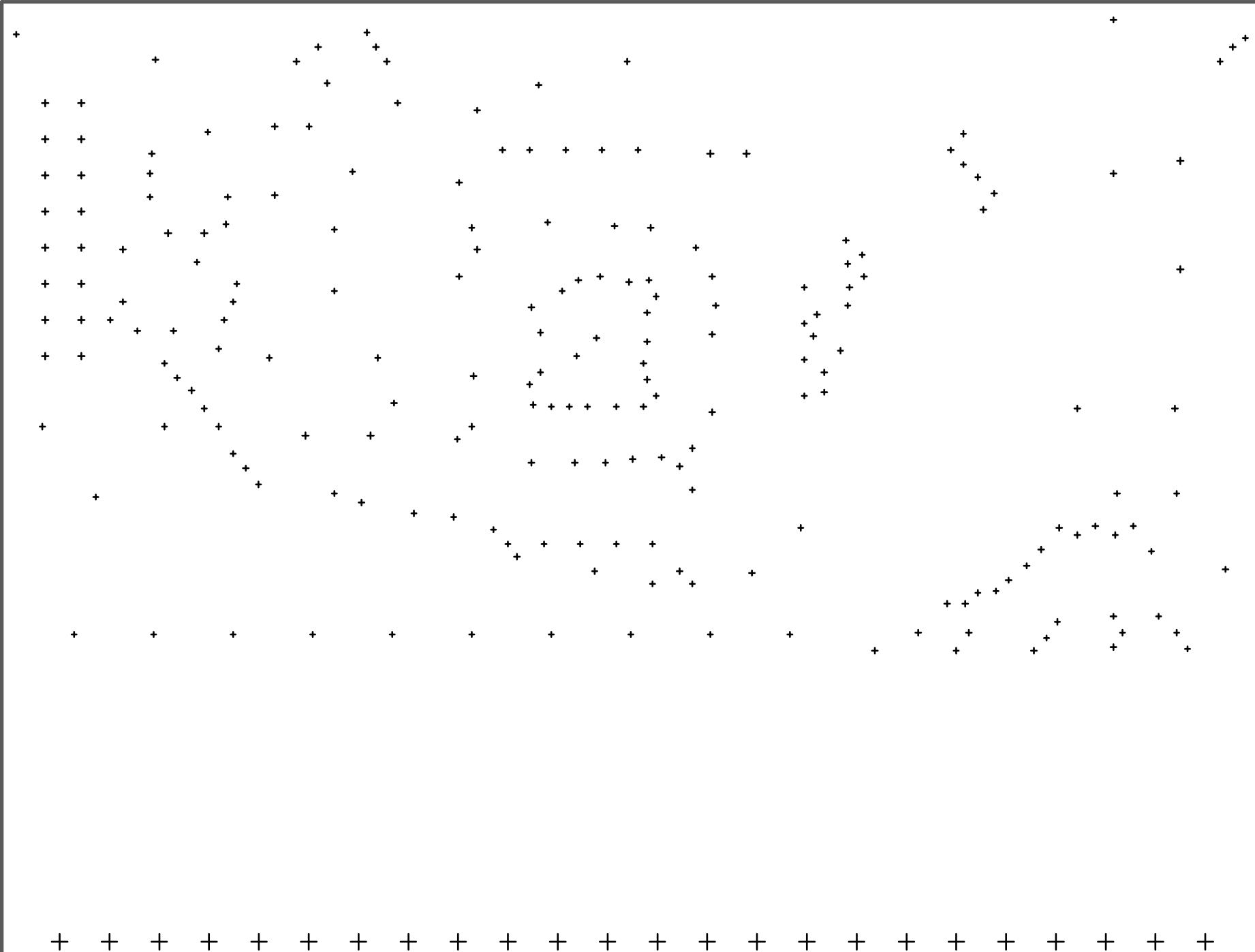












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