

A.2 ESD and EMI Codesign – a Topic Both for IC and PCB Designers

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ESD protection concepts and EMI sensitivity have a close interaction, as improving ESD robustness of the IO typically lowers the impedance of the IO circuit under interference conditions when for example diodes get forward biased. Also very aggressive ESD power clamps can fire at EMI related supply noise leading to a soft fail of the system. In extreme cases, EMI events will even cause latch-up. These design challenges provide enough motivation to select ESD/EMI codesign for an interactive EOS/ESD Symposium workshop, where experience and questions will be shared. Please join, learn, and contribute!