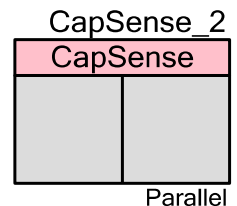
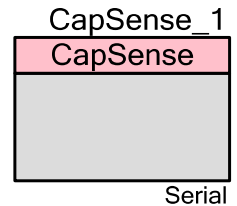


# Capacitive Sensing (CapSense®)

1.30

## Features

- Supports different combinations of independent and slider capacitive sensors
- Exhibits high immunity to AC mains noise, EMC noise, and power supply voltage changes
- Supports parallel (synchronized or asynchronous) and serial scanning configurations
- Supports shield electrodes for reliable operation in the presence of water film or droplets
- Gives guided slot and terminal assignments using the CapSense Configure dialog
- Does not support PSoC 3 ES3 silicon; use CapSense\_CSD Version 2.0 instead



## General Description

The Capacitive Sensing (CapSense) component provides a versatile and efficient way to measure capacitance in applications such as touch-sensing buttons, sliders, and proximity detectors.

## When to use a CapSense Component

Capacitance sensing systems can be used in many applications in place of conventional buttons, switches, and other controls, even in applications that are exposed to rain or water. Such applications include ATMs, automotive systems, outdoor equipment, public access systems, portable devices such as cell phones and PDAs, and kitchen and bathroom home appliances.

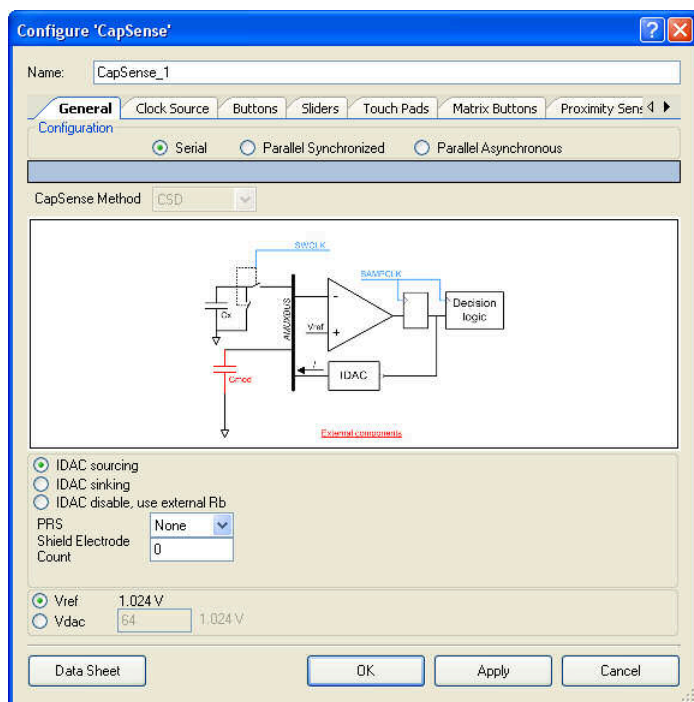
## CapSense Component Quick Start

The following steps walk you through creating a CapSense project that senses two CapSense buttons and displays the status on an LCD. This section assumes that you are familiar with PSoC® Creator™ and describes the basics of configuring the CapSense component in a project targeted to work on the CY8CKIT-001 PSoC® Development Kit (DVK). If you are not familiar with PSoC Creator, you may want to learn the basics before continuing.

**PRELIMINARY**

This quick start section describes the process of placing and configuring the CapSense component, assigning the CapSense signals to physical PSoC pins, and adding application level calls to CapSense component APIs to scan the sensors and act on sensor status values. The parameter values here are suggested starting values assuming you use the DVK. If you are creating an actual application, you must tune the parameters on your target hardware for optimal CapSense functionality.

1. Create a new PSoC Creator project. This project uses the default PSoC Creator clock resource configuration.
2. Locate the CapSense component in the PSoC Creator Component Catalog. Select the component icon and drag it onto the TopDesign schematic view.
3. Double-click the CapSense component to open the configure dialog. The dialog contains several tabs to configure the CapSense sensing method and various sensor types. Refer to the Parameters and Setup section for more details.

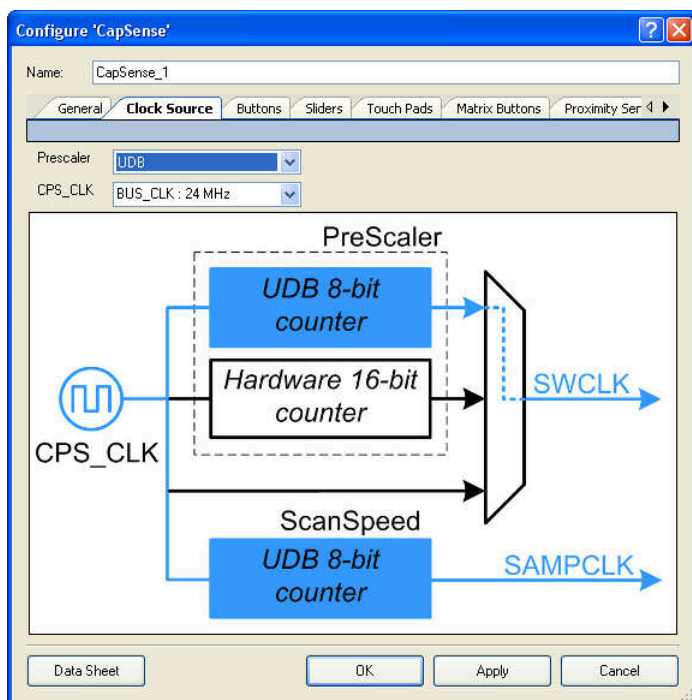


4. Under the **General** tab, choose the following settings:
  - Configuration: **Serial**
  - CapSense Method: **CSD**
  - Select: **IDAC Sourcing**
  - PRS: **None**

**PRELIMINARY**



5. Select the **Clock Source** tab and choose the following settings:



- Prescaler: **UDB**
- CPS\_CLK: **BUS\_CLK: 24MHz**

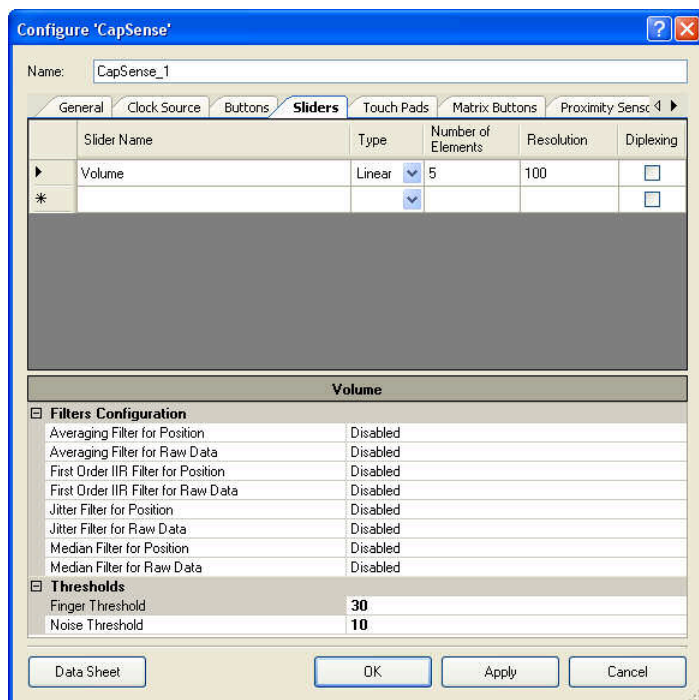
6. Select the **Buttons** tab.

The screenshot shows the Buttons tab of the 'Configure CapSense' dialog. The Name field is 'CapSense\_1'. The Buttons tab displays a list of buttons: B1, B2, and a wildcard (\*). Below the list, the configuration for button B1 is shown, including Filters Configuration, Misc, and Thresholds.

B1	
<b>Filters Configuration</b>	
Averaging Filter for Raw Data	Disabled
First Order IIR Filter for Raw Data	Disabled
Jitter Filter for Raw Data	Disabled
Median Filter for Raw Data	Disabled
<b>Misc</b>	
Debounce	5
Hysteresis	5
<b>Thresholds</b>	
Finger Threshold	75
Noise Threshold	10

- Click in the first row of the button definition table with the "\*" symbol in the first column. Type "B1" for the **Button Name**. Type "B2" as the name for the second button.
- The sensor-specific CapSense parameters for the selected button are displayed in the lower half of the dialog. Enter the following values for both buttons:
  - Finger Threshold: **75**
  - Noise Threshold: **10**
  - Debounce: **5**
  - Hysteresis: **5**

## 7. Select the **Sliders** tab.

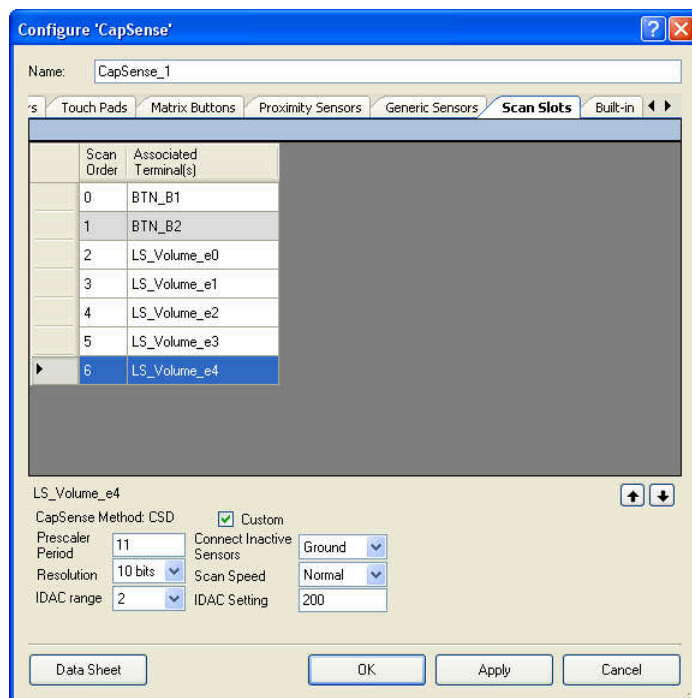


- Click in the first row of the button definition table with the "\*" symbol in the first column. Type "Volume" for the **Slider Name**.
- Specify Type of slider as **Linear**, Number of Elements as **5**, and Resolution as **100**.
- The sensor-specific CapSense parameters for the selected slider are displayed in the lower half of the dialog. Enter the following values:
  - Finger Threshold: **30**
  - Noise Threshold: **10**

**PRELIMINARY**



8. Select the **Scan Slots** tab (if the Scan Slots tab is not visible, click the right arrow control to scroll the Scan Slots tab into view). This tab allows you to configure the scanning order for the sensor slots. Set the sensing algorithm parameters individually for each scan slot.

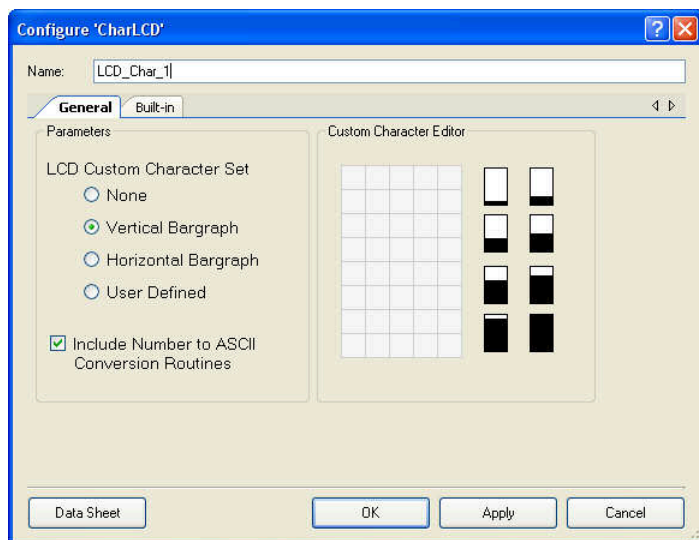


- Select button "BTN\_B1" and "BTN\_B2" by clicking in the button alias in the Associated Terminal column.
  - Click a check in the **Custom** check box. This allows you to set the scanning method parameters for this slot. Select the following for each scan slot:
    - Prescaler Period: **11**
    - Resolution: **10 bits**
    - IDAC Range: **2**
    - Connect Inactive Sensors: **Ground**
    - Scan Speed: **Normal**
    - IDAC Setting: **127**
  - Select all LS\_Volume\_ex elements by clicking in the element alias in the Associated Terminal column. Select the same **Custom** settings as above except:
    - IDAC Setting: **200**
9. Click **OK** to close the dialog.
10. Locate the Character LCD component in the Component Catalog (in the "Display" folder). Select the component icon and drag it onto the TopDesign schematic view.



PRELIMINARY

11. Double-click the Character LCD component to open the configure dialog. Select Vertical Bargraph and Include Number to ASCII Conversion Routines so that Character LCD component works with the sample code provided.



12. Click **OK** to exit the dialog.

13. Click the **Save** button to save the project.

14. The sensor signals (as well as the LCD signals) must be assigned to physical pins using the Design Wide Resources Pin Editor.

Alias	Name	Pin	Lock	Type
sCmod	CapSense_sbCSD_cCmod	P2[7]	<input checked="" type="checkbox"/>	Analog
BTN_B1	CapSense_sbCSD_cPort[0]	P0[5]	<input checked="" type="checkbox"/>	Analog
BTN_B2	CapSense_sbCSD_cPort[1]	P0[6]	<input checked="" type="checkbox"/>	Analog
LS_Volume_e0	CapSense_sbCSD_cPort[2]	P0[0]	<input checked="" type="checkbox"/>	Analog
LS_Volume_e1	CapSense_sbCSD_cPort[3]	P0[1]	<input checked="" type="checkbox"/>	Analog
LS_Volume_e2	CapSense_sbCSD_cPort[4]	P0[2]	<input checked="" type="checkbox"/>	Analog
LS_Volume_e3	CapSense_sbCSD_cPort[5]	P0[3]	<input checked="" type="checkbox"/>	Analog
LS_Volume_e4	CapSense_sbCSD_cPort[6]	P0[4]	<input checked="" type="checkbox"/>	Analog
	LCD_LCDPort[6:0]	P2[6:0]	<input checked="" type="checkbox"/>	Digital Output

Each of the external signals associated with the project will be displayed in the pin assignment table on the right side of the dialog. Assign the CapSense signal pins as follows to use this project on the CY8CKIT-001 DVK board:

- sCmod: **P2[7]**
- BTN\_B1: **P0[5]**

**PRELIMINARY**



- BTN\_B2: **P0[6]**
- LS\_Volume\_e0 – e4: **P0[0] – P0[4]**

**Note** Cmod 2.2 nF implemented on the board starts from Rev 5.

**Note** Cmod is connected between pin and GND.

General guidelines for assigning CapSense signals to physical pins are provided in the Pin Assignments section.

15. Assign the LCD control signals (LCD\_LCDPort[6:0]) to P2[6:0] in order to use this project on the CY8CKIT-001 DVK board.

16. Copy the following code into the project *main.c* file. The generated API prototypes and available *#defines* used in this code are located in the *CapSense.h* file.

The code fragment below demonstrates the initialization of the CapSense component and a loop to scan the two buttons and slider. The LCD displays the button status and the position on the slider, and moves the bargraph according to the slider position.

```
#include <device.h>

extern const uint8 LCD_Char_1_customFonts[];

void main()
{
    uint8 CurPos, OldPos = 0;
    CYGlobalIntEnable;

    /* Initialize LCD */
    LCD_Char_1_Start();
    LCD_Char_1_LoadCustomFonts(LCD_Char_1_customFonts);
    LCD_Char_1_Position(0, 0);
    LCD_Char_1_PrintString("TEST");

    /* Initialize the CapSense component */
    CapSense_1_Start();
    CapSense_1_CSHL_InitializeAllBaselines();

    /* Sensor Scanning Loop */
    while(1)
    {
        /* Scan all sensors and update baseline */
        CapSense_1_ScanAllSlots();
        CapSense_1_CSHL_UpdateAllBaselines();

        LCD_Char_1_Position(0, 6);

        /* Left button pressed */
        if (CapSense_1_CSHL_CheckIsSlotActive(CapSense_1_SCANSLOT_BTN_B1))
        {
            LCD_Char_1_PrintString("ON ");
        }
        else
        {

```



**PRELIMINARY**

```

        LCD_Char_1_PrintString("OFF");
    }

    LCD_Char_1_Position(0, 10);

    /* Right button pressed */
    if (CapSense_1_CSHL_CheckIsSlotActive(CapSense_1_SCANSLOT_BTN_B2))
    {
        LCD_Char_1_PrintString("ON ");
    }
    else
    {
        LCD_Char_1_PrintString("OFF");
    }


    /* Find Slider Position */
    CurPos = CapSense_1_CSHL_GetCentroidPos(CapSense_1_CSHL_LS_VOLUME);

    /* Reset position */
    if(CurPos == 0xFF)
    {
        CurPos = 0;
    }

    /* Move bargraph */
    if (CurPos != OldPos)
    {
        LCD_Char_1_DrawVerticalBG(1, OldPos/6, 1, 0);
        OldPos = CurPos;
        if (CurPos != 0)
        {
            LCD_Char_1_DrawVerticalBG(1, CurPos/6, 1, 7);
        }

        LCD_Char_1_Position(0, 14);
        LCD_Char_1_PrintInt8(CurPos);
    }
}
}

```

17. Click Build Project . This generates and compiles the source code files that implement the CapSense solution.

18. Program the project onto your DVK board.

## Input/Output Connections

There are no schematic connections required for the CapSense component. The component encompasses the analog and digital functional blocks and required interconnections that implement the capacitive sensing algorithms.

**PRELIMINARY**





## Parameters and Setup

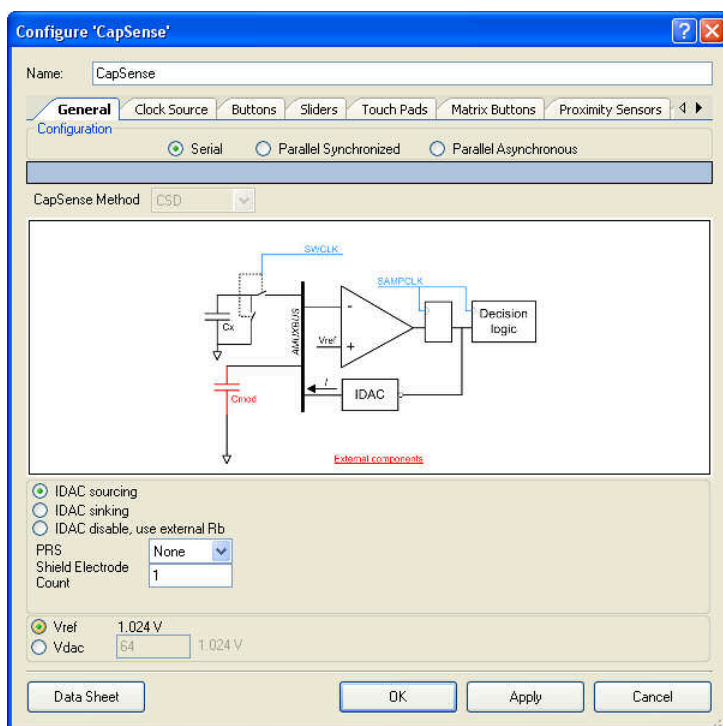
Drag a CapSense component onto the design and double-click it to open the Configure dialog. This dialog has several tabs that guide through the process of setting up the CapSense component.

### General Tab

#### Configuration

There are three configuration options:

- **Serial** – The component is capable of performing one capacitive scan at a time. Multiple sensors are scanned one at a time in succession.



- The AMUX buses are tied together.

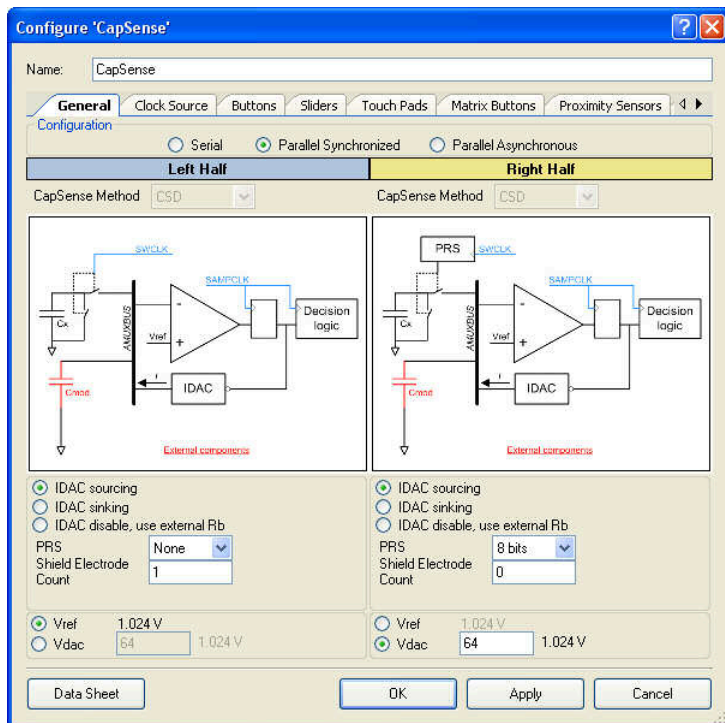
**Note** If all capacitive sensors are allocated on one side of the device, left (0 – (#EVEN\_PORT\_GPIO – 1) or right (0 – (#ODD\_PORT\_GPIO – 1)), then the AMUX buses do not tie together. Only one half of the AMUX bus is used.

- The component is capable of scanning one to (#GPIO – 1) capacitive sensors.
- The tied AMUX bus can use only one sensing method.
- One Cmod external capacitor is required.



**PRELIMINARY**

- Parallel Synchronized** – The component is capable of performing two simultaneous capacitive scans. Both the left and right AMUX buses are used. Left and right sensors are scanned two at a time (one left sensor and one right sensor at a time) in succession. If one channel has more sensors than the other, the channel with the greatest number of sensors will finish scanning the remaining sensors in its array one at a time until done.

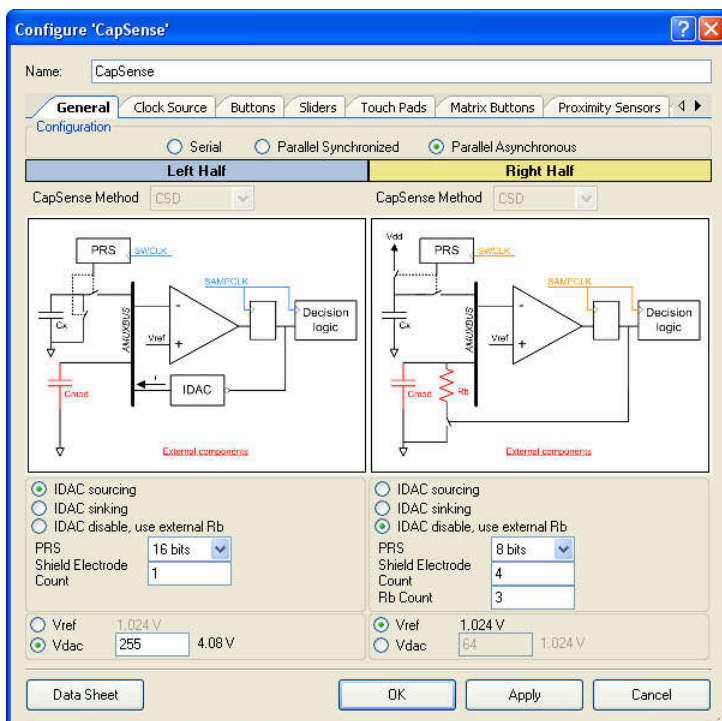


- The left AMUX bus is capable of scanning one to ( $\# \text{EVEN\_PORT\_GPIO} - 1$ ) capacitive sensors.
- The right AMUX bus is capable of scanning one to ( $\# \text{ODD\_PORT\_GPIO} - 1$ ) capacitive sensors.
- Each AMUX bus can use a different sensing method.
- Two Cmod external capacitors are required.
- Parallel scans run at the same scan rate.

PRELIMINARY



- **Parallel Asynchronous** – The component is capable of performing two simultaneous capacitive scans. The left and right AMUX buses are used. The left and right channels are scanned independently of each other.



- The left AMUX bus is capable of scanning one to (#EVEN\_PORT\_GPIO – 1) capacitive sensors.
- The right AMUX bus is capable of scanning one to (#ODD\_PORT\_GPIO – 1) capacitive sensors.
- Each AMUX bus can use a different sensing method.
- Two Cmod external capacitors are required.
- Parallel scans run at different scan rates.

## CapSense Method

Choose a CapSense Method:

- **CSD** – The CSD (Capacitive Sensing using a Sigma Delta Modulator) provides capacitance sensing using the switched capacitor technique with a sigma delta modulator to convert the sensing switched capacitor current to a digital code. CSD allows implementation of buttons, sliders, touch pads, and matrix buttons using arrays of conductive sensors. High-level software routines allow for enhancement of slider resolution using dithering, and compensation for physical and environmental sensor variation.

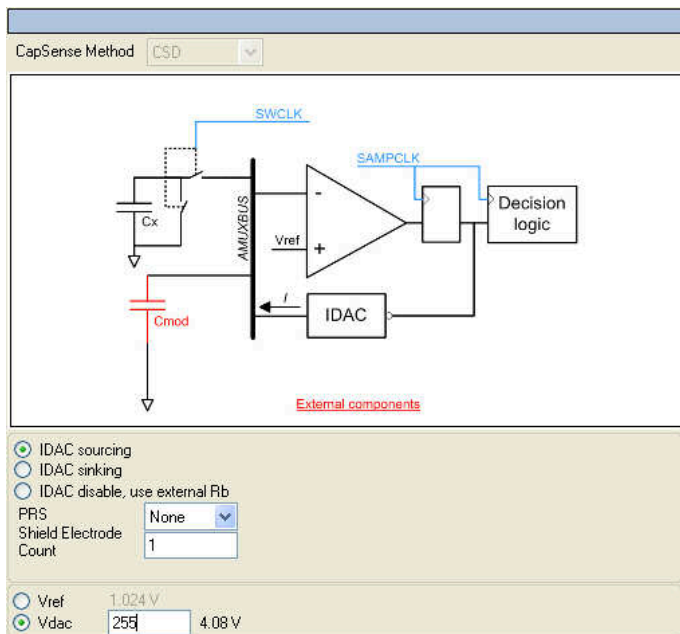


**PRELIMINARY**

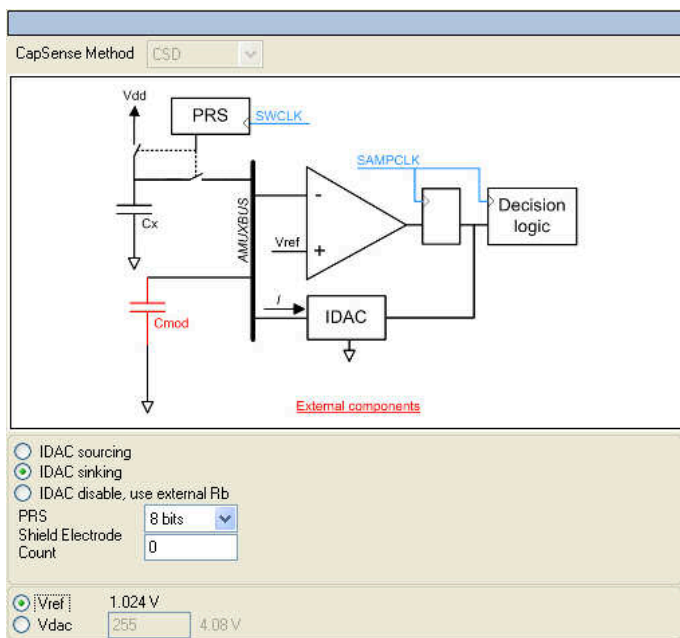
## CSD Method Variants

There are three CSD method variants:

- **IDAC Sourcing** – The switch stage is configured to alternate between GND and the AMUX bus. The IDAC sources the current.



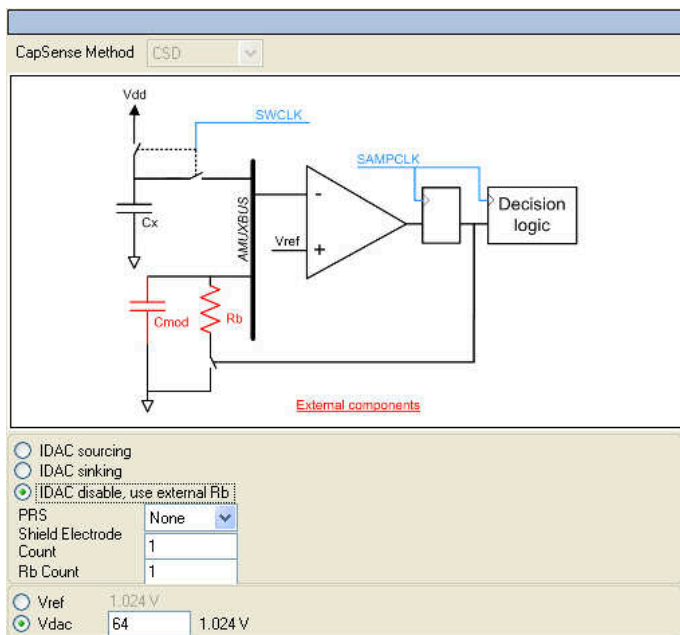
- **IDAC Sinking** – The switch stage is configured to alternate between Vdd and the AMUX bus. The IDAC sinks the current.



PRELIMINARY



- **IDAC Disable, use External Rb** – This is the same as the IDAC sinking configuration except here the IDAC is replaced with a bleed resistor to ground, Rb. The bleed resistor is connected between the Cmod and a GPIO. The GPIO is configured to Open Drain Drive Low drive mode. This drive mode allows the Cmod to be discharged through Rb.



## PRS

The Pseudo Random Sequence (PRS) generator drives the switching clock, SW\_CLK. Options include:

- PRS 16 – 16-bit PRS generator is used.
- PRS 8 – 8-bit PRS generator is used.
- None – No PRS generator is used.

**Note** When using PRS 16, the SW\_CLK should be two times faster.

## Shield Electrode Count

The Shield Electrode Count determines the number of shield electrodes. This option creates a number of special terminals for shield signals. Shield signals are shown in the Pin Editor but are not shown in the Scan Slots tab.

**Note** For proper shield electrode operation using the IDAC Sourcing method variant, the reference voltage should be set to Vdac with a value of 255.



PRELIMINARY

## Rb Count

This determines the number of bleed resistors. The maximum number of bleed resistors is three. Bleed resistor terminals are shown in the Pin Editor but are not shown in the Scan Slots tab.

**Note** The maximum quantity of sensors, shield electrodes, and bleed resistors is 62.

## Vref

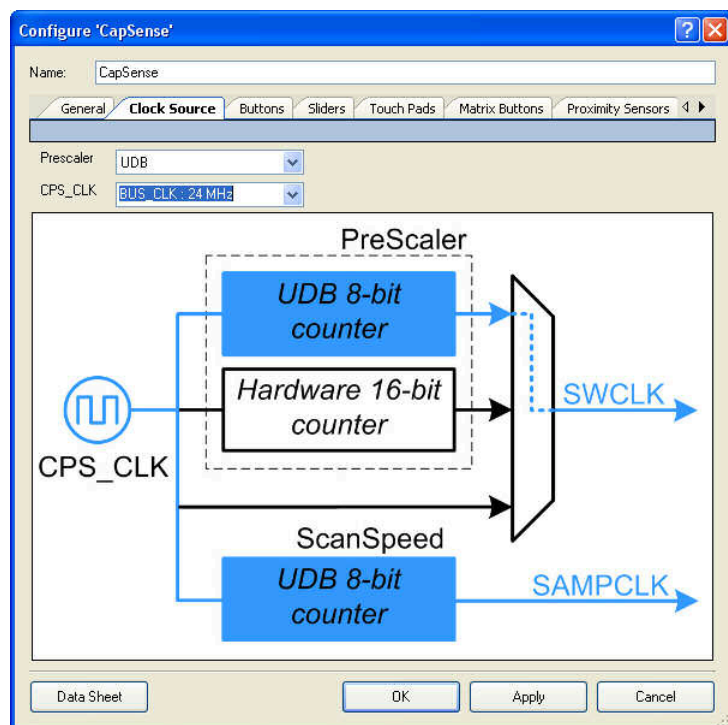
This determines the reference voltage for CapSense. The available choices are dedicated internal reference for CapSense 1.024V and the reference voltage provided from the Vdac component. The Vdac reference consumes an additional VIDAC block. The Vdac value regulates the reference voltage.

**Note** The Vdac reference also depends on the power supply. For example, for a Vdac value of 255, the reference is ~3.3V for a power supply of 3.3V and 4.08V for a power supply of 5.0V.

## Clock Sources Tab

### Serial Configuration

In this configuration, only one clock source is needed for the entire CapSense system. Choose a CPS\_CLK from the available sources.

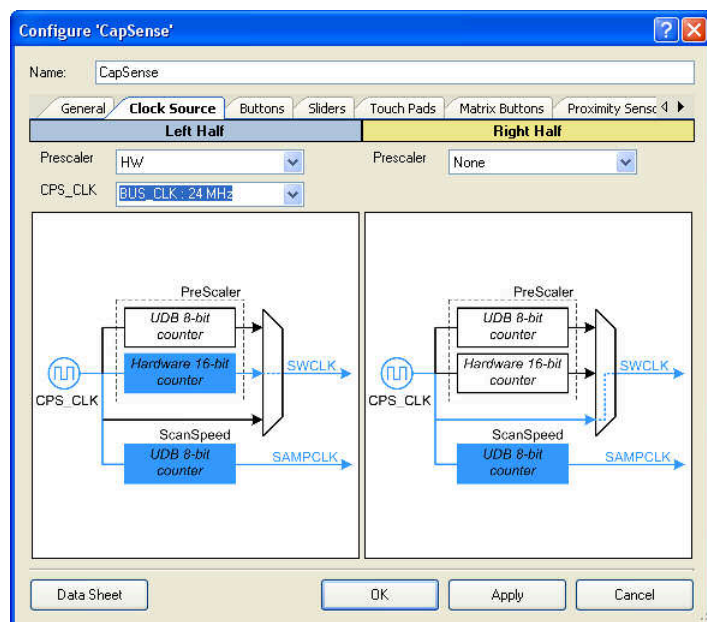


**PRELIMINARY**



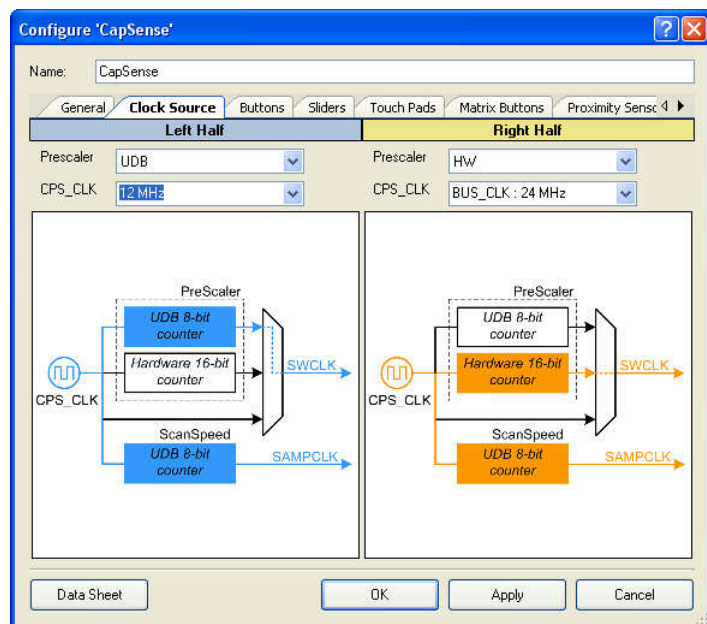
## Parallel Synchronized Configuration

In this configuration, the same clock is provided to both prescalers. Choose one CPS\_CLK for both channels from the available sources.



## Parallel Asynchronous Configuration

In this configuration, the different clocks are provided to the left and right prescalers. Choose a CPS\_CLK for each channel from the available sources.



## Prescaler

This option selects the resource used for the prescaler:

- UDB – UDB-based, 8-bit counter is used as the prescaler.
- HW – Fixed function, 16-bit timer is used as the prescaler.
- None – Prescaler functionality is not used.

**Note** When using the HW prescaler to run at a frequency equal to the bus clock, the bus clock must be used.

## CPS\_CLK

Select the clock source for the prescaler. Available choices are:

- Several clocks are specifically created for the CapSense component. If you choose one of these clocks, one clock divider from the digital clock tree will be utilized. The special CapSense clock sources are:
  - 12 MHz
  - 24 MHz
  - 48 MHz
- A CapSense clock can be used as a direct clock that goes directly from one of the system clocks. The only available choice is BUS\_CLK. Choosing direct clock does not consume any additional system resources.
- A custom CapSense clock can be created by entering the desired clock frequency in MHz into the field. If you create a custom clock, one clock divider from the digital clock tree will be utilized.

**Note** The CapSense clock cannot be greater than the fastest clock in the system (MASTER\_CLK).

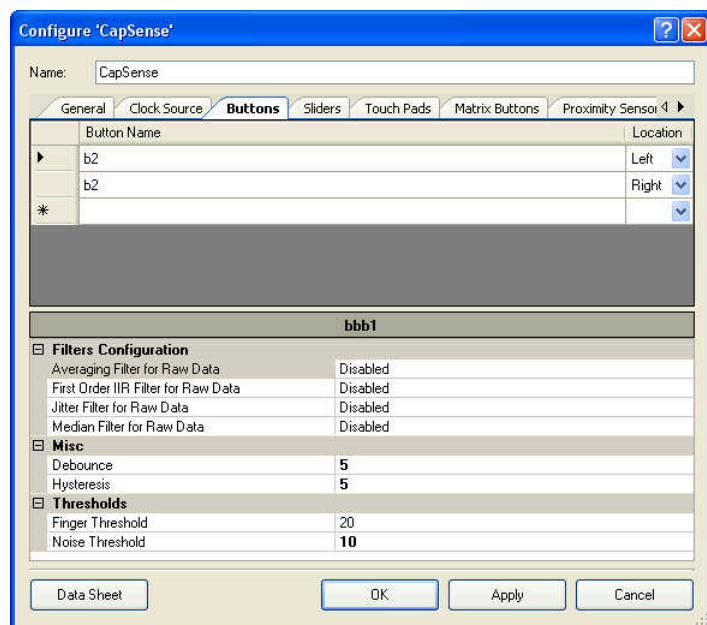
PRELIMINARY





## Buttons Tab

The Buttons tab varies slightly depending on whether the configuration is serial or parallel. The serial configuration does not have the choice of assigning buttons to the left or right side.

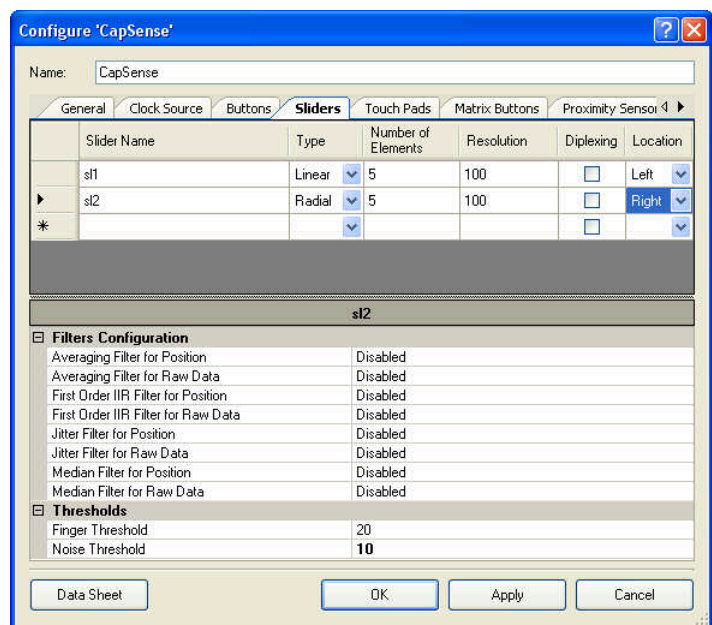


Definitions of the parameters are in the Functional Description section.

- Finger Threshold and Noise Threshold (simple ON/OFF result)
- Hysteresis for Finger Threshold
- Debounce Support
- Raw Data Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter

## Sliders Tab

The sliders tab varies slightly depending on whether the configuration is serial or parallel. The serial configuration does not have the choice of assigning sliders to the left or right side.



## Linear Slider

Definitions of the parameters are in the Functional Description section.

- Interpolated Position (Resolution)
- Diplexing
- Raw Data Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter
- Position Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter

**PRELIMINARY**



## Radial Slider

Definitions of the parameters are in the Functional Description section.

- Interpolated Position (Resolution)
- Diplexing
- Raw Data Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter
- Position Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter



**PRELIMINARY**

## Touch Pads Tab

The Touch Pads tab varies slightly depending on whether the configuration is serial or parallel. In the **serial** configuration there is no choice of left or right side. In the **parallel synchronized** configuration, the column and row can be assigned to the left side or right side separately. For example, the left side can scan rows while the right side scans columns. In the **parallel asynchronous** configuration, the entire touch pad, rows and columns should be assigned to the same side. For example, the right side scans the touch pad while the left side scans all other CapSense controls.

Touchpad Name	Number of Rows	Number of Columns	Rows Resolution	Columns Resolution	Rows Location	Columns Location
tp1	2	2	100	100	Left	Left
tp2	2	2	100	100	Left	Right
*						

**Filters Configuration**

Filter	Status
Averaging Filter for Position	Disabled
Averaging Filter for Raw Data	Disabled
First Order IIR Filter for Position	Disabled
First Order IIR Filter for Raw Data	Disabled
Jitter Filter for Position	Disabled
Jitter Filter for Raw Data	Disabled
Median Filter for Position	Disabled
Median Filter for Raw Data	Disabled

**Touchpad Rows**

Threshold	Value
Finger Threshold	20
Noise Threshold	10

**Touchpad Columns**

Threshold	Value
Finger Threshold	20
Noise Threshold	10

Definitions of the parameters are in the Functional Description section.

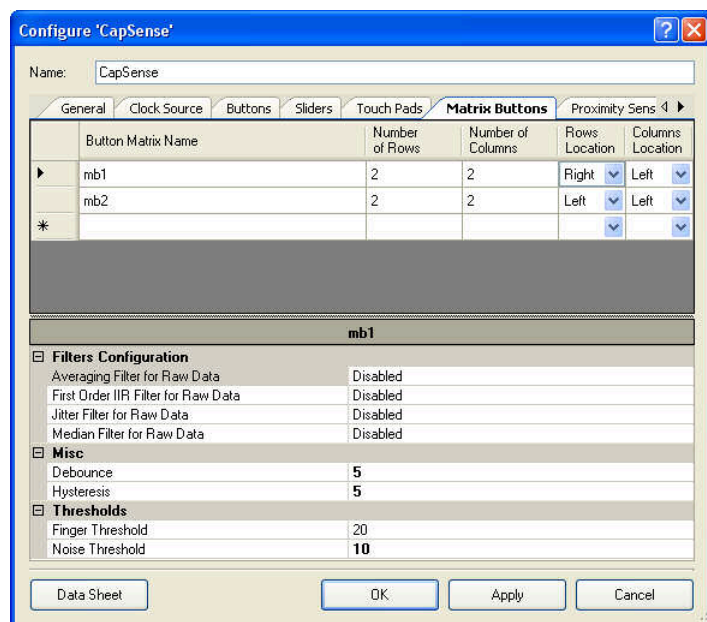
- Interpolated Position (Resolution) for X and Y
- Raw Data Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter
- Position Filtering (X and Y):
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter

**PRELIMINARY**



## Matrix Buttons Tab

The Matrix Buttons tab varies slightly depending on whether the configuration is serial or parallel. In the **serial** configuration, there is no choice of left or right side. In the **parallel synchronized** configuration, the column and row can be assigned to the left side or right side separately. For example, the left side can scan rows while the right side scans columns. In the **parallel asynchronous** configuration, all matrixed buttons, rows and columns should be assigned to the same side. For example, the right side scans the button matrix while the left side scans all other CapSense controls.

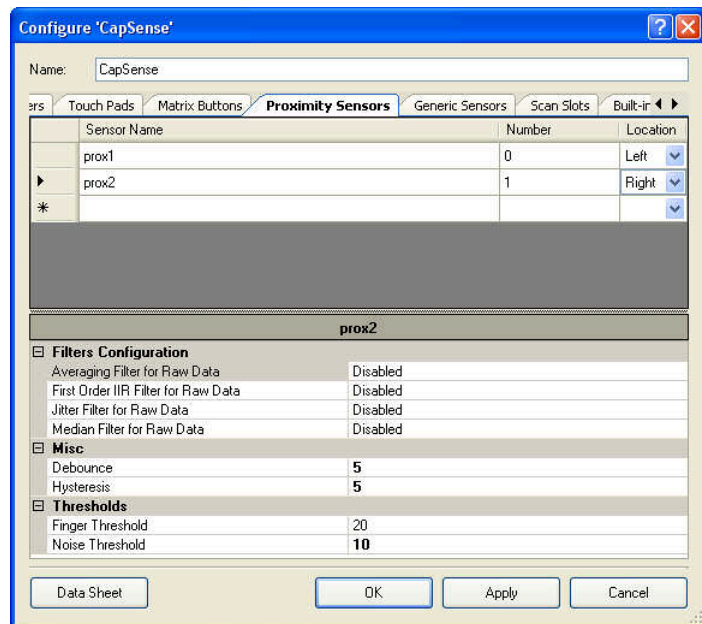


Definitions of the parameters are in the Functional Description section.

- Finger Threshold and Noise Threshold (simple ON/OFF result for each button)
- Hysteresis for Finger Threshold
- Debounce Support
- Raw Data Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter

## Proximity Sensors Tab

The Proximity Sensors tab varies slightly depending on whether the configuration is serial or parallel. The serial configuration does not have the choice of assigning proximity sensors to the left or right side.



Definitions of the parameters are in the Functional Description section.

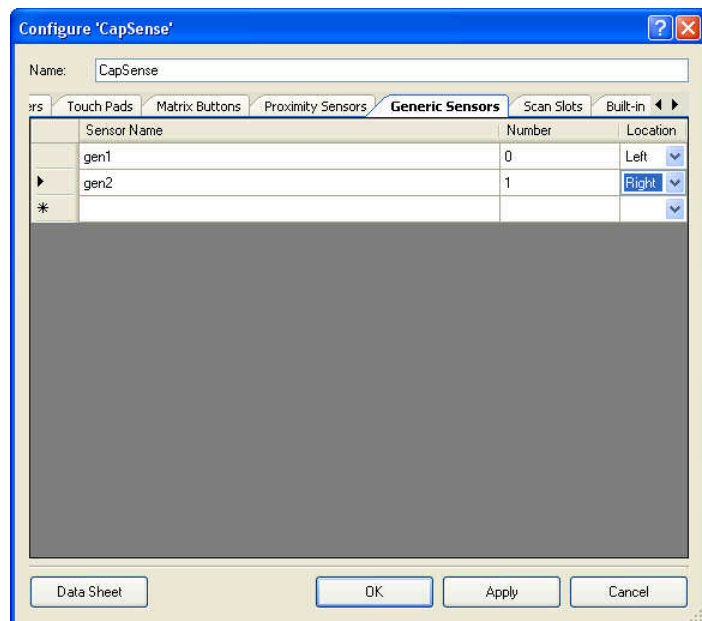
- Finger Threshold and Noise Threshold (simple ON/OFF result for the proximity sensor)
- Hysteresis for Proximity Threshold
- Debounce Support
- Number – Selects the number of proximity sensors:
  - 0 – The proximity sensor scans one or more existing sensors to determine proximity. No terminals are allocated for this sensor.
  - 1 to N – Number of dedicated proximity sensors in the system.
- Raw Data Filtering:
  - Median Filter
  - Averaging Filter
  - First Order IIR Filter
  - Jitter Filter

**PRELIMINARY**



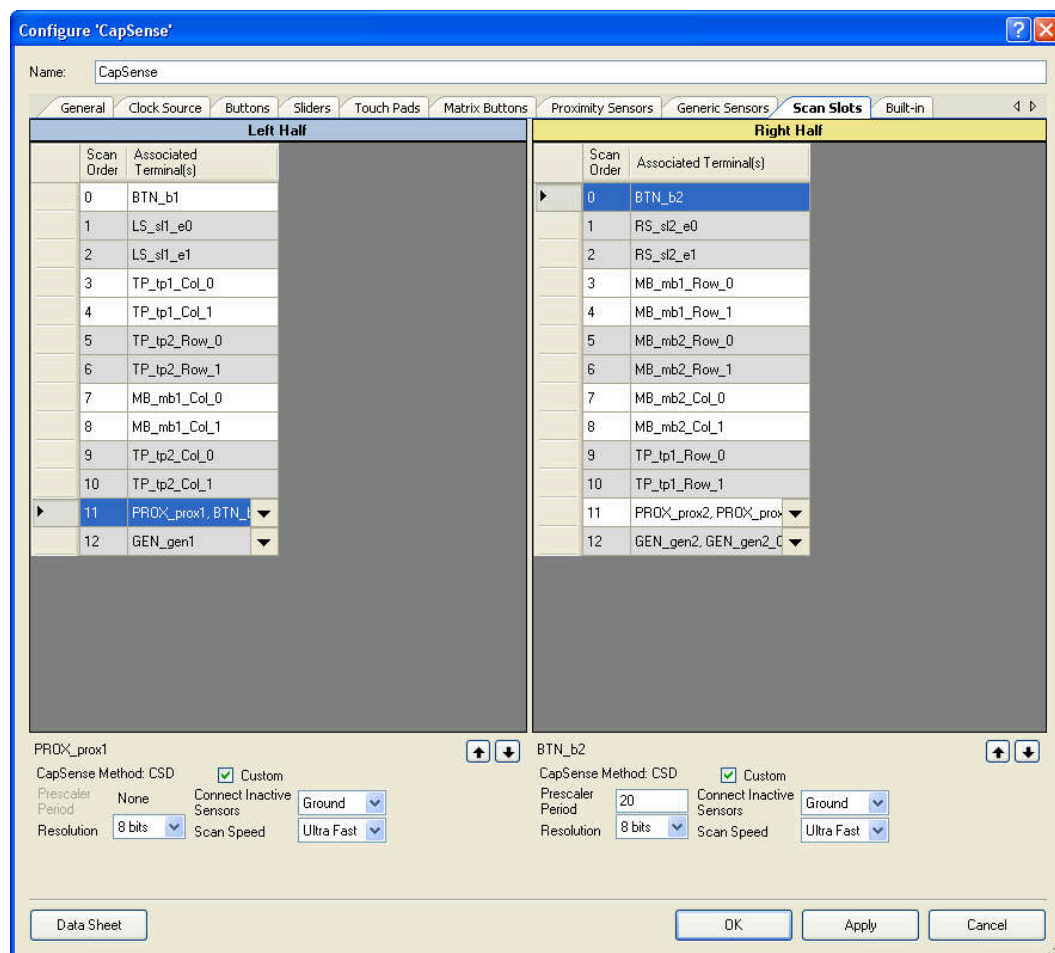
## Generic Sensors Tab

The Generic Sensors tab varies slightly depending on whether the configuration is serial or parallel. The serial configuration does not have the choice of assigning generic sensors to the left or right side.



- **Generic Sensors** – No high-level support is provided. Raw sensor data is provided by the component. The user develops the high-level functionality required by the application.
- **Number** – Selects the number of generic sensors:
  - 0 – Use this setting to obtain raw data from another sensor or sensors. No terminals are allocated for this sensor.
  - 1 to N – Number of generic sensors. 1 to N generic terminals is allocated.

## Scan Slots Tab



For serial configurations, one table lists the scan slots for the AMUX bus (two AMUX buses tied together\*). For parallel configurations, two tables list the scan slots for each AMUX bus. Widgets are listed in alternating gray and white rows in the table. All terminals are associated with a widget (for example, a touch pad's rows or columns) share the same color.

It is not possible to use the Scan Slots tab to move a scan slot from the right half to the left half, but it is possible to change the scan order of the slots. If users move one member of a widget (for example, the first sensor in a linear slider), all other sensors belonging to the widget will be moved at the same time.

Proximity scan slots can use dedicated proximity sensors, or they can detect proximity from a combination of dedicated sensors and other sensors. For example, the board may have a trace that goes all the way around an array of buttons and the proximity sensor may be made up of the trace and all of the buttons in the array. All of these sensors are scanned at the same time to detect proximity. A drop down list is provided on proximity scan slots to choose the sensors to scan to detect proximity.

**PRELIMINARY**





Like proximity sensors, generic sensors can consist of multiple sensors. A generic sensor obtains data from a dedicated sensor, any other existing sensor, or from multiple sensors. Select the sensors with the drop down list provided.

If all capacitive sensors are allocated on one side of the device, left (0 – (#EVEN\_PORT\_GPIO – 1) or right (0 – (#ODD\_PORT\_GPIO – 1), then the AMUX buses do not tie together. Only one half of the AMUX bus is used.

### CSD Scan Slot Parameters

Scan slots within a particular widget, such as all the sensors in a linear slider, should have identical parameters except the IDAC Range and IDAC Setting. If you change any of the other parameters for one sensor in a widget, you should change all the others to be the same.

**Note** If **Custom** is checked, each scan slot has different settings. Select each scan slot in turn to define its settings. If **Custom** is unchecked, all scan slots share the same settings.

IDAC Sourcing and IDAC Sinking Configurations:

- Scan Speed – Defines the scan speed as Ultra Fast, Fast, Normal, or Slow.
- Resolution – Defines the scanning resolution of the PWM. Choices are from 8 to 16 bits.
- IDAC Range – Multiplies the IDAC current by the number selected (1 – 3).
- IDAC Settings – Selects the IDAC scanning value (0 – 255).
- PreScaler Period – The switch clock (SWCLK) is the CapSense clock (CPS\_CLK) divided by this number to obtain the switch frequency for the break-before-make logic (1 – 255).
- Connect Inactive Sensors – Defines the unscanning sensor connection as GND, High Z or Shield Electrode.\*

Configurations without an IDAC:

- Scan Speed – Defines the scan speed as Ultra Fast, Fast, Normal, or Slow.
- Resolution – Defines the scanning resolution of the PWM. Choices are 8 and 16 bits.
- PreScaler Period – The switch clock (SWCLK) is the CapSense clock (CPS\_CLK) divided by this number to obtain the switch frequency for the break-before-make logic (1 – 255).
- Connect Inactive Sensors – Defines the unscanning sensor connection as GND, High Z or Shield Electrode.<sup>1</sup>

---

<sup>1</sup> The Shield Electrode option is only available if the shield electrode count is greater than zero.

## Clock Selection

Select the clock source for the prescaler. Available choices are:

- Several clocks are specifically created for the CapSense component. If you choose one of these clocks, one clock divider from the digital clock tree will be utilized. The special CapSense clock sources are:
  - 12 MHz
  - 24 MHz
  - 48 MHz
- A CapSense clock can be selected as the direct clock that goes directly from one of the system clocks. The only available choice is BUS\_CLK. Choosing direct clock does not consume any additional system resources.
- A custom CapSense clock can be created by entering the desired clock frequency in MHz into the field. If you create custom clock, one clock divider from the digital clock tree will be utilized.

**Note** The CapSense clock cannot be greater than the fastest clock in the system (MASTER\_CLK).

## Placement

Not applicable.

## Resources

Resolution	Digital Blocks					API Memory (Bytes)		Pins (per External I/O)
	Datapaths	Macro Cells	Status Registers	Control Registers	Counter7	Flash	RAM	
1	1	TBD	1	1	0	TBD	TBD	TBD
2	2	TBD	1	1	0	TBD	TBD	TBD

## Application Programming Interface

Application Programming Interface (API) routines allow you to configure the component using software. The following table lists and describes the interface to each function. The subsequent sections cover each function in more detail.

By default, PSoC Creator assigns the instance name "CapSense\_1" to the first instance of a component in a given design. You can rename it to any unique value that follows the syntactic

**PRELIMINARY**



rules for identifiers. The instance name becomes the prefix of every global function name, variable, and constant symbol. For readability, the instance name used in the following table is "CapSense."

## High Level APIs

The CSHL is the prefix for all high level APIs. These APIs obtain raw data from scan slots and convert it to ON/OFF for buttons, position for sliders, or X and Y coordinates for touch pads.

Some high level API functions are appended with "Left" and "Right" in Parallel Configuration. APIs appended with "Left" work only with the left side of the CapSense system. Those appended with "Right" work only with the right side.

Function	Description
CapSense_CSHL_InitializeSlotBaseline	Loads the CapSense_CSHL_SlotBaseline[slot] array element with an initial value by scanning the selected slot. The raw count value is copied into the baseline array for each slot. The raw data filters are initialized if enabled.
CapSense_CSHL_InitializeAllBaselines	Uses the CapSense_CSHL_InitializeSlotBaseline function to load the CapSense_CSHL_SlotBaseline[ ] array with an initial value by scanning all slots. The raw count values are copied into the baseline array for all slots. The raw data filters are initialized if enabled.
CapSense_CSHL_UpdateSlotBaseline	Updates the CapSense_CSHL_SlotBaseline[ ] array using the LP filter with $k = 256$ . The signal calculates the difference of count by subtracting the previous baseline and noise threshold from the current raw count value. The baseline stops updating if the signal is greater than zero. Raw data filters are applied to the values if enabled.
CapSense_CSHL_UpdateAllBaselines	Uses the CapSense_CSHL_UpdateSlotBaseline function to update the baselines for all slots. Raw data filters are applied to the values if enabled.
CapSense_CSHL_CheckIsSlotActive	Compares the selected slot of the CapSense_CSHL_Signal[ ] array to its finger threshold. Hysteresis and debounce are taken into account. The hysteresis value is added or subtracted from the finger threshold based on whether the slot is currently active. If the slot is active, the threshold is lowered by the hysteresis amount. If it is inactive, the threshold is raised by the hysteresis amount. The debounce counter must expire before the slot transitions to active. This function also updates the slot's bit in the CapSense_CSHL_SlotOnMask[ ] array.
CapSense_CSHL_CheckIsAnySlotActive	Compares all slots of the CapSense_CSHL_Signal[ ] array to their finger threshold. Calls CapSense_CSHL_CheckIsSlotActive() for each slot so the CapSense_CSHL_SlotOnMask[ ] array is up to date after calling this function.
CapSense_CSHL_GetCentroidPos	Checks the CapSense_CSHL_Signal[ ] array for a centroid within slider specified range. The centroid position is calculated to the resolution specified in the CapSense Customizer. The position filters



**PRELIMINARY**

Function	Description
	are applied to the result if enabled. This function is available only if a linear slider is defined by the CapSense Customizer.
CapSense_CSHL_GetRadialCentroidPos	Checks the CapSense_CSHL_Signal[ ] array for a centroid within slider specified range. The centroid position is calculated to the resolution specified in the CapSense Customizer. The position filters are applied to the result if enabled. This function is available only if a radial slider is defined by the CapSense Customizer.
CapSense_CSHL_GetDoubleCentroidPos	If a finger is present, this function calculates the X and Y position of the finger by calculating the centroids within touch pad specified range. The X and Y positions are calculated to the resolutions set in the CapSense Customizer. Returns a '1' if a finger is on the touch pad. The position filters are applied to the result if enabled. This function is available only if a touch pad is defined by the CapSense Customizer.

### void CapSense\_CSHL\_InitializeSlotBaseline(uint8 slot)

**Description:** Loads the CapSense\_CSHL\_SlotBaseline[slot] array element with an initial value by scanning the selected slot. The raw count value is copied into the baseline array for each slot. The raw data filters are initialized if enabled.

**Parameters:** slot: uint8 – Scan slot number

**Return Value:** None

**Side Effects:** None

### void CapSense\_CSHL\_InitializeAllBaselines

**Description:** Uses the CapSense\_CSHL\_InitializeSlotBaseline function to load the CapSense\_CSHL\_SlotBaseline[ ] array with an initial value by scanning all slots. The raw count values are copied into the baseline array for all slots. The raw data filters are initialized if enabled.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

**PRELIMINARY**



**void CapSense\_CSHL\_UpdateSlotBaseline(uint8 slot)**

**Description:** Updates the CapSense\_CSHL\_SlotBaseline[ ] array using the LP filter with  $k = 256$ . The signal calculates the difference of count by subtracting the previous baseline and noise threshold from the current raw count value. The baseline stops updating if the signal is greater than zero. Raw data filters are applied to the values if enabled.

**Parameters:** slot: uint8 – Scan slot number

**Return Value:** None

**Side Effects:** None

**void CapSense\_CSHL\_UpdateAllBaselines**

**Description:** Uses the CapSense\_CSHL\_UpdateSlotBaseline function to update the baselines for all slots. Raw data filters are applied to the values if enabled.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

**uint8 CapSense\_CSHL\_CheckIsSlotActive(uint8 slot)**

**Description:** Compares the selected slot of the CapSense\_CSHL\_Signal[ ] array to its finger threshold. Hysteresis and debounce are taken into account. The hysteresis value is added or subtracted from the finger threshold based on whether the slot is currently active. If the slot is active, the threshold is lowered by the hysteresis amount. If it is inactive, the threshold is raised by the hysteresis amount. The debounce counter must expire before the slot transitions to active. This function also updates the slot's bit in the CapSense\_CSHL\_SlotOnMask[ ] array.

**Parameters:** slot: uint8 – Scan slot number

**Return Value:** uint8: Scan slot state '1' if active, '0' if inactive

**Side Effects:** None

**uint8 CapSense\_CSHL\_CheckIsAnySlotActive**

**Description:** Compares all slots of the CapSense\_CSHL\_Signal[ ] array to their finger threshold. Calls CapSense\_CSHL\_CheckIsSlotActive() for each slot so the CapSense\_CSHL\_SlotOnMask[ ] array is up to date after calling this function.

**Parameters:** None

**Return Value:** uint8: '1' if any scan slot is active, '0' if no scan slot is active

**Side Effects:** None

**PRELIMINARY**

**uint16 CapSense\_CSHL\_GetCentroidPos(uint8 widget)**

**Description:** Checks the CapSense\_CSHL\_Signal[ ] array for a centroid within slider specified range. The centroid position is calculated to the resolution specified in the CapSense Customizer. The position filters are applied to the result if enabled. This function is available only if a linear slider is defined by the CapSense Customizer.

**Parameters:** widget:uint8 – Widget number. For every linear slider widget, there are #defines in this format:  

```
#define CapSense_CSHL_LS_ "widget_name" 5
```

**Return Value:** uint16: Position value of the slider

**Side Effects:** If any slider slot is active, the function returns values from zero to the resolution value set in the CapSense Customizer. If no sensors are active, the function returns -1. If an error occurs during execution of the centroid/diplexing algorithm, the function returns -1. You can use the CSHL\_CheckIsSlotActive() routine to determine which slider segments are touched, if required.

**Note** If noise counts on the slider segments are greater than the noise threshold, this subroutine may generate a false centroid result. The noise threshold should be set carefully (high enough above the noise level) so that noise will not generate a false centroid.

**uint16 CapSense\_CSHL\_GetRadialCentroidPos(uint8 widget)**

**Description:** Checks the CapSense\_CSHL\_Signal[ ] array for a centroid within slider specified range. The centroid position is calculated to the resolution specified in the CapSense Customizer. The position filters are applied to the result if enabled. This function is available only if a radial slider is defined by the CapSense Customizer.

**Parameters:** widget:uint8 – Widget number. For every radial slider widget, there are #defines in this format:  

```
#define CapSense_CSHL_RS_ "widget_name" 5
```

**Return Value:** uint16: Position value of the slider

**Side Effects:** If any radial slider slot is active, the function returns values from zero to the resolution value set in the CapSense Customizer. If no slots are active, the function returns -1. If an error occurs during execution of the centroid/diplexing algorithm, the function returns -1. You can use the CSHL\_CheckIsSlotActive() routine to determine which slider segments are touched, if required.

**Note** If noise counts on the slider segments are greater than the noise threshold, this subroutine may generate a false centroid result. The noise threshold should be set carefully (high enough above the noise level) so that noise will not generate a false centroid.

**PRELIMINARY**



**uint8 CapSense\_CSHL\_GetDoubleCentroidPos(uint8 widget)**

**Description:** If a finger is present on the touch pad, this function calculates the X and Y position of the finger by calculating the centroids within touch pad specified range. The X and Y positions are calculated to the resolutions set in the CapSense Customizer. Returns a '1' if a finger is on the touch pad. The position filters are applied to the result if enabled. This function is available only if a touch pad is defined by the CapSense Customizer.

**Parameters:** widget: uint8 – Widget number. For every touch pad widget, there are #defines in this format:  

```
#define CapSense_CSHL_TP_ "widget_name" 5
```

**Return Value:** uint8: '1' if finger is on the touch pad, '0' if not.

**Side Effects:** The result calculation of X and Y positions are stored in global arrays. The array names are:  
 CapSense\_CSHL\_TPCol\_ "widget\_name" \_Results – position of X  
 CapSense\_CSHL\_TPRow\_ "widget\_name" \_Results – position of Y

**Scan Control APIs**

These APIs start and stop the component and control scanning functions.

Function	Description
CapSense_Start	Initializes registers for each module and starts the component. This function calls functions automatically depending on modules selected in the CapSense Customizer. This function should be called prior to calling any other component function.
CapSense_Stop	Stops the slot scanner, disables interrupts, and resets all slots to an inactive state.
CapSense_ScanSlot	Calls the function <METHOD_NAME>_ScanSlot where <METHOD_NAME> is method name for this scan slot in the CapSense Customizer.
CapSense_ScanAllSlots	<b>Parallel Configuration:</b> Scans slots from both sides in parallel. One slot from the right side and one slot from the left side are scanned at a time. Whether they are scanned with or without synchronization depends on the selection in the CapSense Customizer. If one side has more slots than the other, the remaining slots on the side with more slots are scanned separately. The scanning ends when all slots are scanned. <b>Serial Configuration:</b> Scans all slots by calling <METHOD_NAME>_ScanAllSlots() function for method selected in customizer.

**void CapSense\_Start**

**Description:** Initializes registers for each module and starts the component. This function calls functions automatically depending on modules selected in the CapSense Customizer. This function should be called prior to calling any other component function.

**Parameters:** None

**Return Value:** None

**Side Effects:** None



**PRELIMINARY**

## void CapSense\_Stop

**Description:** Stops the slot scanner, disables interrupts, and resets all slots to an inactive state.

**Note** This API is not recommended for use on PSoC 3 ES2 and PSoC 5 ES1 silicon. These devices have a defect that causes connections to several analog resources to be unreliable when not powered. The unreliability manifests itself in silent failures (e.g. unpredictably bad results from analog components) when the component utilizing that resource is stopped. It is recommended that all analog components in a design should be powered up (by calling the <INSTANCE\_NAME>\_Start() APIs) at all times. Do not call the <INSTANCE\_NAME>\_Stop() APIs.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

## void CapSense\_ScanSlot(uint8 slot)

**Description:** Calls the function <METHOD\_NAME>\_ScanSlot where <METHOD\_NAME> is the defined module name for this scan slot in the CapSense Customizer.

**Parameters:** slot: uint8 – Scan slot number

**Return Value:** None

**Side Effects:** None

## void CapSense\_ScanAllSlots

**Description:** **Parallel Configuration:** Scans slots from both sides in parallel. One slot from the right side and one slot from the left side are scanned at a time. Whether they are scanned with or without synchronization depends on the selection in the CapSense Customizer. If one side has more slots than the other, the remaining slots on the side with more slots are scanned separately. The scanning ends when all slots are scanned.

**Serial Configuration:** Scans all slots by calling <METHOD\_NAME>\_ScanAllSlots() function for method selected in the CapSense Customizer.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

## Method Specific APIs

These API functions depend on the CapSense method selected in the CapSense Customizer. For example, if the method chosen is CSD, the instance name is “CapSense\_CSD.”

PRELIMINARY





Method Specific API functions are appended with “Left” and “Right” in Parallel Configuration. APIs appended with “Left” work only with the left side of the CapSense system. Those appended with “Right” work only with the right side.

Function	Description
CapSense_CSD_Start	Initializes registers and starts the CSD method of the CapSense component.
CapSense_CSD_Stop	Stops the slot scanner, disables internal interrupts, and calls CSD_ClearSlots() to reset all slots to an inactive state.
CapSense_CSD_ScanSlot	Sets scan settings and scans the selected scan slot.
CapSense_CSD_ScanAllSlots	Scans all scan slots by calling CSD_ScanSlot() for each slot index.
CapSense_CSD_SetSlotSettings	Sets the scan settings of selected scan slot.
CapSense_CSD_SetRBleed	Sets the pin to use for the bleed resistor (Rb) connection. This function can be called at runtime to select the current Rb pin setting from those defined in the CapSense Customizer. The function overrides the component parameter setting. This function is available only if Rb configuration is defined by the CapSense Customizer.
CapSense_CSD_ClearSlots	Clears all slots to the non-sampling state by sequentially disconnecting all slots from the AMUX bus and putting them in inactive state.
CapSense_CSD_EnableSensor	Configures the selected sensor to measure during the next measurement cycle. The corresponding pins are set to Analog High Z drive mode and connected to the AMUX bus. This also enables the comparator function.
CapSense_CSD_DisableSensor	Disables the selected sensor. The corresponding pin is disconnected from the AMUX bus and connected to GND, High_Z, or Shield Electrode.
CapSense_CSD_ReadSlot	Returns scan slot raw data from the CapSense_SlotResult[ ] array. Each scan slot has a unique number within the slot array. This number is assigned by the CapSense Customizer in sequence.

### void CapSense\_CSD\_Start

**Description:** Initializes registers and starts the CSD method of the CapSense component.

**Parameters:** None

**Return Value:** None

**Side Effects:** None



**PRELIMINARY**

**void CapSense\_CSD\_Stop**

**Description:** Stops the slot scanner, disables internal interrupts, and calls CSD\_ClearSlots() to reset all slots to an inactive state.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

**void CapSense\_CSD\_ScanSlot(uint8 slot)**

**Description:** Sets scan settings and scans the selected scan slot. Each scan slot has a unique number within the slot array. This number is assigned by the CapSense Customizer in sequence.

**Parameters:** slot: uint8 – Scan slot number

**Return Value:** None

**Side Effects:** None

**void CapSense\_CSD\_ScanAllSlots**

**Description:** Scans all scan slots by calling CSD\_ScanSlot() for each slot index.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

**void CapSense\_CSD\_SetSlotSettings(uint8 slot)**

**Description:** Sets the scan settings of selected scan slot. Each setting has a unique number within the settings array and is connected to corresponding scan slot. This number is assigned by the CapSense Customizer in sequence.

**Parameters:** slot: uint8 – Scan slot number

**Return Value:** None

**Side Effects:** None

**PRELIMINARY**



**void CapSense\_CSD\_SetRBleed(uint8 rbleed)**

**Description:** Sets the pin to use for the bleed resistor (Rb) connection. This function can be called at runtime to select the current Rb pin setting from those defined in the CapSense Customizer. The function overrides the component parameter setting. This function is available only if Rb configuration is defined by the CapSense Customizer.

This function is effective when some slots need to be scanned with different bleed resistor values. For example, regular buttons can be scanned with a lower valued bleed resistor. The proximity detector can be scanned less often with a larger bleed resistor to maximize proximity detection distance. This function can be used in conjunction with the CapSense\_CSD\_ScanSlot() function.

**Parameters:** rbleed: uint8 – Ordering number for bleed resistor terminal defined in the CapSense Customizer.

**Return Value:** None

**Side Effects:** None

**void CapSense\_CSD\_ClearSlots**

**Description:** Clears all slots to the non-sampling state by sequentially disconnecting all slots from the AMUX bus and putting them in inactive state.

**Parameters:** None

**Return Value:** None

**Side Effects:** None

**void CapSense\_CSD\_EnableSensor(uint8 sensor)**

**Description:** Configures the selected sensor to measure during the next measurement cycle. The corresponding pins are set to Analog High Z drive mode and connected to the AMUX bus. This also enables the comparator function.

**Parameters:** sensor: uint8 – Sensor number

**Return Value:** None

**Side Effects:** None

**PRELIMINARY**

**void CapSense\_CSD\_DisableSensor(uint8 sensor, uint8 state)**

**Description:** Disables the selected sensor. The corresponding pin is disconnected from the AMUX bus and connected to GND, High Z, or Shield Electrode.

**Parameters:** sensor: uint8 – Sensor number

state: uint8 – State of sensor when disabled. Possible states include:

- CapSense\_DISABLE\_STATE\_GND
- CapSense\_DISABLE\_STATE\_HIGHZ
- CapSense\_DISABLE\_STATE\_SHIELD

**Return Value:** None

**Side Effects:** None

**uint16 CapSense\_CSD\_ReadSlot(uint8 slot)**

**Description:** Returns scan slot raw data from the CapSense\_SlotResult[ ] array. Each scan slot has a unique number within the slot array. This number is assigned by the CapSense Customizer in sequence.

**Parameters:** slot: uint8 – Scan slot number

**Return Value:** uint16: Current raw data value

**Side Effects:** None

**Data Structures**

API functions use different global arrays. You should not alter these arrays manually. You can inspect these values for debugging purposes, however. For example, you can use a charting tool to display the contents of the arrays. There are several global arrays:

- CapSense\_SlotResult[ ]
- CapSense\_CSHL\_SlotBaseline[ ]
- CapSense\_CSHL\_SlotBaselineLow[ ]
- CapSense\_CSHL\_SlotSignal[ ]

In Parallel Configuration all data structures are appended with “Left” and “Right.” Data structures appended with “Left” contain values for the left side of the CapSense system. Those appended with “Right” contain values for the right side. In Serial Configuration the structures are not divided left and right. The data structures documented here assume Serial Configuration for simplicity.

**CapSense\_SlotResult[ ]**

This array contains the raw data of each scan slot. The array size is equal to the total number of scan slots (CapSense\_TOTAL\_SCANSLOT\_COUNT). The CapSense\_SlotResult[ ] data is updated by these functions:

**PRELIMINARY**



- CapSense\_ScanSlot()
- CapSense\_ScanAllSlots()
- CapSense\_CSD\_ScanSlot()
- CapSense\_CSD\_ScanAllSlots()

### CapSense\_PortShiftTable[ ]

This array contains a port and mask for every sensor. The PortMask data structure contains fields:

- Port – Defines the port number that pin belongs to.
- Shift – Defines pin within the port.

### CapSense\_ScanSlotTable[ ]

This array contains all scan slots in the CapSense system. The ScanSlot data structure contains fields:

- RawIndex – Contains the place in the SlotResult[] array where the raw data is placed after scanning. This field should not be edited manually to ensure proper CapSense component operation.
- IndexOffset – Contains offset for proximity sensor and generic widgets. This field should not be edited manually to ensure proper CapSense component operation.
- SnsCnt – This field contains the number of sensors in this scan slot. This field should not be edited manually to ensure proper CapSense component operation.
- WidgetNumber – Contains the widget that the sensor belongs to. If this field contains 0xFF, the current scan slot has no widget. Generic scan slots use 0xFF as their widget number. This field should not be edited manually to ensure proper CapSense component operation.
- DebounceCount – Contains the debounce counter. This field should not be edited manually to ensure proper CapSense component operation.

### CapSense\_SettingsTable[ ]

This array contains the settings of every scan slot in the CapSense system. The CSD\_Settings data structure contains the following fields:

- IdacRange – Contains the range selection parameter for the IDAC. This parameter is only available in configurations with an IDAC. The following constants are provided:

```
CapSense_IDAC_RANGE_32uA
CapSense_IDAC_RANGE_255uA
CapSense_IDAC_RANGE_2mA
```



**PRELIMINARY**

- **IdacSettings** – Contains the IDAC value. The capacitance measurement range depends on this parameter. A higher value indicates a wider range. Possible values are 1 to 255. This field is only available in configurations with an IDAC.
- **PrescalerPeriod** – Contains the value of the prescaler period register and determines the precharge switch output frequency. This parameter is only available on configurations with a prescaler. The prescaler period values can range from 1 to 255.
- **Resolution** – Contains the scanning resolution in bits. The sensors can be scanned with resolutions ranging from 8 to 16 bits. The maximum raw count for a scanning resolution for N bits is  $2^N - 1$ . The following constants are provided.

```
CapSense_PWM_RESOLUTION_8_BITS
CapSense_PWM_RESOLUTION_9_BITS
CapSense_PWM_RESOLUTION_10_BITS
CapSense_PWM_RESOLUTION_11_BITS
CapSense_PWM_RESOLUTION_12_BITS
CapSense_PWM_RESOLUTION_13_BITS
CapSense_PWM_RESOLUTION_14_BITS
CapSense_PWM_RESOLUTION_15_BITS
CapSense_PWM_RESOLUTION_16_BITS
```

- **ScanSpeed** – This field affects the sensors' scanning speed. The available selections are: Ultra Fast, Fast, Normal, and Slow. The following constants are provided:

```
CapSense_SCAN_SPEED_ULTRA_FAST
CapSense_SCAN_SPEED_FAST
CapSense_SCAN_SPEED_NORMAL
CapSense_SCAN_SPEED_SLOW
```

- **PrsPolynomial** – Contains the polynomial value for the PRS. This parameter is only available for configuration with the PRS.
- **Disable State** – Defines the unscanning sensor connection as GND, High Z, or Shield Electrode. The following constants are provided:

```
CapSense_DISABLE_STATE_GND
CapSense_DISABLE_STATE_HIGHZ
CapSense_DISABLE_STATE_SHIELD
```

Edit the `CapSense_SettingsTable[slot]` fields when scan slot parameters need to be changed before the next scan.

**PRELIMINARY**



```
CapSense_SettingsTable[CapSense_SCANSLOT_BTN_UP].IdacSettings = 200;
CapSense_SettingsTable[CapSense_SCANSLOT_BTN_UP].PrescalerPeriod = 21;
CapSense_SettingsTable[CapSense_SCANSLOT_BTN_UP].Resolution =
CapSense_PWM_RESOLUTION_12_BITS;
```

### **CapSense\_CSHL\_SlotBaselineLow[ ]**

This array holds the fractional byte of baseline data of each slot. The arrays size is equal to the total number of scan slots.

### **CapSense\_CSHL\_SlotBaseline[ ]**

This array holds the baseline data of each slot. The arrays size is equal to the total number of scan slots. The CapSense\_CSHL\_SlotBaseline[ ], CapSense\_CSHL\_SlotBaselineLow[ ], and CapSense\_CSHL\_SlotSignal[ ] arrays are updated by these functions:

- CapSense\_CSHL\_InitializeSlotBaseline()
- CapSense\_CSHL\_InitializeAllBaselines()
- CapSense\_CSHL\_UpdateSlotBaseline()
- CapSense\_CSHL\_UpdateAllBaselines()

### **CapSense\_CSHL\_SlotSignal[ ]**

This array holds the difference of count by subtracting the previous baseline and noise threshold from the current raw count value of each slot. The array size is equal to the total number of scan slots.

### **CapSense\_CSHL\_SlotOnMask[ ]**

This is a byte array that holds the slots in an ON or OFF state.

### **CapSense\_CSHL\_SlotOnMask[0]**

This array contains the masked bits for slots 0 through 7 (slot 0 is bit 0, slot 1 is bit 1). CapSense\_CSHL\_SlotOnMask[1] contains the masked bits for slots 8 through 15 (if needed), and so on. This byte array holds as many elements as are necessary to contain the total number of scan slots. The value of a bit is '1' if the button is ON and '0' if the button is OFF. The CapSense\_CSHL\_SlotOnMask[ ] data is updated by the following functions:

- CapSense\_CSHL\_CheckIsSlotActive()
- CapSense\_CSHL\_CheckIsAnySlotActive()

### **CapSense\_CSHL\_WidgetTable[ ]**

This array contains all widgets in the CapSense system. The WidgetTable data structure contains:



**PRELIMINARY**

- **Type** – Contains the type of widget. The types of widgets are: buttons, sliders (linear and radial), touch pads, matrix buttons, and proximity sensors. Generic sensors do not have high level APIs so there are no instances for them in the widget table. This field should not be edited manually to ensure proper CapSense component operation.
- **RawOffset** – Contains the start position of the widget in the CapSenseSlotResult[ ] array. This field should not be edited manually to ensure proper CapSense component operation.
- **ScanSlotCount** – Contains the number of scan slots within the widget. This field should not be edited manually to ensure proper CapSense component operation.
- **FingerThreshold** – Contains the software value that is used to determine if a finger is present on the sensor.
- **NoiseThreshold** – Contains a value that indicates the level of noise in the capacitive scan. The baseline algorithm tracks and filters the noise, but if the measured count value exceeds the noise threshold, the baseline algorithm stops updating.
- **Debounce** – Contains a debounce counter. In order for the sensor to transition from inactive to active, the difference count value must stay above the finger threshold plus the hysteresis value for the number of samples specified.
- **Hysteresis** – Contains the hysteresis value for the widget. If hysteresis is desired, the slot is not considered ON or active until the count value exceeds the finger threshold plus the hysteresis value. The slot is not considered OFF or inactive until the measured count value drops below the finger threshold minus the hysteresis value.
- **Filters** – Contains the bit field with raw counts and position filters. This field should not be edited manually for proper CapSense component operation.
- **AdvancedSettings** – Contains a pointer to the structure with advanced settings for the widget. Every widget has a different structure of advanced settings. This field should not be edited manually to ensure proper CapSense component operation.

When widget parameters need to be changed, edit the following CapSense\_CSHL\_WidgetTable[widget] fields:

```
CapSense_CSHL_WidgetTable[CapSense_CSHL_BTN_B1].FingerThreshold = 50;
CapSense_CSHL_WidgetTable[CapSense_CSHL_BTN_B1].NoiseThreshold = 10;
```

## Constants

Following are constant definitions. Some constants are defined conditionally and only present if necessary for the current configuration.

- **CapSense\_TOTAL\_SCANSLOT\_COUNT** – The total number of scan slots in the CapSense component.
- **CapSense\_TOTAL\_SCANSLOT\_COUNT\_LEFT** – The total number of scan slots in the left half of the CapSense component (exists only in parallel configuration).

**PRELIMINARY**





- CapSense\_TOTAL\_SCANSLOT\_COUNT\_RIGHT – The total number of scan slots in the right half of the CapSense component (exists only in parallel configuration).
- CapSense\_TOTAL\_GENERIC\_SCANSLOT\_COUNT – The total number of generic scan slots in the CapSense component.
- CapSense\_TOTAL\_GENERIC\_SCANSLOT\_COUNT\_LEFT – The total number of generic scan slots in the left half of the CapSense component (exists only in parallel configuration).
- CapSense\_TOTAL\_GENERIC\_SCANSLOT\_COUNT\_RIGHT - Total number of generic scan slots in the right half of the CapSense component (exists only in parallel configuration).

### Sensor Constants

A constant is provided for each sensor slot. These constants can be used as parameters in the following functions:

- CapSense\_CSD\_EnableSensor
- CapSense\_CSD\_DisableSensor

The constant names consist of:

*Instance Name* + **\_SENSOR** + *Widget Type* + *Widget Name* + *Element* + *Side*

### Widget Type

There are several widget types:

Alias	Description
BTN	Buttons
LS	Linear Sliders
RS	Radial Sliders
TP	Touch Pads
MB	Matrix Buttons
PROX	Proximity Sensors
GEN	Generic Sensors

- **Widget Name** – The user-defined name of the widget (must be a valid 'C' style identifier). The widget name must be unique within the widget type. For example, you can have a BTN\_MyName and a PROX\_MyName, but you may not have two aliases of BTN\_MyName.
- **Element Number** – The element number only exists for widgets that have multiple elements, such as radial sliders. For touch pads and matrix buttons, the element number



**PRELIMINARY**

consists of the word 'COL' or 'ROW' and its number 'COL\_0', 'COL\_1', 'ROW\_0', or 'ROW\_1'. For linear and radial sliders, the element number consists of the character 'e' and its number 'e\_0', 'e\_1', 'e\_2', or 'e\_3'.

- **Side** – In parallel configuration, sides are left and right. Sides do not exist in serial configuration.

For example:

```
/* Parallel configuration */
#define CapSense_SENSOR_TP_TP1_ROW_0_LEFT    0
#define CapSense_SENSOR_TP_TP1_ROW_1_LEFT    1
#define CapSense_SENSOR_TP_TP1_COL_0_LEFT    2
#define CapSense_SENSOR_PROX_PROX1_RIGHT     0

/* Serial configuration */
#define CapSense_SENSOR_TP_TP1_ROW_0         0
#define CapSense_SENSOR_TP_TP1_ROW_1         1
#define CapSense_SENSOR_TP_TP1_COL_0         2
#define CapSense_SENSOR_BTN_UP               3
```

## Scan Slot Constants

A constant is provided for each scan slot. These constants can be used as parameters in the following functions:

- CapSense\_CSD\_SetSlotSettings()
- CapSense\_CSD\_ScanSlot()
- CapSense\_CSD\_ReadSlot()
- CapSense\_CSHL\_InitializeSlotBaseline()
- CapSense\_CSHL\_UpdateSlotBaseline()
- CapSense\_CSHL\_CheckIsSlotActive()

The constant names consist of:

*Instance Name* + **\_SCANSLOT** + *Widget Type* + *Widget Name* + *Element* + *Side*

Examples:

```
/* Parallel configuration */
#define CapSense_SCANSLOT_TP_TP1_ROW_0_LEFT    0
#define CapSense_SCANSLOT_TP_TP1_ROW_1_LEFT    1
#define CapSense_SCANSLOT_TP_TP1_COL_0_LEFT    2
#define CapSense_SCANSLOT_PROX_PROX1_RIGHT     0

/* Serial configuration */
#define CapSense_SCANSLOT_TP_TP1_ROW_0         0
#define CapSense_SCANSLOT_TP_TP1_ROW_1         1
#define CapSense_SCANSLOT_TP_TP1_COL_0         2
#define CapSense_SCANSLOT_BTN_UP               3
```

**PRELIMINARY**



## Widget Constants

A constant is provided for each widget. These constants can be used as parameters in the following functions:

- CapSense\_CSHL\_GetCentroidPos()
- CapSense\_CSHL\_GetRadialCentroidPos()
- CapSense\_CSHL\_GetDoubleCentroidPos()

The constants consist of:

*Instance Name* + **\_CSHL** + *Widget Type* + *Widget Name*

For example:

```
#define CapSense_CSHL_BTN_B1      0
#define CapSense_CSHL_BTN_B2      1
#define CapSense_CSHL_RS_SL2      7
```

For widgets that have columns and rows (such as touch pad and matrix buttons), you add the word 'COL' or 'ROW' to the widget type. The constants look like the following examples:

```
#define CapSense_CSHL_TPCOL_TP1    0
#define CapSense_CSHL_TPROW_TP1    2
#define CapSense_CSHL_MBCOL_MB1    1
#define CapSense_CSHL_MBROW_MB1    3
```

The touch pad has separate #define to provide as argument to function CapSense\_CSHL\_GetDoubleCentroidPos():

```
#define CapSense_CSHL_TP_TP1      0
```

## Sample Firmware Source Code

The following is a 'C' language example that demonstrates the basic functionality of the CapSense component. This example assumes the component has been placed in a design with the default name "CapSense\_1."

**Note** If you rename your component, you must also edit the example code as appropriate to match the renamed component you specify.

```
#include <device.h>

void main()
{
    CYGlobalIntEnable;

    /* Initialize LCD */
    LCD_Char_1_Start();
    LCD_Char_1_Position(0,0);
    LCD_Char_1_PrintString("B1      B2");

    /* Initialize the CapSense component */
```



**PRELIMINARY**

```

CapSense_1_Start();
CapSense_1_CSHL_InitializeAllBaselines();

/* Sensor Scanning Loop */
while(1)
{
    CapSense_1_CSD_ScanAllSlots();
    CapSense_1_CSHL_UpdateAllBaselines();

    /* Position LCD pointer to update button 1 status */
    LCD_Char_1_Position(1,0);

    /* Left button pressed */
    if (CapSense_1_CSHL_CheckIsSlotActive(CapSense_1_SCANSLOT_BTN_B1))
    {
        /* Action for button 1 active */
        LCD_Char_1_PrintString("On ");
    }
    else
    {
        /* Action for button 1 inactive */
        LCD_Char_1_PrintString("Off");
    }

    /* Position LCD pointer to update button 2 status */
    LCD_Char_1_Position(1,6);

    /* Check if B2 is pressed */
    if (CapSense_1_CSHL_CheckIsSlotActive(CapSense_1_SCANSLOT_BTN_B2))
    {
        /* Action for button 2 active */
        LCD_Char_1_PrintString("On ");
    }
    else
    {
        /* Action for button 2 inactive */
        LCD_Char_1_PrintString("Off");
    }
}
}

```

## Pin Assignments

The CapSense Customizer generates pin alias names for each of the CapSense sensors and support signals. These aliases are used to assign sensors and signals to physical pins on the PSoC® device. Assign sensors and signals to pins in the Pin Editor tab of the Design Wide Resources file view.

**PRELIMINARY**



## Sides

The analog routing matrix within the PSoC device is divided into two halves – left and right. Even numbered port pins are on the left side of the device and odd numbered port pins are on the right side.

For serial configuration sensing applications, sensor pins can be assigned to either side of the device. If the application uses a small number of sensors, assigning all sensor signals to one side of the device makes routing of analog resources more efficient.

In parallel configuration sensing applications, the CapSense component is capable of performing two simultaneous scans on two sets of hardware. Each of the two parallel circuits has a separate Cmod and Rb (as applicable), and its own set of sensor pins. One set occupies the right side of the device and the other occupies the left side. The signal name alias indicates with which side the signal is associated.

## Sensor Pins – CapSense\_cPort – Pin Assignment

Aliases are provided to associate sensor names with widget types and widget names in the CapSense Customizer.

The aliases for sensors are:

*Widget Type + Widget Name + Element Number*

## Widget Type

There are several widget types:

Alias	Description
BTN	Buttons
LS	Linear Sliders
RS	Radial Sliders
TP	Touch Pads
MB	Matrix Buttons
PROX	Proximity Sensors
GEN	Generic Sensors

## Widget Name

The user-defined name of the widget (must be a valid 'C' style identifier). The widget name must be unique within the widget type. For example, you can have a BTN\_MyName and a PROX\_MyName, but you may not have two aliases of BTN\_MyName.



**PRELIMINARY**

## Element Number

The element number only exists for widgets that have multiple elements, such as radial sliders. For touch pads and matrix buttons, the element number consists of the word 'COL' or 'ROW' and its number 'COL\_0', 'COL\_1', 'ROW\_0', or 'ROW\_1'. For linear and radial sliders, the element number consists of the character 'e' and its number 'e\_0', 'e\_1', 'e\_2', or 'e\_3'.

**Note** In parallel configuration sensing applications, widget elements that belong to the left side of can only connect to even numbered (left side) ports. Widget elements that belong to the right side can only connect to odd numbered (right side) ports. **The Pin Editor does not verify correct pin assignment.**

**Note** The opamp outputs P0[0], P0[1], P3[6], and P3[7] have greater parasitic capacitance than other pins. This causes less finger response from P0[0], P0[1], P3[6], and P3[7] in CapSense applications.

## CapSense\_cCmod\_Port – Pin Assignment

One side of the external modulator capacitor (Cmod) should be connected to a physical pin and the other to GND. Parallel configurations require two Cmod capacitors, one for the left side and one for the right side. The Cmod can be connected to **any pin**, but for direct connection (do not overuse routing resources) use pins:

- Left Side: P2[0], P2[4], P6[0], P6[4], P15[4]
- Right Side: P1[0], P1[4], P5[0], P5[4]

The aliases for the Cmod capacitors are:

Alias	Description
sCmod	Cmod for serial configuration applications.
lCmod	Left Cmod in parallel configuration applications.
rCmod	Right Cmod in parallel configuration applications.

The recommended value for the modulator capacitor is 4.7 – 47 nF. The optimal capacitance can be selected through experimentation to get maximum SNR (signal-to-noise ratio). A value of 5.6 – 10 nF yields good results in most cases.

A ceramic capacitor should be used. The temperature capacitance coefficient is not important.

When the *CSD*, *IDAC disable*, *use external Rb* configuration is being used, the external Rb feedback resistor value should be selected before experimenting to determine the optimal Cmod value.

PRELIMINARY



## CapSense\_cRb\_Ports – Pin Assignment

An external bleed resistor (Rb) is required when the *CSD, IDAC disable, use external Rb* configuration is selected. The Rb should be connected to a physical pin and to the modulator capacitor (Cmod).

Up to three bleed resistors are supported. The following three pins can be allocated for bleed resistors: cRb0, cRb1, and cRb2.

The aliases for the external bleed resistors are:

*Side + **Rb** + Number*

Alias	Description
Side	Either 'l' for the left side or 'r' for the right side. The side prefix 's' is used in serial configuration applications.
Number	Multiple bleed resistors on the same side are given a sequence number 0, 1, or 2. For example, rRb0, rRb1, rRb2...

The resistor values depend on the total sensor capacitance. The resistor value should be selected as follows:

- Monitor the raw counts for different sensor touches.
- Select a resistance value that provides maximum readings about 30% less than the full-scale readings at the selected scanning resolution. The raw counts increase when resistor values increase.

Typical values are 500Ω – 10 kΩ depending on sensor capacitance.

## CapSense\_cShield\_Port – Pin Editor

Shield electrodes are available only in the *CSD, IDAC sinking*, and *CSD, IDAC disable, use external Rb* configurations. Shield electrodes (Shield) should be connected to a physical pin and a shield electrode layer on the board.

The aliases for shield electrodes are:

*Side + **Shield** + Number*

Alias	Description
Side	Either 'l' for the left side or 'r' for the right side. The side 's' is used in serial configuration applications.
Number	Multiple shield electrodes on the same side are given a sequence number 0, 1, or 2. For example, rShield0, rShield1, rShield2...

**Note** The maximum quantity of sensors, shield-electrodes, and bleed resistors is 62.

**Note** For proper shield electrode operation in IDAC source mode, the reference voltage should be set to Vdac with Vdac value of 255.



**PRELIMINARY**

## Interrupt Service Routines

The CapSense component uses an interrupt that triggers the ISR after the end of the sensor scan. Stub routines are provided where you can add your own code. The stub routines are generated in the *CapSense\_INT.c* file the first time the project is built. The number of interrupts depends on CapSense configuration selection: one interrupt for serial and two interrupts for parallel. The conditional compilation is used to provide one stub routine for serial and parallel configurations. Your code must be added between the provided comment tags as follows:

### Serial Configuration

```
CY_ISR(CapSense_1_ISR)
{
    /* Place your Interrupt code here. */
    /* `#START CapSense_1_ISR` */

    /* `#END` */
    CapSense_1_status &= ~CapSense_1_START_CAPSENSING;

    #if(CYDEV_CHIP_DIE_EXPECT == CYDEV_CHIP_DIE_LEOPARD)
        #if defined(CapSense_1_CSD_METHOD)
            #if((CYDEV_CHIP_REV_EXPECT <= CYDEV_CHIP_REV_LEOPARD_ES2) &&
(CapSense_1_sbCSD_cISR_ES2_PATCH))
                CapSense_1_ISR_PATCH();
            #endif
        #endif
        #if defined(CapSense_1_CSA_METHOD)
            #if((CYDEV_CHIP_REV_EXPECT <= CYDEV_CHIP_REV_LEOPARD_ES2) &&
(CapSense_1_sbCSA_cISR_ES2_PATCH))
                CapSense_1_ISR_PATCH();
            #endif
        #endif
    #endif
}
```

### Parallel Configuration (Left and Right Sides)

```
CY_ISR(CapSense_1_ISRLeft)
{
    /* Place your Interrupt code here. */
    /* `#START CapSense_1_ISRLeft` */

    /* `#END` */
    CapSense_1_statusLeft &= ~CapSense_1_START_CAPSENSING;

    #if(CYDEV_CHIP_DIE_EXPECT == CYDEV_CHIP_DIE_LEOPARD)
        #if defined(CapSense_1_CSD_METHOD_LEFT)
            #if((CYDEV_CHIP_REV_EXPECT <= CYDEV_CHIP_REV_LEOPARD_ES2) &&
(CapSense_1_lbCSD_cISR_ES2_PATCH))
                CapSense_1_ISR_PATCH();
            #endif
        #endif
        #if defined(CapSense_1_CSA_METHOD_LEFT)
            #if((CYDEV_CHIP_REV_EXPECT <= CYDEV_CHIP_REV_LEOPARD_ES2) &&
(CapSense_1_lbCSA_cISR_ES2_PATCH))
                CapSense_1_ISR_PATCH();
            #endif
        #endif
    #endif
}
```

**PRELIMINARY**





```

        #if((CYDEV_CHIP_REV_EXPECT <= CYDEV_CHIP_REV_LEOPARD_ES2) &&
(CapSense_1_lbCSA_cISR_ES2_PATCH))
            CapSense_1_ISR_PATCH();
        #endif
    #endif
#endif
}

CY_ISR(CapSense_1_ISRRight)
{
    /* Place your Interrupt code here. */
    /* `#START CapSense_1_ISRRight` */

    /* `#END` */
    CapSense_1_statusRight &= ~CapSense_1_START_CAPSENSING;

    #if(CYDEV_CHIP_DIE_EXPECT == CYDEV_CHIP_DIE_LEOPARD)
        #if defined(CapSense_1_CSD_METHOD_RIGHT)
            #if((CYDEV_CHIP_REV_EXPECT <= CYDEV_CHIP_REV_LEOPARD_ES2) &&
(CapSense_1_rbCSD_cISR_ES2_PATCH))
                CapSense_1_ISR_PATCH();
            #endif
        #endif
        #if defined(CapSense_1_CSA_METHOD_RIGHT)
            #if((CYDEV_CHIP_REV_EXPECT <= CYDEV_CHIP_REV_LEOPARD_ES2) &&
(CapSense_1_rbCSA_cISR_ES2_PATCH))
                CapSense_1_ISR_PATCH();
            #endif
        #endif
    #endif
}

```

## Functional Description

### Definitions

#### Sensor

One CapSense element connected to PSoC via one pin. A sensor is a conductive element on a substrate. Examples of sensors include copper on FR4, copper on flex, and silver ink on PET.

#### Scan Slot

A scan slot is a period of time that the CapSense component scans one or more capacitive sensors. Multiple sensors can be combined in a given scan slot to enable widget types such as matrix buttons or proximity sensing.



**PRELIMINARY**

## CapSense Widget

A CapSense widget is built from one or more scan slots. Examples of CapSense widgets include buttons, linear sliders, radial sliders, touch pads, matrix buttons, and proximity sensors.

## FingerThreshold

This value is used to determine if a finger is present on the sensor or not.

## NoiseThreshold

This value determines the level of noise in the capacitive scan. The baseline algorithm tracks and filters the noise. If the measured count value exceeds the noise threshold, the baseline algorithm stops updating.

## Debounce

Adds a debounce counter that must expire before the sensor transitions to active. In order for the sensor to transition from inactive to active, the difference count value must stay above the finger threshold plus the hysteresis value for the number of samples specified.

## Hysteresis

Sets the hysteresis value used with the finger threshold. If hysteresis is desired, the slot is not considered ON or active until the count value exceeds the finger threshold PLUS the hysteresis value. The slot is not considered OFF or inactive until the measured count value drops below the finger threshold MINUS the hysteresis value.

## Resolution – Interpolation and Scaling

In applications for sliding sensors and touch pads, it is often necessary to determine finger (or other capacitive object) position at higher resolution than the native pitch of the individual sensors. The contact area of a finger on a sliding sensor or a touch pad is often larger than any single sensor.

In order to calculate the interpolated position using a centroid, the array is first scanned to verify that a given sensor location is valid. The requirement is for some number of adjacent sensor signals to be above a noise threshold. When the strongest signal is found, this signal and those contiguous signals larger than the noise threshold are used to compute a centroid. As few as two and as many as (typically) eight sensors are used to calculate the centroid in the form of:

$$N_{Cent} = \frac{n_{i-1}(i-1) + n_i i + n_{i+1}(i+1)}{n_{i-1} + n_i + n_{i+1}}$$

The calculated value is typically fractional. In order to report the centroid to a specific resolution, for example, a range of 0 to 100 for 12 sensors, the centroid value is multiplied by a calculated scalar. It is more efficient to combine the interpolation and scaling operations into a single

**PRELIMINARY**



calculation and report this result directly in the desired scale. This is handled in the high-level APIs.

Slider sensor count and resolution are set in the CSD Wizard. A scaling value is calculated by the wizard and stored as a fractional value.

The multiplier for the centroid resolution is contained in three bytes with the following bit definitions:

Resolution Multiplier MSB								
Bit	7	6	5	4	3	2	1	0
Multiplier	$2^{15}$	$2^{14}$	$2^{13}$	$2^{12}$	$2^{11}$	$2^{10}$	$2^9$	$2^8$
Resolution Multiplier ISB								
Multiplier	128	64	32	18	16	8	4	2
Resolution Multiplier LSB								
Multiplier	1/2	1/4	1/8	1/16	1/32	1/64	1/128	1/256

The resolution is found by using this equation:

$$\text{Resolution} = (\text{Number of Sensors} - 1) \times \text{Multiplier}$$

The centroid is held in a 24-bit unsigned integer and its resolution is a function of the number of sensors and the multiplier.

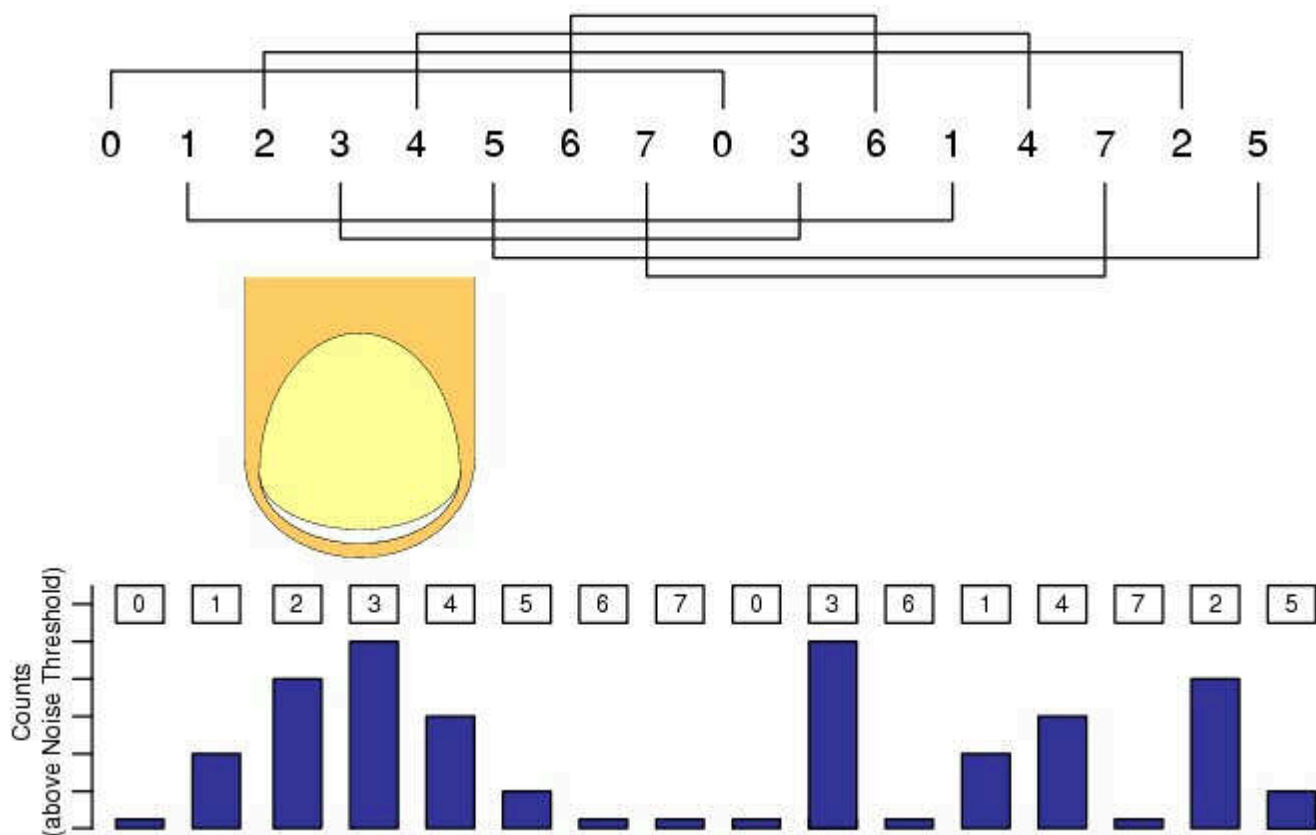
## Diplexing

In a diplexed slider, each PSoC sensor connection in the slider is mapped to two physical locations in the array of slider sensors. The first (or numerically lower) half of the physical location is mapped sequentially to the base assigned sensors, with the port pin assigned by the designer using the CapSense Customizer. The second (or upper) half of the physical sensor location is automatically mapped by an algorithm in the CapSense Customizer and listed in an include file. The order is established so that adjacent sensor actuation in one half does not result in adjacent sensor actuation in the other half. Exercise care to determine this order and map it onto the printed circuit board.

There are a number of methods to order the second half of the physical sensor locations. The simplest is to index the sensors in the upper half, first the even sensors followed by the odd sensors. Other methods include indexing by other values. The method selected for this component is to index by three.



**PRELIMINARY**

**Figure 1 Diplexing, Index by Three**

You should balance sensor capacitance in the slider. Depending on sensor or PCB layouts, there may be longer routes for some of the sensor pairs. The diplex sensor index table is automatically generated by the CapSense Customizer when you select diplexing.

### Diplexing Sequence for Different Slider Segment Counts

Total Slider Segment Count	Segment Sequence
10	0,1,2,3,4,0,3,1,4,2
12	0,1,2,3,4,5,0,3,1,4,2,5
14	0,1,2,3,4,5,6,0,3,6,1,4,2,5
16	0,1,2,3,4,5,6,7,0,3,6,1,4,7,2,5
18	0,1,2,3,4,5,6,7,8,0,3,6,1,4,7,2,5,8
20	0,1,2,3,4,5,6,7,8,9,0,3,6,9,1,4,7,2,5,8

**PRELIMINARY**



22	0,1,2,3,4,5,6,7,8,9,10,0,3,6,9,1,4,7,10,2,5,8
24	0,1,2,3,4,5,6,7,8,9,10,11,0,3,6,9,1,4,7,10,2,5,8,11
26	0,1,2,3,4,5,6,7,8,9,10,11,12,0,3,6,9,12,1,4,7,10,2,5,8,11
28	0,1,2,3,4,5,6,7,8,9,10,11,12,13,0,3,6,9,12,1,4,7,10,13,2,5,8,11
30	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,0,3,6,9,12,1,4,7,10,13,2,5,8,11,14
32	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,0,3,6,9,12,15,1,4,7,10,13,2,5,8,11,14
34	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,0,3,6,9,12,15,1,4,7,10,13,16,2,5,8,11,14
36	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,0,3,6,9,12,15,1,4,7,10,13,16,2,5,8,11,14,17
38	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,0,3,6,9,12,15,18,1,4,7,10,13,16,2,5,8,11,14,17
40	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,0,3,6,9,12,15,18,1,4,7,10,13,16,19,2,5,8,11,14,17
42	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,0,3,6,9,12,15,18,1,4,7,10,13,16,19,2,5,8,11,14,17,20
44	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,0,3,6,9,12,15,18,21,1,4,7,10,13,16,19,2,5,8,11,14,17,20
46	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,0,3,6,9,12,15,18,21,1,4,7,10,13,16,19,22,2,5,8,11,14,17,20
48	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,0,3,6,9,12,15,18,21,1,4,7,10,13,16,19,22,2,5,8,11,14,17,20,23
50	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,0,3,6,9,12,15,18,21,24,1,4,7,10,13,16,19,22,2,5,8,11,14,17,20,23
52	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,0,3,6,9,12,15,18,21,24,1,4,7,10,13,16,19,22,25,2,5,8,11,14,17,20,23
54	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,0,3,6,9,12,15,18,21,24,1,4,7,10,13,16,19,22,25,2,5,8,11,14,17,20,23,26
56	0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,25,26,27,0,3,6,9,12,15,18,21,24,27,1,4,7,10,13,16,19,22,25,2,5,8,11,14,17,20,23,26

## Filters

Several filters are provided in the CapSense component: median, averaging, first order IIR, and jitter. The filters are divided into two categories: position and raw data. (The order of the filters is shown ahead.)

### Position Median Filter

The position median filter looks at the three most recent samples of position and reports the median value. It is used to remove short noise spikes. This filter generates a delay of one



**PRELIMINARY**

sample. It is generally not recommended because of the delay and RAM use. Enabling this filter consumes TBD Flash and 4 bytes of RAM. It is disabled by default.

### Position Averaging Filter

The median filter looks at the three most recent samples of position and reports the averaging value. It is used to remove short noise spikes. This filter generates a delay of one sample. It is generally not recommended because of the delay and RAM use. Enabling this filter consumes TBD Flash and 4 bytes of RAM. It is disabled by default.

### Position First Order IIR Filter

The first order IIR filter looks at the two most recent samples of position and adds them with defined coefficients. Enabling this filter consumes TBD Flash and 2 bytes of RAM. It is disabled by default.

First order IIR filter with selectable coefficients:

$$\text{IIR} = \frac{1}{2} \text{ previous} + \frac{1}{2} \text{ current (default)}$$

$$\text{IIR} = \frac{3}{4} \text{ previous} + \frac{1}{4} \text{ current}$$

### Position Jitter Filter

This filter eliminates noise in the position that toggles between two values (jitter). It is most effective when applied to data that contains noise of four LSBs peak-to-peak or less. Enabling this filter consumes TBD Flash and 2 bytes of RAM. It is disabled by default.

### Raw Data Median Filter

The raw data median filter looks at the three most recent samples from a sensor and reports the median value. It is used to remove short noise spikes. This filter generates a delay of one sample. This filter is generally not recommended because of the delay and RAM use. Enabling this filter consumes TBD Flash and (number of sensors × 4) bytes of RAM. It is disabled by default.

### Raw Data Averaging Filter

The median filter looks at the three most recent samples from a sensor and reports the averaging value. It is used to remove short noise spikes. This filter generates a delay of one sample. It is generally not recommended because of the delay and RAM use. Enabling this filter consumes TBD Flash and (number of sensors × 4) bytes of RAM. It is disabled by default.

### Raw First Order IIR Filter

The first order IIR filter looks at the two most recent samples from a sensor and adds them with defined coefficients. Enabling this filter consumes TBD Flash and (number of sensors × 2) bytes of RAM. It is disabled by default.

**PRELIMINARY**



First order IIR filter with selectable coefficients:

$$\text{IIR} = \frac{1}{2} \text{ previous} + \frac{1}{2} \text{ current (default)}$$

$$\text{IIR} = \frac{3}{4} \text{ previous} + \frac{1}{4} \text{ current}$$

### Raw Data Jitter Filter

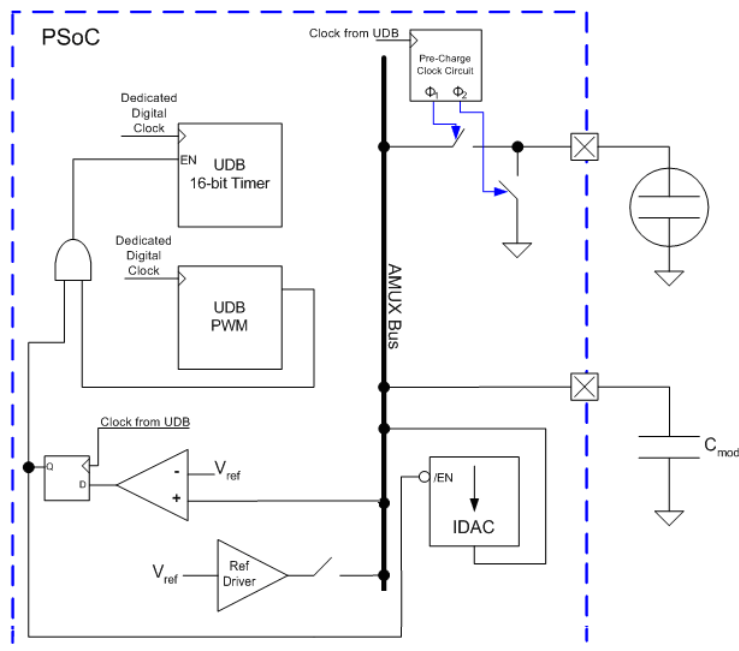
This filter eliminates noise in the raw data that toggles between two values (jitter). It is most effective when applied to data that contains noise of four LSBs peak-to-peak or less. Enabling this filter consumes TBD Flash and (number of sensors × 2) bytes of RAM. It is disabled by default.

### Block Diagram and Configuration

The CSD (Capacitive Sensing using a Sigma Delta Modulator) provides capacitance sensing using the switched capacitor technique with a sigma delta modulator to convert the sensing switched capacitor current to digital code. It allows implementation of buttons, sliders, touch pads, and touchscreens using arrays of conductive sensors. High-level software routines allow for enhancement of slider resolution using diplexing, and compensation for physical and environmental sensor variation.

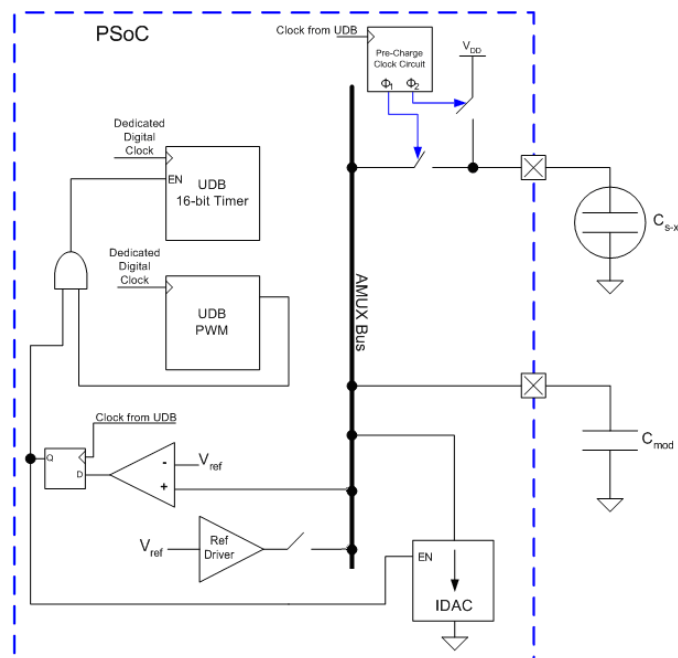
### IDAC Sourcing

The switch stage is reconfigured to alternate between GND and the AMUX bus. In this configuration, the IDAC is configured to source current and to the AMUX bus.



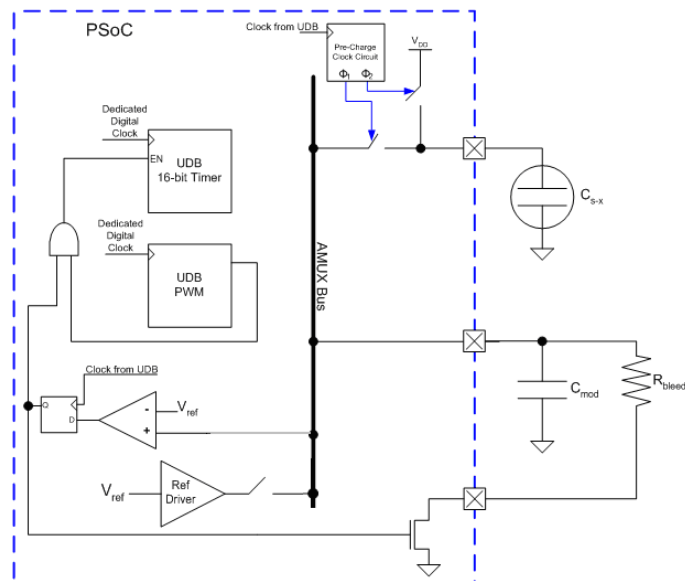
## IDAC Sinking

The switch stage is reconfigured to alternate between Vdd and the AMUX bus. In this configuration, the IDAC is configured to sink current instead of sourcing current.



### IDAC Disable, use External $R_b$

The *DAC disable, use external Rb* configuration is the same as the IDAC sinking configuration except that the IDAC is replaced by resistor to ground, Rb. The bleed resistor is physically connected between Cmod and a GPIO. The GPIO is configured in the Open Drain Drives Low drive mode. This drive mode allows Cmod to be discharged through Rb.



# PRELIMINARY





## DC and AC Electrical Characteristics

### 5.0V/3.3V DC and AC Electrical Characteristics

Parameter	Typical	Min	Max	Units	Conditions and Notes
Input					
Input Voltage Range	–		Vss to Vdd	V	
Input Capacitance	–		–	pF	
Input Impedance	–		–	$\Omega$	
Maximum Clock Rate	–		67	MHz	

## Component Changes

This section lists the major changes in the component from the previous version.

Version	Description of Changes	Reason for Changes / Impact
1.30.b	Added information to the component that advertizes its compatibility with silicon revisions.	The tool reports an error/warning if the component is used on incompatible silicon. If this happens, update to a revision that supports your target device.
1.30.a	Moved local parameters to formal parameter list.	To address a defect that existed in PSoC Creator v1.0 Beta 4.1 and earlier, the component was updated so that it could continue to be used in newer versions of the tool. This component used local parameters to do background calculations on user input. These parameters have been changed to formal parameters which are visible, but un-editable. There are no functional changes to the component but the affected parameters are now visible in the “expression view” of the Configure dialog.
1.30	Modified the Configure dialog.	Added the Vref and Vdac features in the Configure dialog. Fixed an issue with the Custom check box in the Scan Slot tab. Fixed filters for raw counts and position for different widgets (BM, TP, RS). Add to Average and IIR filter direct type conversion to uint32, and to not enter borders of uint16.

**Note** CapSense version 1.30 supports PSoC 3 ES2 silicon revision. Use CapSense\_CSD version 2.0 for PSoC 3 ES3 silicon revisions and above.



**PRELIMINARY**

© Cypress Semiconductor Corporation, 2009-2010. The information contained herein is subject to change without notice. Cypress Semiconductor Corporation assumes no responsibility for the use of any circuitry other than circuitry embodied in a Cypress product. Nor does it convey or imply any license under patent or other rights. Cypress products are not warranted nor intended to be used for medical, life support, life saving, critical control or safety applications, unless pursuant to an express written agreement with Cypress. Furthermore, Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress products in life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

PSoC® and CapSense® are registered trademarks, and PSoC Creator™ and Programmable System-on-Chip™ are trademarks of Cypress Semiconductor Corp. All other trademarks or registered trademarks referenced herein are property of the respective corporations.

Any Source Code (software and/or firmware) is owned by Cypress Semiconductor Corporation (Cypress) and is protected by and subject to worldwide patent protection (United States and foreign), United States copyright laws and international treaty provisions. Cypress hereby grants to licensee a personal, non-exclusive, non-transferable license to copy, use, modify, create derivative works of, and compile the Cypress Source Code and derivative works for the sole purpose of creating custom software and or firmware in support of licensee product to be used only in conjunction with a Cypress integrated circuit as specified in the applicable agreement. Any reproduction, modification, translation, compilation, or representation of this Source Code except as specified above is prohibited without the express written permission of Cypress.

Disclaimer: CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS MATERIAL, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Cypress reserves the right to make changes without further notice to the materials described herein. Cypress does not assume any liability arising out of the application or use of any product or circuit described herein. Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress' product in a life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

Use may be limited by and subject to the applicable Cypress software license agreement.

**PRELIMINARY**

