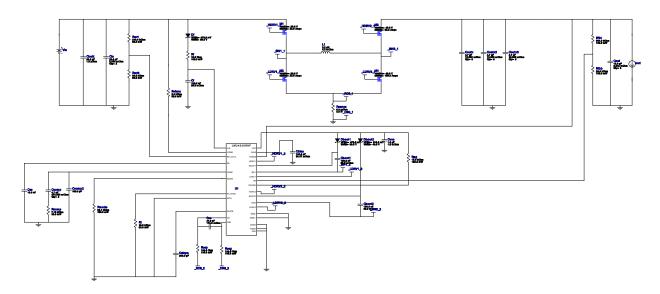


VinMin = 10.0V VinMax = 15.0V Vout = 25.0V Iout = 5.0A Device = LM34936RHFR Topology = Buck_Boost Created = 2025-09-17 15:03:00.982 BOM Cost = NA BOM Count = 54 Total Pd = 8.87W

WEBENCH® Design Report

Design: 7 LM34936RHFR LM34936RHFR 10V-15V to 25.00V @ 5A

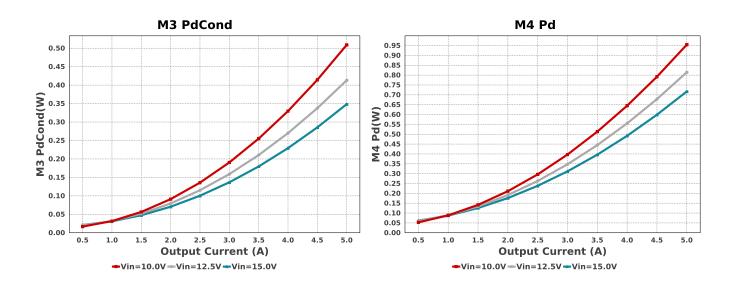


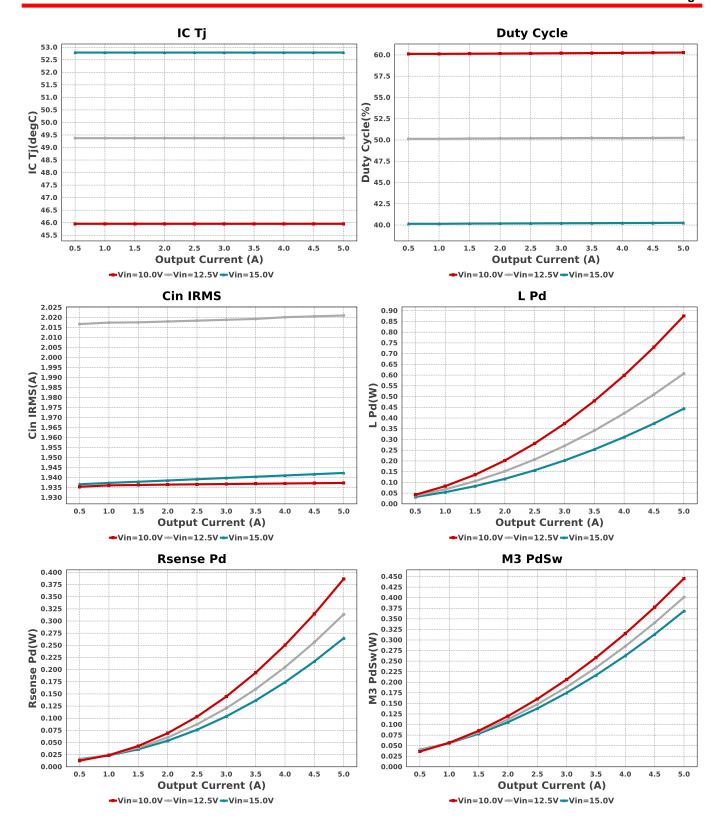
Electrical BOM

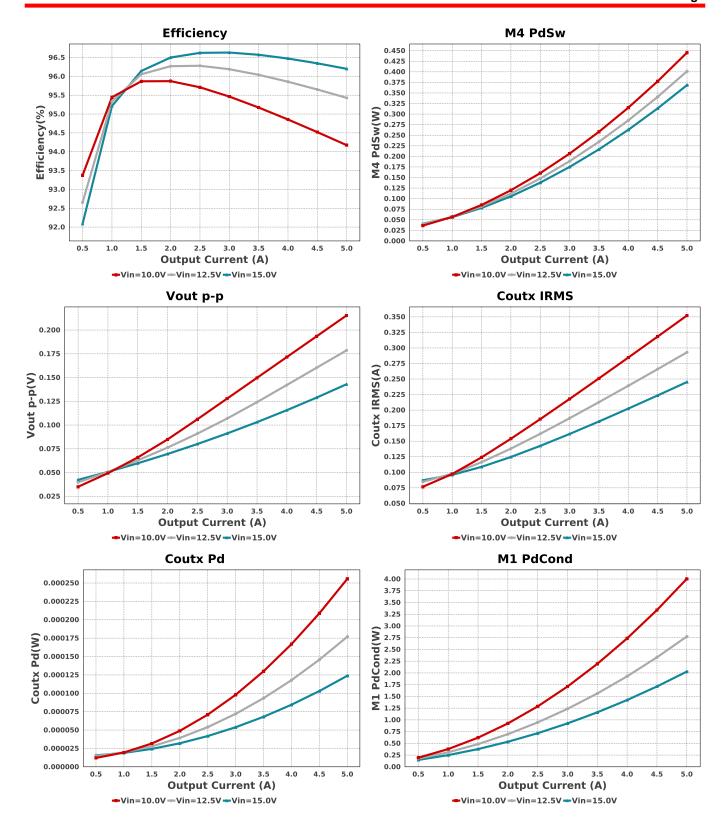
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbias	Kemet	C0805C104M5RACTU Series= X7R	Cap= 100.0 nF ESR= 35.47 mOhm VDC= 50.0 V IRMS= 1.64 A	1	\$0.01	0805 7 mm ²
Cboot1	AVX	06033C104KAT2A Series= X7R	Cap= 100.0 nF ESR= 50.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Cboot2	AVX	06033C104KAT2A Series= X7R	Cap= 100.0 nF ESR= 50.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Cbulk	CUSTOM	CUSTOM Series= ?	Cap= 68.0 uF ESR= 1.0 uOhm VDC= 21.429 V	1	NA	CUSTOM 0 mm ²
Ccomp	TDK	CGA2B2X7R1H472K050BA Series= X7R	Cap= 4.7 nF ESR= 413.51 mOhm VDC= 50.0 V IRMS= 330.88 mA	2	\$0.01	0402 3 mm ²
Ccomp2	Taiyo Yuden	UMK105CG151JV-F Series= C0G/NP0	Cap= 150.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccs	AVX	06035A470JAT2A Series= C0G/NP0	Cap= 47.0 pF ESR= 174.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Cf	TDK	CGA3E2X7R1H104K080AA Series= X7R	Cap= 100.0 nF ESR= 29.6 mOhm VDC= 50.0 V IRMS= 971.99 mA	1	\$0.01	0603 5 mm ²
Cin	Taiyo Yuden	MSAST32MSB7226KPNB25 Series= X7R	Cap= 22.0 uF ESR= 3.298 mOhm VDC= 25.0 V IRMS= 3.7481 A	2	\$0.18	1210 15 mm ²

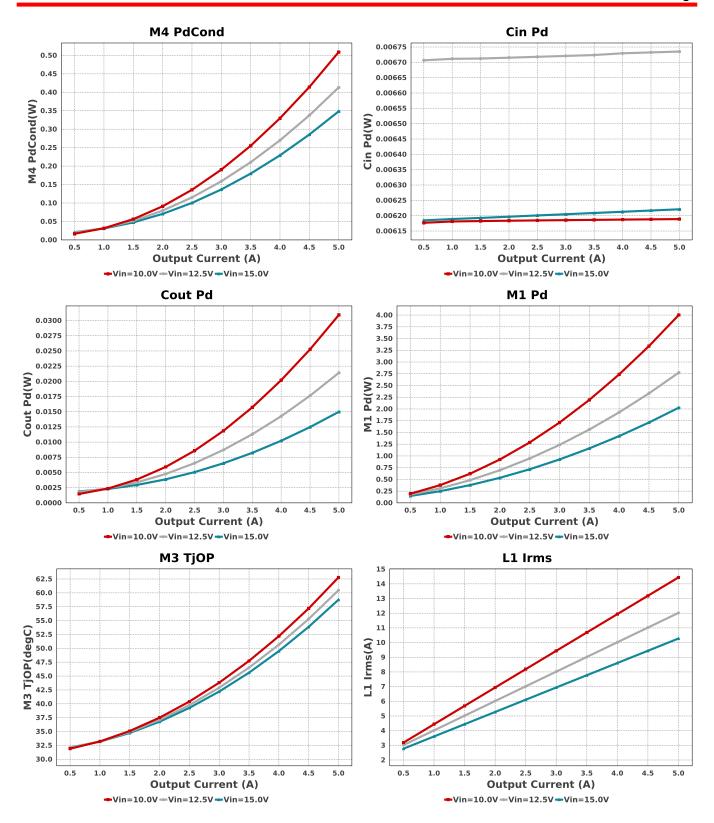
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cout	TDK	C5750X7S2A156M250KB Series= X7S	Cap= 15.0 uF ESR= 2.642 mOhm VDC= 100.0 V IRMS= 5.6162 A	3	\$1.13	2220_280 54 mm ²
Coutx	Taiyo Yuden	MSASU21GBB5475KTNA01 Series= X5R	Cap= 4.7 uF ESR= 10.306 mOhm VDC= 50.0 V IRMS= 2.1789 A	5	\$0.09	0805 7 mm ²
Coutx2	Taiyo Yuden	MSASU21GBB5475KTNA01 Series= X5R	Cap= 4.7 uF ESR= 10.306 mOhm VDC= 50.0 V IRMS= 2.1789 A	5	\$0.09	0805 7 mm ²
Coutx3	Taiyo Yuden	MSASU21GBB5475KTNA01 Series= X5R	Cap= 4.7 uF ESR= 10.306 mOhm VDC= 50.0 V IRMS= 2.1789 A	5	\$0.09	0805 7 mm ²
Cslope	Samsung Electro- Mechanics	CL10C201JB8NNNC Series= C0G/NP0	Cap= 200.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Css	Kemet	C0603C153J3GACTU Series= C0G/NP0	Cap= 15.0 nF VDC= 25.0 V IRMS= 0.0 A	1	\$0.09	0603 5 mm ²
Cvcc	Taiyo Yuden	EMK107B7105KA-T Series= X7R	Cap= 1.0 uF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Dboot1	Torex USA Corporation	XBS053V15R-G	VF@Io= 470.0 mV VRRM= 30.0 V	1	\$0.15	SOD-523 5 mm ²
Dboot2	Comchip Technology	CDBK0540-HF	VF@Io= 510.0 mV VRRM= 40.0 V	1	\$0.07	SOD-123F 12 mm ²
Df	Torex USA Corporation	XBS053V15R-G	VF@Io= 470.0 mV VRRM= 30.0 V	1	\$0.15	SOD-523 5 mm ²
L1	Bourns	SRP1270-2R2M	L= 2.2 μH 4.2 mOhm	1	\$0.83	SRP1270 246 mm ²
M1	Texas Instruments	CSD15571Q2	VdsMax= 20.0 V IdsMax= 22.0 Amps	1	\$0.12	DQK0006C 9 mm²
M2	Texas Instruments	CSD17571Q2	VdsMax= 30.0 V IdsMax= 22.0 Amps	1	\$0.12	DQK0006C 9 mm²
M3	Texas Instruments	CSD18514Q5A	VdsMax= 40.0 V IdsMax= 50.0 Amps	1	\$0.28	TRANS_NexFET_Q5A 55 mm²
M4	Texas Instruments	CSD18511Q5A	VdsMax= 40.0 V IdsMax= 100.0 Amps	1	\$0.40	TRANS_NexFET_Q5A 55 mm²
Rcomp	Vishay-Dale	CRCW04027K15FKED Series= CRCWe3	Res= 7.15 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rcsg	Vishay-Dale	CRCW0603100RFKEA Series= CRCWe3	Res= 100.0 Ohm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rcsp	Vishay-Dale	CRCW0603100RFKEA Series= CRCWe3	Res= 100.0 Ohm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²

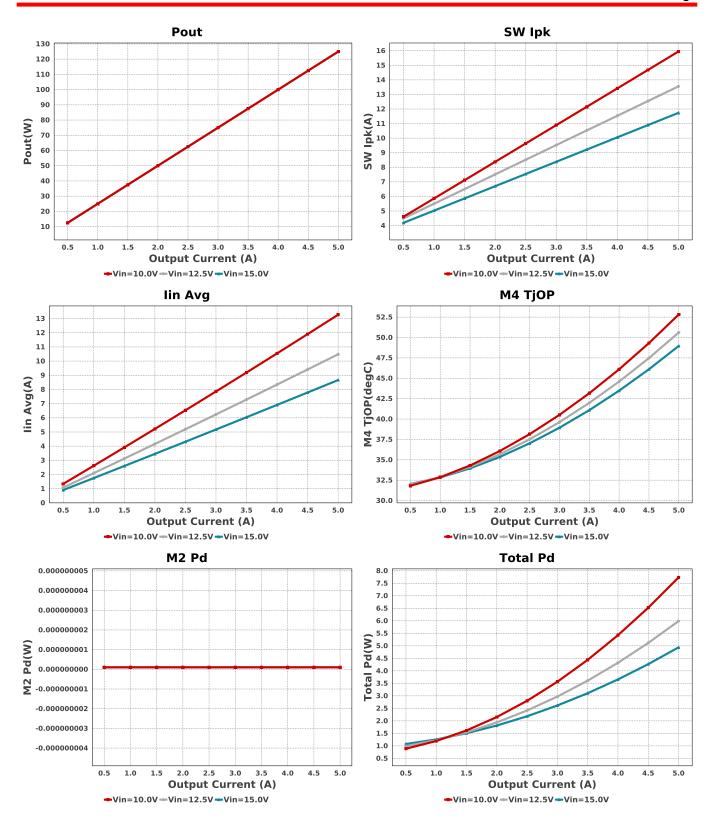
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Rf	Yageo	RC0603FR-0710RL Series= ?	Res= 10.0 Ohm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rfbb	Yageo	RC0603FR-0720KL Series= ?	Res= 20.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rfbt	Yageo	RC0603FR-07604KL Series= ?	Res= 604.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rmode	Yageo	RC0603FR-0793K1L Series= ?	Res= 93.1 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rpg	Yageo	RC0603FR-0710KL Series= ?	Res= 10.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
Rsense	Vishay-Dale	WSR36L000FEA Series= WSR	Res= 6.0 mOhm Power= 3.0 W Tolerance= 1.0%	1	\$0.72	4527 122 mm ²
Rt	Vishay-Dale	CRCW040219K6FKED Series= CRCWe3	Res= 19.6 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Ruvb	Yageo	AC0402FR-0736K5L Series=?	Res= 36.5 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Ruvt	Vishay-Dale	CRCW0402249KFKED Series= CRCWe3	Res= 249.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
Rvisns	Vishay-Dale	CRCW04022K00FKED Series= CRCWe3	Res= 2.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
U1	Texas Instruments	LM34936RHFR	Switcher	1	\$2.76	RHF0028A 42 mm ²

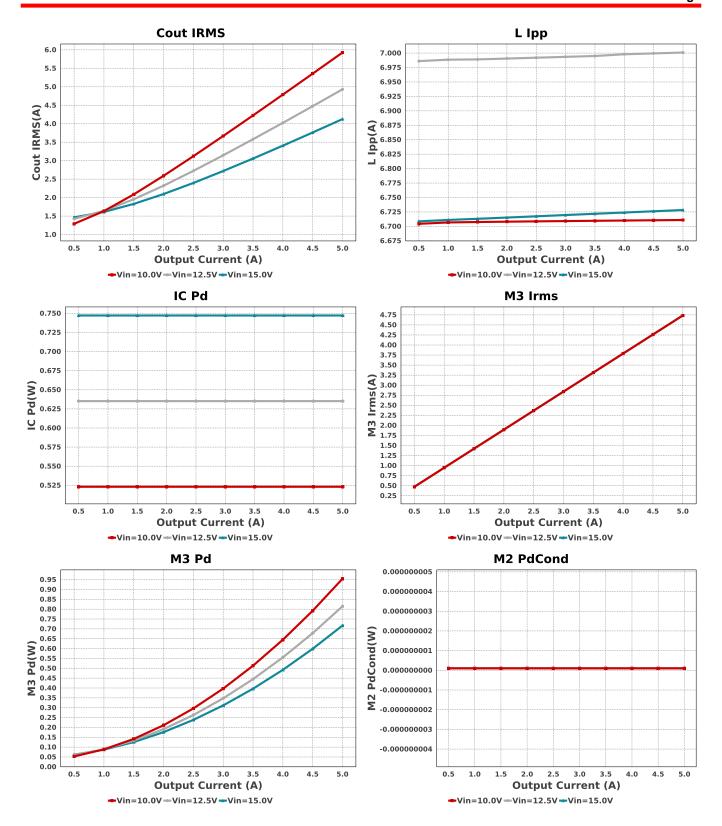


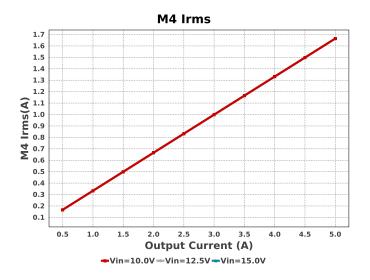


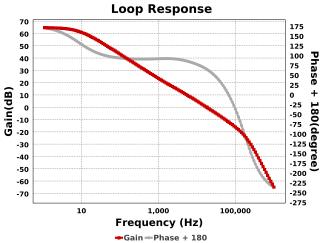












Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	1.934 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	6.169 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	6.011 A	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	31.821 mW	Capacitor	Output capacitor power dissipation
5.	Coutx IRMS	350.461 mA	Capacitor	Output capacitor_x RMS ripple current
6.	Coutx Pd	253.16 μW	Capacitor	Output capacitor_x power loss
7.	IC Pd	747.19 mW	IC	IC power dissipation
8.	IC Tj	52.789 degC	IC	IC junction temperature
9.	IC Tolerance	0.0 V	IC	IC Feedback Tolerance
10.	ICThetaJA	30.5 degC/W	IC	IC junction-to-ambient thermal resistance
11.	lin Avg	13.387 Å	IC	Average input current
	L lpp	6.7 A	Inductor	Peak-to-peak inductor ripple current
	L Pd	875.06 mW	Inductor	Inductor power dissipation
	L1 Irms	14.434 A	Inductor	Inductor ripple current
	M1 Pd	4.0 W	Mosfet	M1 MOSFET total power dissipation
-	M1 PdCond	4.0 W	Mosfet	M1 MOSFET conduction losses
17.		100.0 pW	Mosfet	M2 MOSFET total power dissipation
	M2 PdCond	100.0 pW	Mosfet	M2 MOSFET conduction losses
	M3 Irms	4.994 A	Mosfet	MOSFET RMS ripple current
-	M3 Pd	1.313 W	Mosfet	MOSFET power dissipation
	M3 PdCond			·
		766.72 mW	Mosfet	M1 MOSFET conduction losses
	M3 PdSw	546.62 mW	Mosfet	M1 MOSFET switching losses
	M3 ThetaJA	50.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
	M3 TjOP	69.347 degC	Mosfet	MOSFET junction temperature
	M4 Irms	2.3 A	Mosfet	MOSFET RMS ripple current
-	M4 Pd	1.313 W	Mosfet	MOSFET power dissipation
	M4 PdCond	766.72 mW	Mosfet	M2 MOSFET conduction losses
28.	M4 PdSw	546.62 mW	Mosfet	M2 MOSFET switching losses
	M4 ThetaJA	50.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
	M4 TjOP	58.42 degC	Mosfet	MOSFET junction temperature
	Cin Pd	6.169 mW	Power	Input capacitor power dissipation
-	Cout Pd	31.821 mW	Power	Output capacitor power dissipation
	Coutx Pd	253.16 μW	Power	Output capacitor_x power loss
34.	IC Pd	747.19 mW	Power	IC power dissipation
35.	L Pd	875.06 mW	Power	Inductor power dissipation
36.	M1 Pd	4.0 W	Power	M1 MOSFET total power dissipation
37.	M1 PdCond	4.0 W	Power	M1 MOSFET conduction losses
38.	M2 Pd	100.0 pW	Power	M2 MOSFET total power dissipation
39.	M2 PdCond	100.0 pW	Power	M2 MOSFET conduction losses
40.	M3 Pd	1.313 W	Power	MOSFET power dissipation
41.	M3 PdCond	766.72 mW	Power	M1 MOSFET conduction losses
	M3 PdSw	546.62 mW	Power	M1 MOSFET switching losses
43.	M3 Rdson	7.9 mOhm	Power	Drain-Source On-resistance
	M4 Pd	1.313 W	Power	MOSFET power dissipation
45.		766.72 mW	Power	M2 MOSFET conduction losses
46.	M4 PdSw	546.62 mW	Power	M2 MOSFET switching losses
	M4 Rdson	3.5 mOhm	Power	Drain-Source On-resistance
47. 48.		582.32 mW	Power	LED Current Rsns Power Dissipation
40. 49.	Total Pd	8.87 W	Power	Total Power Dissipation
-				•
50.	Rsense Pd	582.32 mW	Resistor	LED Current Rsns Power Dissipation
51.	BOM Count	54	System Information	Total Design BOM count

System System System System System System System System Information System Information System System Steady state efficiency System Steady state efficiency Steady sta	#	Nama	Value	Cotogory	Description
Information System Duty cycle 40.325 % System Duty cycle Information System Switching frequency 405.91 kHz System Information System System Information System Switching frequency Information Information System Information System Information System Information System System Information System Information System System Information System Information System System System Information System System System System System Information System Standard		Name Crass From		Category	Description Rede plot greeners from any and a plot greeners from a plot greener from a plot greeners from a pl
Efficiency 93.374 % System Information System System Information System System Information In	52.	Cross Freq	18.099 KHZ		Bode plot crossover frequency
System Information System	53.	Duty Cycle	40.325 %	System	Duty cycle
Information System Total Foot Print Area of BOM components Information Information Information System Information Informatio		• •		Information	• •
55.FootPrint954.0 mm²System Information System InformationTotal Foot Print Area of BOM components56.Frequency405.91 kHzSystem InformationSwitching frequency57.Gain Marg-12.165 dBSystem 	54.	Efficiency	93.374 %	System	Steady state efficiency
Information System Information System Information System Information Information System Information System Information Information System Information System Information Information System Information Informat				Information	
56.Frequency405.91 kHzSystem Information InformationSwitching frequency Information57.Gain Marg-12.165 dBSystem Information58.Iout5.0 ASystem Information59.Low Freq Gain64.667 dBSystem Information60.ModeCCMSystem Information61.Operating TopologyBoostSystem Information62.Phase Marg66.255 degSystem Information63.Pout125.0 WSystem Information64.SW lpk15.956 ASystem Information65.Total BOMNASystem Information66.Vin10.0 VSystem Information67.Vout Actual24.96 VSystem Information68.Vout Tolerance1.956 %System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	55.	FootPrint	954.0 mm ²	System	Total Foot Print Area of BOM components
Information System Information System Information Information System Information Information Information System Information Information Information System Information Information System Information Information Information System Information Information Information System Information Information				Information	
57.Gain Marg-12.165 dBSystem InformationBode Plot Gain Margin58.lout5.0 ASystem Informationlout operating point59.Low Freq Gain64.667 dBSystem InformationGain at 1Hz60.ModeCCMSystem InformationConduction Mode61.Operating TopologyBoostSystem InformationThe current operating topology of the device62.Phase Marg66.255 degSystem InformationBode Plot Phase Margin63.Pout125.0 WSystem InformationTotal output power64.SW lpk15.956 ASystem InformationPeak switch current65.Total BOMNASystem InformationPeak switch current66.Vin10.0 VSystem InformationTotal BOM Cost67.Vout25.0 VSystem InformationOperational Output Voltage68.Vout Actual24.96 VSystem InformationVout Actual calculated based on selected voltage divider resistors69.Vout Tolerance1.956 %System InformationVout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	56.	Frequency	405.91 kHz	System	Switching frequency
Information System Iout operating point Information System Vout Actual calculated based on selected voltage divider resistors if applicable				Information	
58.lout5.0 ASystem Informationlout operating point59.Low Freq Gain64.667 dBSystem InformationGain at 1Hz60.ModeCCMSystem InformationConduction Mode61.Operating TopologyBoostSystem InformationThe current operating topology of the device62.Phase Marg66.255 degSystem InformationBode Plot Phase Margin63.Pout125.0 WSystem InformationTotal output power64.SW lpk15.956 ASystem InformationPeak switch current65.Total BOMNASystem InformationTotal BOM Cost66.Vin10.0 VSystem InformationVin operating point67.Vout25.0 VSystem InformationOperational Output Voltage68.Vout Actual24.96 VSystem InformationVout Actual calculated based on selected voltage divider resistors69.Vout Tolerance1.956 %System InformationVout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	57.	Gain Marg	-12.165 dB	System	Bode Plot Gain Margin
Information System Gain at 1Hz				Information	
59.Low Freq Gain64.667 dBSystem InformationGain at 1Hz60.ModeCCMSystem InformationConduction Mode61.Operating TopologyBoostSystem InformationThe current operating topology of the device62.Phase Marg66.255 degSystem InformationBode Plot Phase Margin63.Pout125.0 WSystem InformationTotal output power64.SW lpk15.956 ASystem InformationPeak switch current65.Total BOMNASystem InformationTotal BOM Cost66.Vin10.0 VSystem InformationVin operating point67.Vout25.0 VSystem InformationOperational Output Voltage68.Vout Actual24.96 VSystem InformationVout Actual calculated based on selected voltage divider resistors Information69.Vout Tolerance1.956 %System System InformationVout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	58.	lout	5.0 A	,	lout operating point
Information System Conduction Mode Information System Conduction Mode Information System Information System Peak switch current System Information System Information System Information System Information System System Information System S					
60. Mode CCM System Information 61. Operating Topology Boost System Information 62. Phase Marg 66.255 deg System Information 63. Pout 125.0 W System Information 64. SW lpk 15.956 A System Information 65. Total BOM NA System Information 66. Vin 10.0 V System Information 67. Vout 25.0 V System Information 68. Vout Actual 24.96 V System Information 69. Vout Tolerance 1.956 % System Vout Tolerance (no load) and voltage divider resistors if applicable	59.	Low Freq Gain	64.667 dB		Gain at 1Hz
Information System The current operating topology of the device					
61. Operating Topology Boost System Information 62. Phase Marg 66.255 deg System Information 63. Pout 125.0 W System Information 64. SW lpk 15.956 A System Information 65. Total BOM NA System Information 66. Vin 10.0 V System Information 67. Vout 25.0 V System Information 68. Vout Actual 24.96 V System Information 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	60.	Mode	CCM	- ,	Conduction Mode
Information System Bode Plot Phase Margin Foundation System Information					
62. Phase Marg 66.255 deg System Information 63. Pout 125.0 W System Information 64. SW lpk 15.956 A System Peak switch current Information 65. Total BOM NA System Total BOM Cost Information 66. Vin 10.0 V System Vin operating point Information 67. Vout 25.0 V System Operational Output Voltage Information 68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors Information 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	61.	Operating Topology	Boost	,	The current operating topology of the device
Information System Total output power Information System Peak switch current Information Information System Peak switch current Information Inform					
63. Pout 125.0 W System Information 64. SW lpk 15.956 A System Peak switch current 65. Total BOM NA System Information 66. Vin 10.0 V System Vin operating point 67. Vout 25.0 V System Information 68. Vout Actual 24.96 V System Vout Tolerance System Information 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	62.	Phase Marg	66.255 deg	,	Bode Plot Phase Margin
Information 64. SW lpk 15.956 A System Information 65. Total BOM NA System Information 66. Vin 10.0 V System Information 67. Vout 25.0 V System Information 68. Vout Actual 24.96 V System Information System Information System Information Vout Actual calculated based on selected voltage divider resistors Information System Vout Tolerance Information Vout Tolerance (no load) and voltage divider resistors if applicable					-
64. SW lpk 15.956 A System Peak switch current 65. Total BOM NA System Information 66. Vin 10.0 V System Vin operating point 67. Vout 25.0 V System Information 68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors Information 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	63.	Pout	125.0 W	•	I otal output power
Information Information System Total BOM Cost Information System Information Total BOM Cost Information System Vin operating point Information System Operational Output Voltage Information System Vout Actual System Vout Actual calculated based on selected voltage divider resistors Information System Vout Tolerance System System	0.4	0144.1	45.050.4		
65. Total BOM NA System Information 66. Vin 10.0 V System Vin operating point Information 67. Vout 25.0 V System Operational Output Voltage Information 68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors Information Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	64.	SW Ipk	15.956 A	,	Peak switch current
Information 66. Vin 10.0 V System Vin operating point Information 67. Vout 25.0 V System Operational Output Voltage Information 68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors Information Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	0.5	Taral DOM	NIA		Total BOM Cont
66. Vin 10.0 V System Vin operating point 67. Vout 25.0 V System Operational Output Voltage 68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	65.	lotal BOM	NA	,	Total BOM Cost
Information 67. Vout 25.0 V System Operational Output Voltage Information 68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors Information 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	66	\/in	10.0 \/		Via aparating point
67. Vout 25.0 V System Operational Output Voltage 68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	00.	VIII	10.0 V	•	vin operating point
Information 68. Vout Actual 24.96 V System Information Vout Actual calculated based on selected voltage divider resistors Information System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	67	Vout	25 O V		Operational Output Voltage
68. Vout Actual 24.96 V System Vout Actual calculated based on selected voltage divider resistors Information 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	07.	vout	23.0 V		Operational Output Voltage
Information 69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	68	Vout Actual	24.06.1/		Vout Actual calculated based on selected voltage divider resistors
69. Vout Tolerance 1.956 % System Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable	00.	vout Actual	∠¬.30 v	- ,	vout Actual calculated based off selected voltage divider resistors
Information resistors if applicable	60	Vout Tolerance	1 956 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider
	υð.	vout Tolerance	1.550 /0	,	
/U. VOUT D-D. 143 79 MV. System Peak-to-deak outbult riddle voltage	70.	Vout p-p	143.29 mV	System	Peak-to-peak output ripple voltage
Information	70.	vout p-p	170.20 1110		i our to pour output rippio voltage

Design Inputs

Name	Value	Description	
lout	5.0	Maximum Output Current	
SoftStart	2.0 ms	Soft Start Time (ms)	
VinMax	15.0	Maximum input voltage	
VinMin	10.0	Minimum input voltage	
Vout	25.0	Output Voltage	
base_pn	LM34936	Base Product Number	
source	DC	Input Source Type	
Та	30.0	Ambient temperature	
UserFsw	407.0 k	Customer Selected Frequency	

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 10.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum lout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

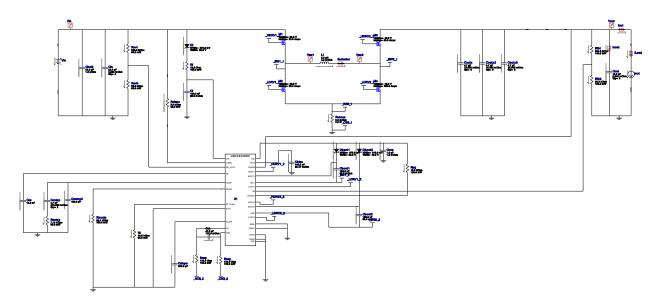


WEBENCH® Electrical Simulation Report

Design Id = 7

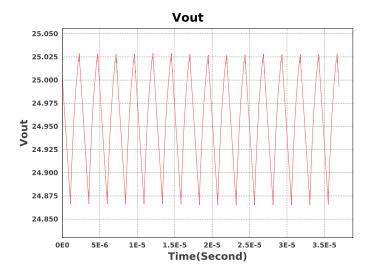
sim_id = 1

Simulation Type = Steady State



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	L1	IC	Initial Current	-11.764705882352942
2.	Cout	IC	no description	25.0
3.	lout	1	Load Current	5.0 A



Design Assistance

- 1. Tip: Snubbers and/or gate resistors may be required to limit the SW1,2 node switching spikes below the IC and FET abs max ratings.
- 2. Tip: Slope Capacitor: smaller slope capacitors provide better transition region behavior.
- 3. Master key: 9E4EC3348F959DE15AFA9944345E8D62[v1]
- 4. LM34936 Product Folder: http://www.ti.com/product/LM34936: contains the data sheet and other resources.

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