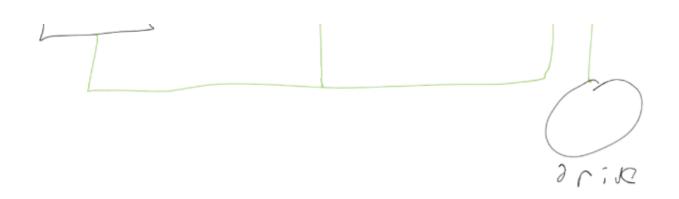


- UCITIONANK, WULLI OCYTCE COUSEUIT - it last command was input, retreive data from device regis-- Start the next operation for that device - no busy wait of ada Tonly I word / time register -Status = busy/free/error - control = command to perform - data in = data going in CPU - data out = data comming out of CPU PMH = write directly to memory - has a DMA controller - Glows Jown, CPU, - interrupts when entire transfer ) isk controller is ready memory count



Last modified: Dec 12, 2018