

# Review session

## Sync properties

- bounded waiting
- failure atomicity
- liveness
- safety

## 2 phase commit

- coordinator requests
- sends to commit / abort

## event ordering in dist

- no shared clock
- clock drift
- CPU generates VA

## Page fault

- OS blocks running process
- ready unmapped pages
- preempt & starts another process
- read completes
- OS maps missing page
- restarts faulting process

Threads have

- stack / SP
- TCB
- PC
- execution
- user of registers

lock = binary sema

Coherence vs Consistency

- single location access
- consistency access across multiple location

Improving paging = cache in TLB

- gets flushed during context switch

Reading from outside is easier  
b/c there are multiple tracks  
& physics

OS 360

- file sys
- manage devices for user

- came w/ compilers

Salt value = random generated #  
you hash w/ your password

Users can't

- access I/O directly
- disable interrupt
- set mode bit

CPU Scheduling

- throughput
- CPU utilization
- turnaround time
- response time
- waiting time

NTFS semantics = idempotence

